

This document contains information on a product under development. The parametric information contains target parameters that are subject to change.

RM806

TDMA/AMPS 3 to 4-Volt Power Amplifier (824—849 MHz)

The RM806 dual mode Time Division Multiple Access (TDMA)/Advanced Mobile Phone Service (AMPS) Power Amplifier is a fully matched 6-pin LCC surface mount module designed for mobile units operating in the 824-849 MHz cellular band width. This device meets stringent IS-136 linearity requirements beyond 30 dBm output power and can be driven to power output levels beyond 31 dBm for high efficiency FM mode operation. A single GaAs Microwave Monolithic Integrated Circuit (MMIC) contains all active circuitry in the module. The MMIC contains on-board bias circuitry, as well as input and inter-stage matching circuits. The output match is realized off-chip within the module package to optimize efficiency and power performance into a $50\ \Omega$ load. This device is manufactured with Conexant's GaAs HBT process that provides for all positive voltage DC supply operation while maintaining high efficiency and good linearity. Primary bias to the RM806 can be supplied directly from a three cell nickel-cadmium, single cell lithium-ion, or other suitable battery with output in the 3-4 volt range. Power down is accomplished by setting the voltage on the low current reference pin to zero volts. No external supply side switch is needed as typical "off" leakage is a few microamperes with full primary voltage supplied from the battery.

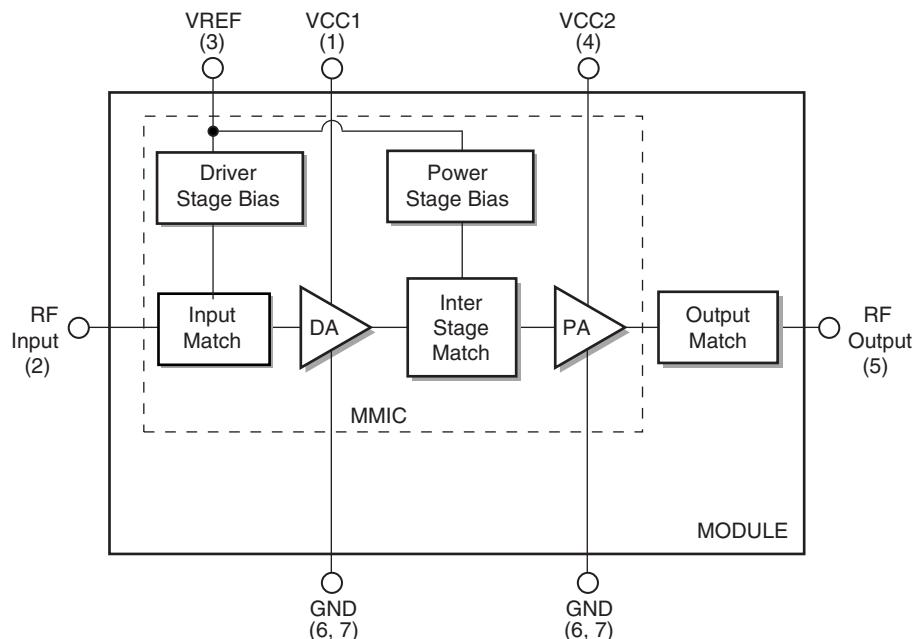
Distinguishing Features

- Low voltage positive bias supply
- Good linearity
- High efficiency
- Dual mode operation
- Large dynamic range
- 6-pin LCC package (6 x 6 x 1.5 mm)
- Power down control

Applications

- Digital cellular (TDMA)
- Analog cellular (AMPS)
- Wireless local loop

Functional Block Diagram



Electrical Specifications

The following tables list the electrical characteristics for the RM806 Power Amplifier. [Table 1](#), lists the absolute maximum rating for continuous operation. [Table 2](#), lists the recommended operating conditions for achieving the electrical performance listed in [Table 3](#). [Table 3](#), lists the electrical performance of the RM806 Power Amplifier over the recommended operating conditions.

Table 1. Absolute Maximum Ratings⁽¹⁾

Parameter	Symbol	Min	Nominal	Max	Unit
Rf Input Power	P _{in}	—	3	8	dBm
Supply Voltage	V _{cc}	—	3.4	5	Volts
Reference Voltage	V _{ref}	—	3.1	3.3	Volts
Case Operating Temperature	T _c	-30	25	+110	°C
Storage Temperature	T _{stg}	-55	—	+125	°C
NOTE(S):					
(1) No damage assuming only one parameter is set at limit at a time with all other parameters set at or below nominal value.					

Table 2. Recommended Operating Conditions

Parameter	Symbol	Min	Nominal	Max	Unit
Supply Voltage	V _{cc}	3.0	3.4	4.2	Volts
Reference Voltage	V _{ref}	—	3.1	—	Volts
Operating Frequency	F ₀	824	836.5	849	MHz
Operating Temperature	T ₀	-30	+25	+85	°C

Table 3. Electrical Specifications

Characteristics	Condition	Symbol	Min	Typical	Max	Unit
Quiescent current	—	—	—	150	185	mA
Reference current	$P_o \leq 32 \text{ dBm}$	I_{ref}	—	6	9	mA
Leakage current	PA Off	—	—	2	25	μA
Gain	$P_o = 0 \text{ dBm}$	G	27	31	—	dB
Gain	$P_o = 30 \text{ dBm}$	G_p	26	28	—	dB
Analog Mode Power Added Efficiency	$P_o = 31 \text{ dBm}$	PAEa	41	43	—	%
Digital Mode Power Added Efficiency	$P_o = 30 \text{ dBm}$	PAEd	37	40	—	%
Adjacent Channel Power ⁽⁴⁾	$P_o \leq 30 \text{ dBm}$	—	—	—	—	—
—30 kHz Offset	$P_o \leq 30 \text{ dBm}$	ACP1	—27	—31	—	dBc
—60 kHz Offset	$P_o \leq 30 \text{ dBm}$	ACP2	—46	—51	—	dBc
—90 kHz Offset	$P_o \leq 30 \text{ dBm}$	ACP3	—50	—58	—	dBc
Harmonics			—	—	—	—
Second	$P_o \leq 31 \text{ dBm}$	H2	—	—40	—30	dBc
Third	$P_o \leq 31 \text{ dBm}$	H3	—	—40	—30	dBc
PA “Turn Off Time”	—	—	—	10	30	μs
PA “Turn On Time”	—	—	—	10	30	μs
Noise Power in RX Band 869-894 MHz ⁽⁵⁾	$P_o \leq -31 \text{ dBm}$	No	—	—132	—	dBm/Hz
Noise Figure	—	NF	—	5	7	dB
Input VSWR	—	—	—	2:1	—	—
Stability (Spurious output) ⁽³⁾	5:1 VSWR All phases	S	—	—60	—	dBc
Ruggedness – No damage $P_o \leq 31 \text{ dBm}$	No damage	Ru	8:1	—	—	VSWR

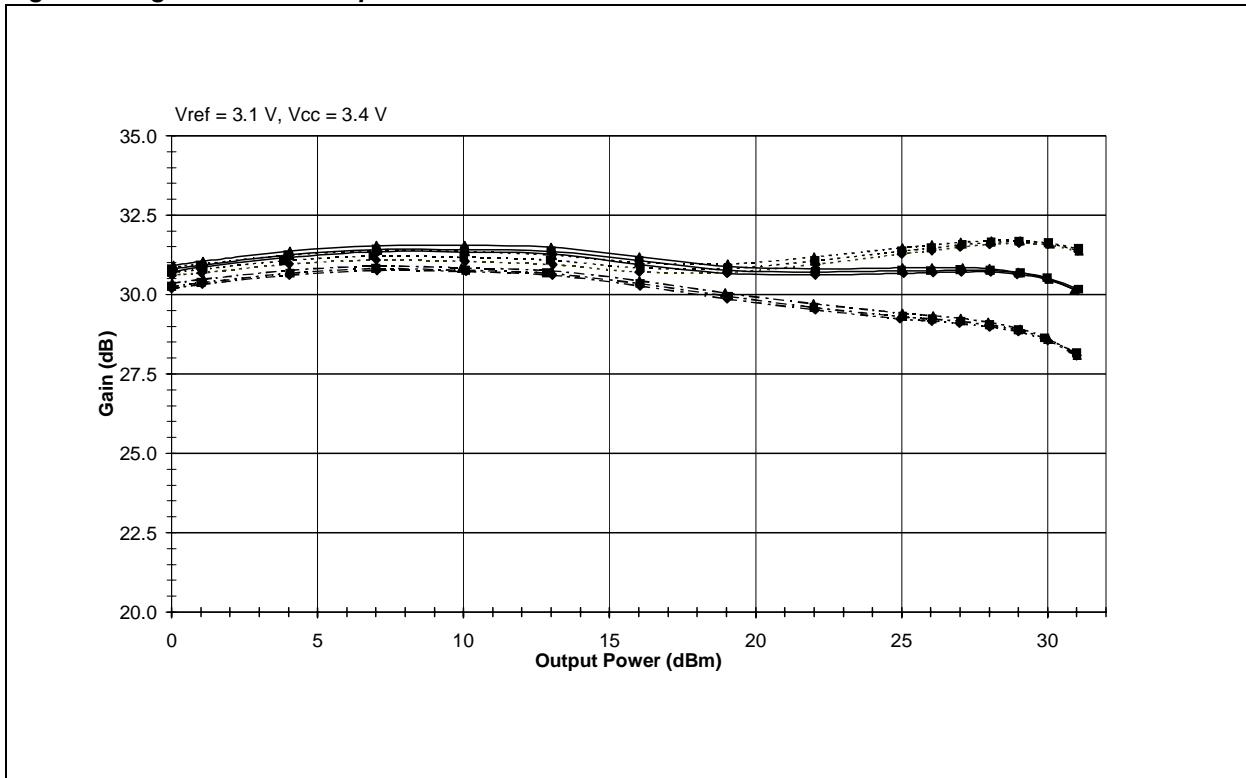
NOTE(S):

1. $V_{cc} = +3.4 \text{ V}$, $V_{ref} = +3.1 \text{ V}$, $\text{FREQ}' = 824\text{--}849 \text{ MHz}$, $T_c = -30 \text{ }^\circ\text{C}$ to $+85 \text{ }^\circ\text{C}$.
2. Min/Max values in table indicate performance over process corners and conditions specified in Note 1 unless otherwise detailed.
- (3) $T_c = 25 \text{ }^\circ\text{C}$.
- (4) Also meets same linearity for $P_o \leq 28.5 \text{ dBm}$ @ $V_{cc} = +3.0 \text{ V}$ and as further specified in Note 1.
- (5) With NADC modulation applied. $T_c = 25 \text{ }^\circ\text{C}$

Characterization Data

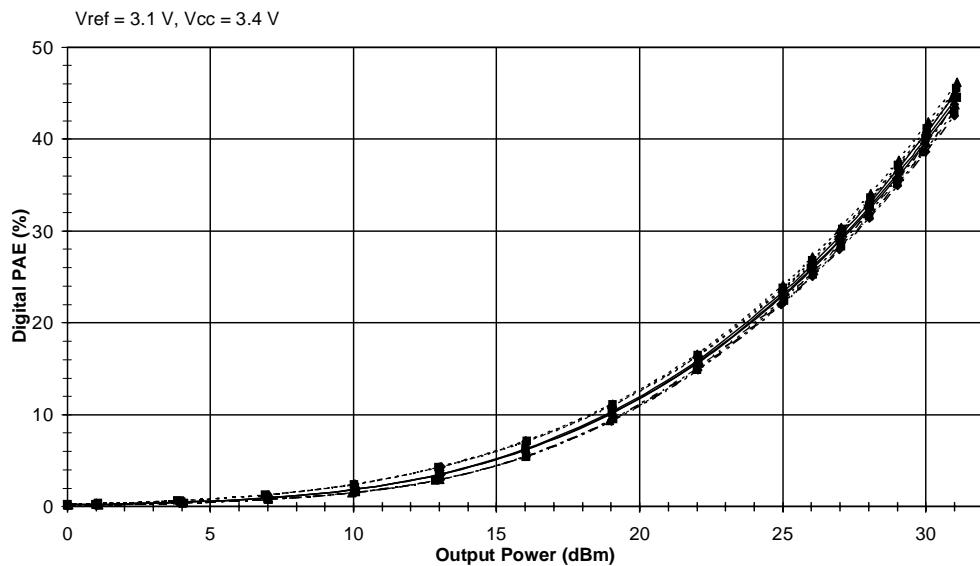
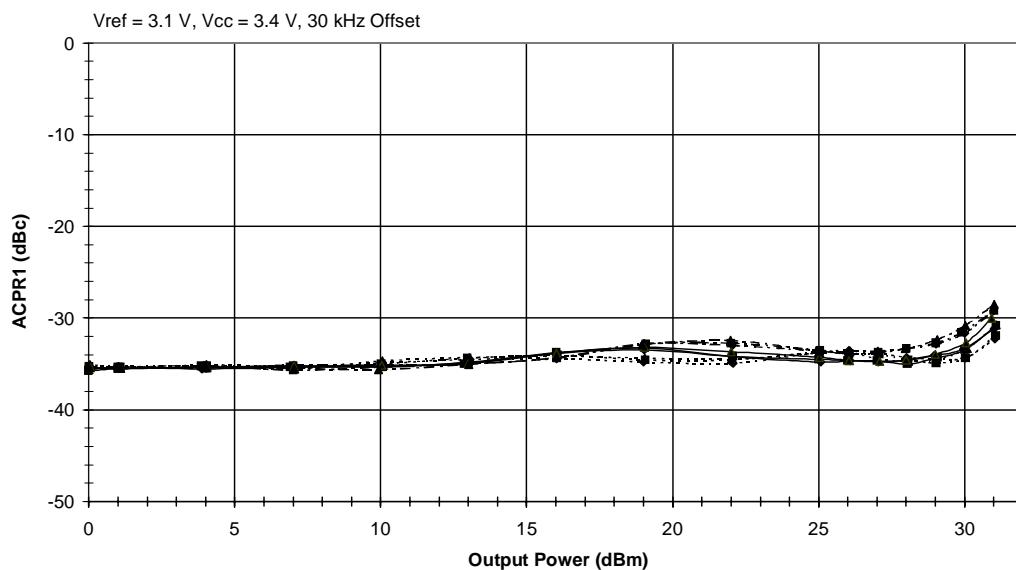
The following graphs illustrate characteristics for a typical RM806 Power Amplifier. The amplifier was selected by characterizing a group of devices and selecting a part having average electrical performance both at nominal and worst case. [Figures 1 through 5](#) illustrate the digital signal characteristics and [Figures 6 through 9](#) illustrate the analog characteristics for the RM806.

Figure 1. Digital Gain vs. Output Power

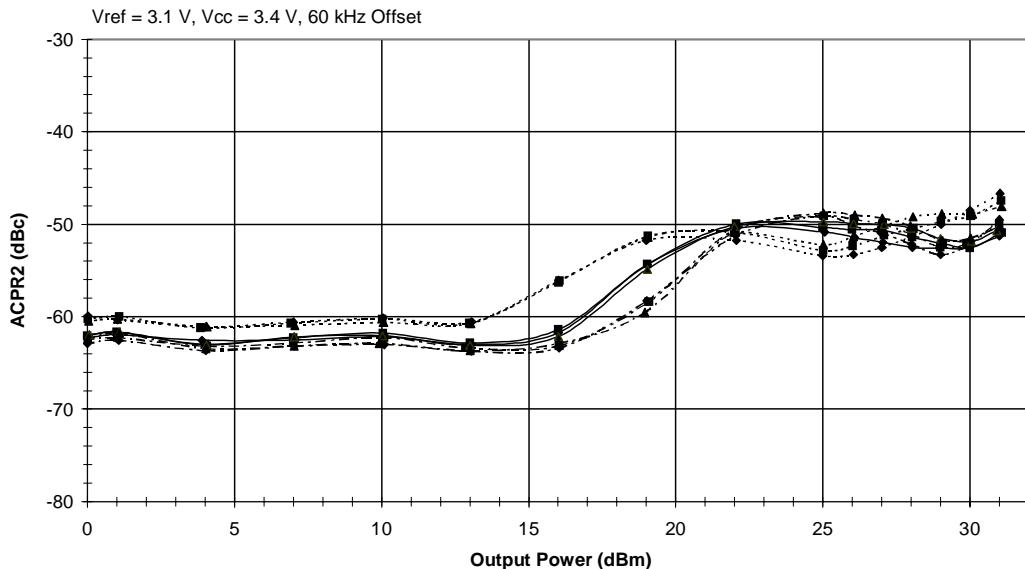
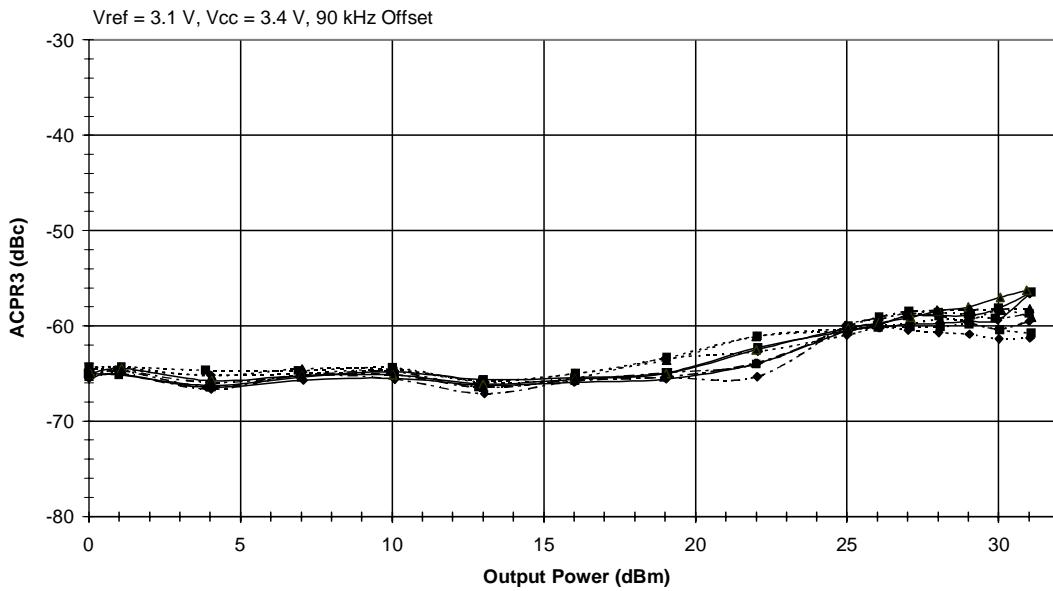


Legend

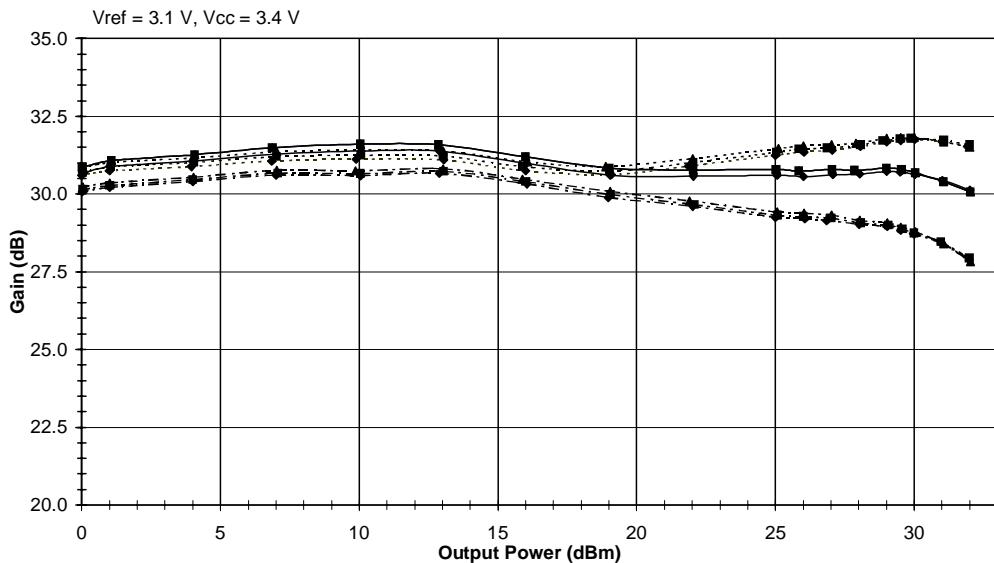
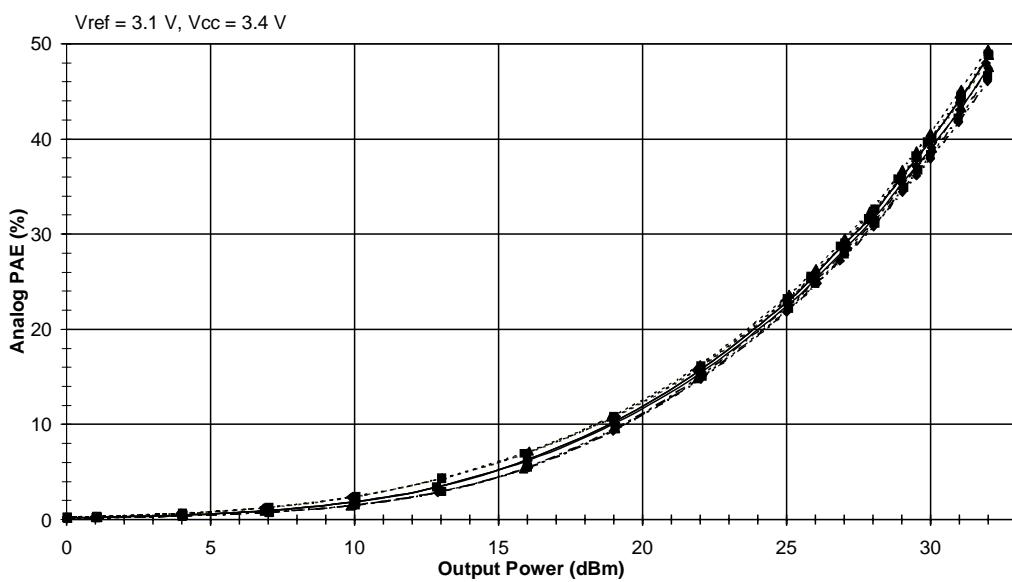
---◆---	824 MHz @ -30 °C	◆	824 MHz @ +25 °C	◆	824 MHz @ +85 °C
---■---	837 MHz @ -30 °C	■	837 MHz @ +25 °C	■	837 MHz @ +85 °C
---▲---	849 MHz @ -30 °C	▲	849 MHz @ +25 °C	▲	849 MHz @ +85 °C

Figure 2. Digital Power Added Efficiency**Figure 3. Digital Adjacent Channel Power vs. Output Power****Legend**

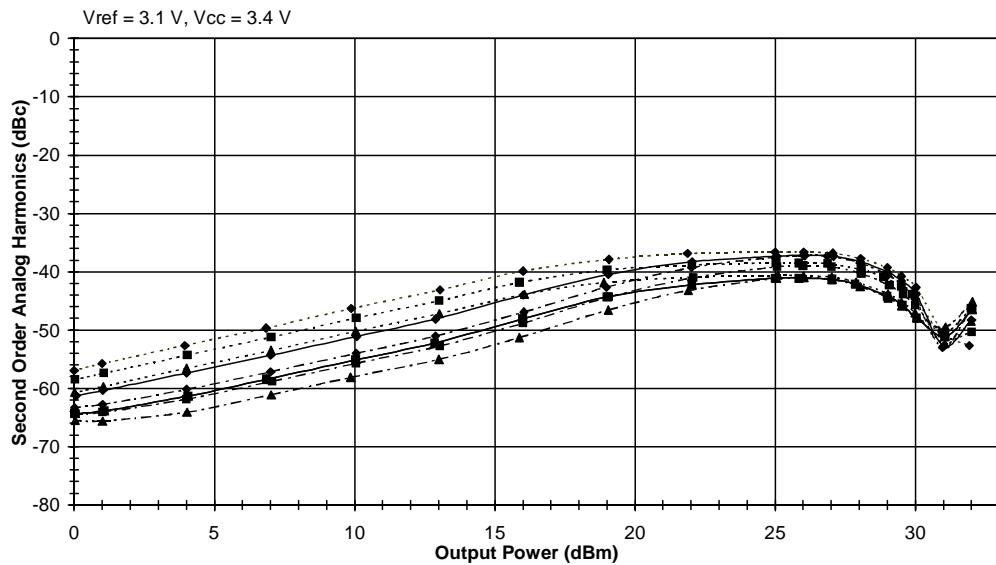
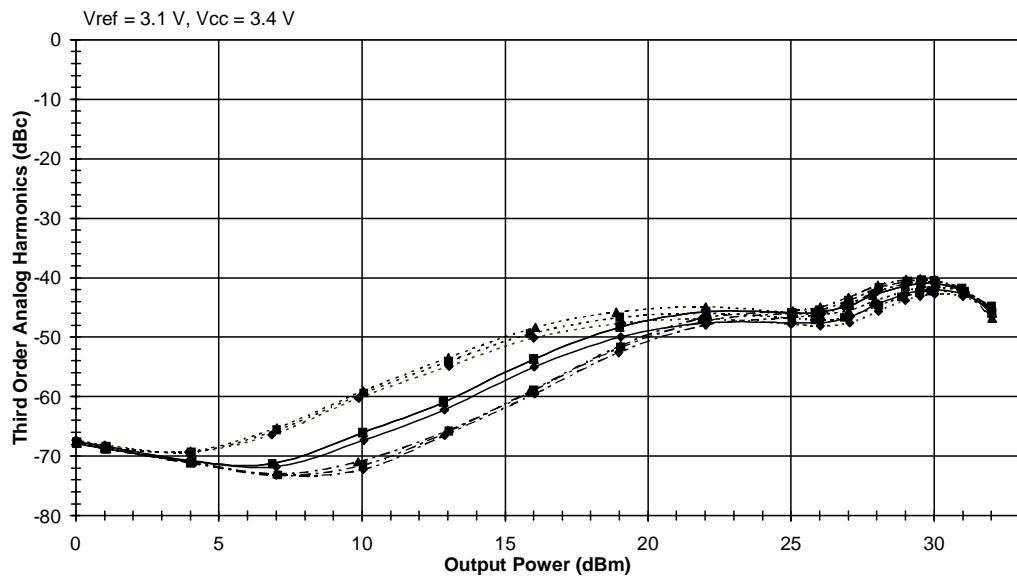
---◆---	824 MHz @ -30 °C	◆	824 MHz @ +25 °C	---◆---	824 MHz @ +85 °C
---■---	837 MHz @ -30 °C	■	837 MHz @ +25 °C	---■---	837 MHz @ +85 °C
---▲---	849 MHz @ -30 °C	▲	849 MHz @ +25 °C	---▲---	849 MHz @ +85 °C

Figure 4. Digital Alternate1 Channel Power vs. Output Power**Figure 5. Digital Alternate2 Channel Power vs. Output Power****Legend**

---◆---	824 MHz @ -30 °C	---◆---	824 MHz @ +25 °C	---◆---	824 MHz @ +85 °C
---■---	837 MHz @ -30 °C	---■---	837 MHz @ +25 °C	---■---	837 MHz @ +85 °C
---▲---	849 MHz @ -30 °C	---▲---	849 MHz @ +25 °C	---▲---	849 MHz @ +85 °C

Figure 6. Analog Gain vs. Output Power**Figure 7. Analog Power Added Efficiency vs. Output Power****Legend**

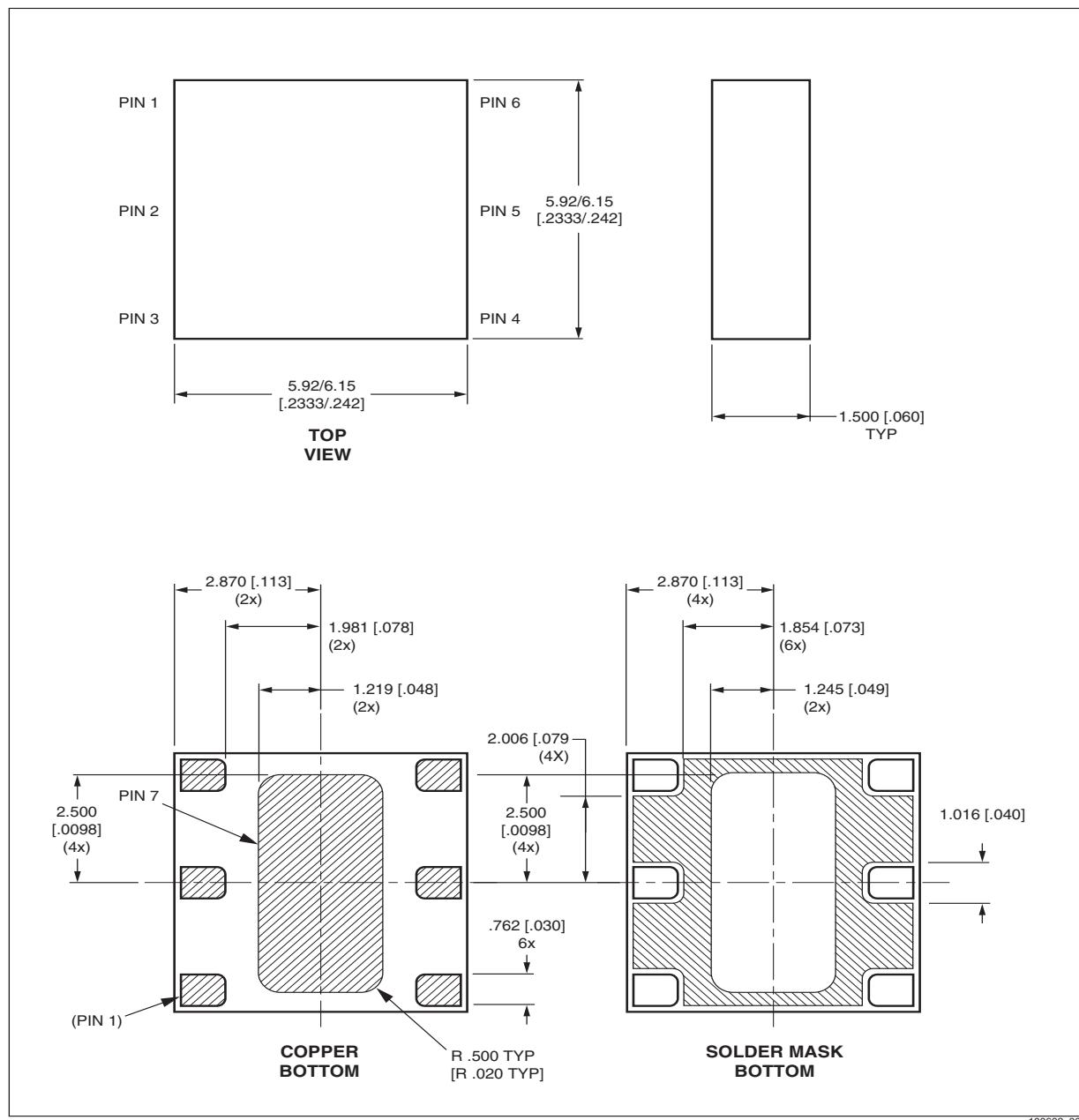
-----◆-----	824 MHz @ -30 °C	-----◆-----	824 MHz @ +25 °C	-----◆-----	824 MHz @ +85 °C
-----■-----	837 MHz @ -30 °C	-----■-----	837 MHz @ +25 °C	-----■-----	837 MHz @ +85 °C
-----▲-----	849 MHz @ -30 °C	-----▲-----	849 MHz @ +25 °C	-----▲-----	849 MHz @ +85 °C

Figure 8. Analog Second Order Harmonic**Figure 9. Analog Third Order Harmonic****Legend**

-----◆-----	824 MHz @ -30 °C	-----◆-----	824 MHz @ +25 °C	-----◆-----	824 MHz @ +85 °C
-----■-----	837 MHz @ -30 °C	-----■-----	837 MHz @ +25 °C	-----■-----	837 MHz @ +85 °C
-----▲-----	849 MHz @ -30 °C	-----▲-----	849 MHz @ +25 °C	-----▲-----	849 MHz @ +85 °C

Package Dimensions and Pin Description

Figure 10. RM806 Package Drawing



*TDMA/AMPS 3 to 4-Volt Power Amplifier (824—849 MHz)***Table 4. Pin Description**

Pin #	Function
1	VCC1 ⁽¹⁾
2	RF Input
3	VREF
4	VCC2 ⁽¹⁾
5	RF Output
6	GND
7	GND ⁽²⁾

NOTE(S):

(1) All supply pins may be connected together at the supply.
(2) Package underside is GND.

Preliminary 5/15/00 1:30 PM

Ordering Information

Model Number	Manufacturing Part Number	Product Revision	Package	Operating Temperature

Revision History

Revision	Level	Date	Description

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