

UNCC
VERIFY &
REVISIO

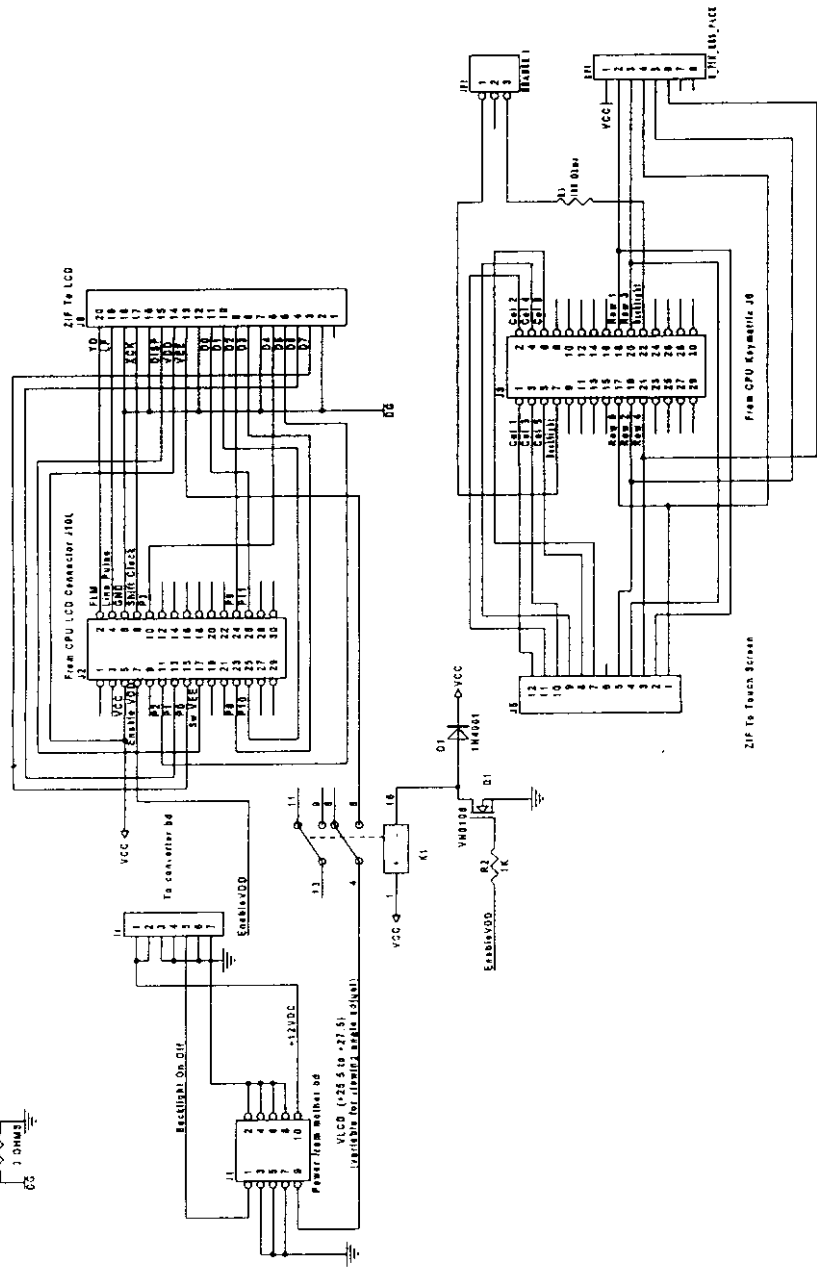
Size: B	Rev: A1	Number: 220 1572 00
File: ba(4)	Rev: 003	Sheet 2 of 2

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REV	ECO	DESCRIPTION	DATE	BY	CHKD	APPD
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DATE	TIME	DESCRIPTION
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11/11/78	11:11	Initial Fabrication

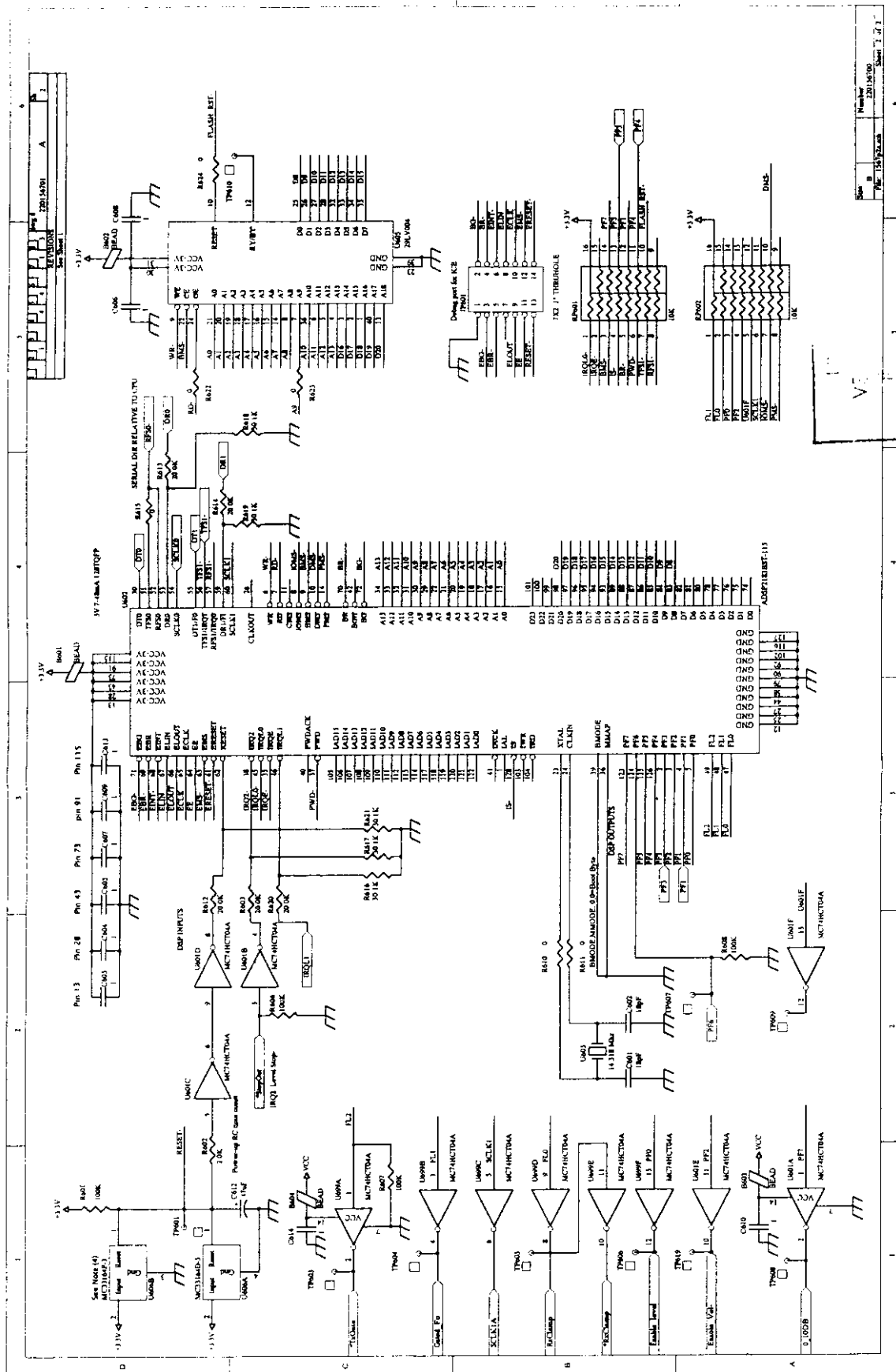
MMI

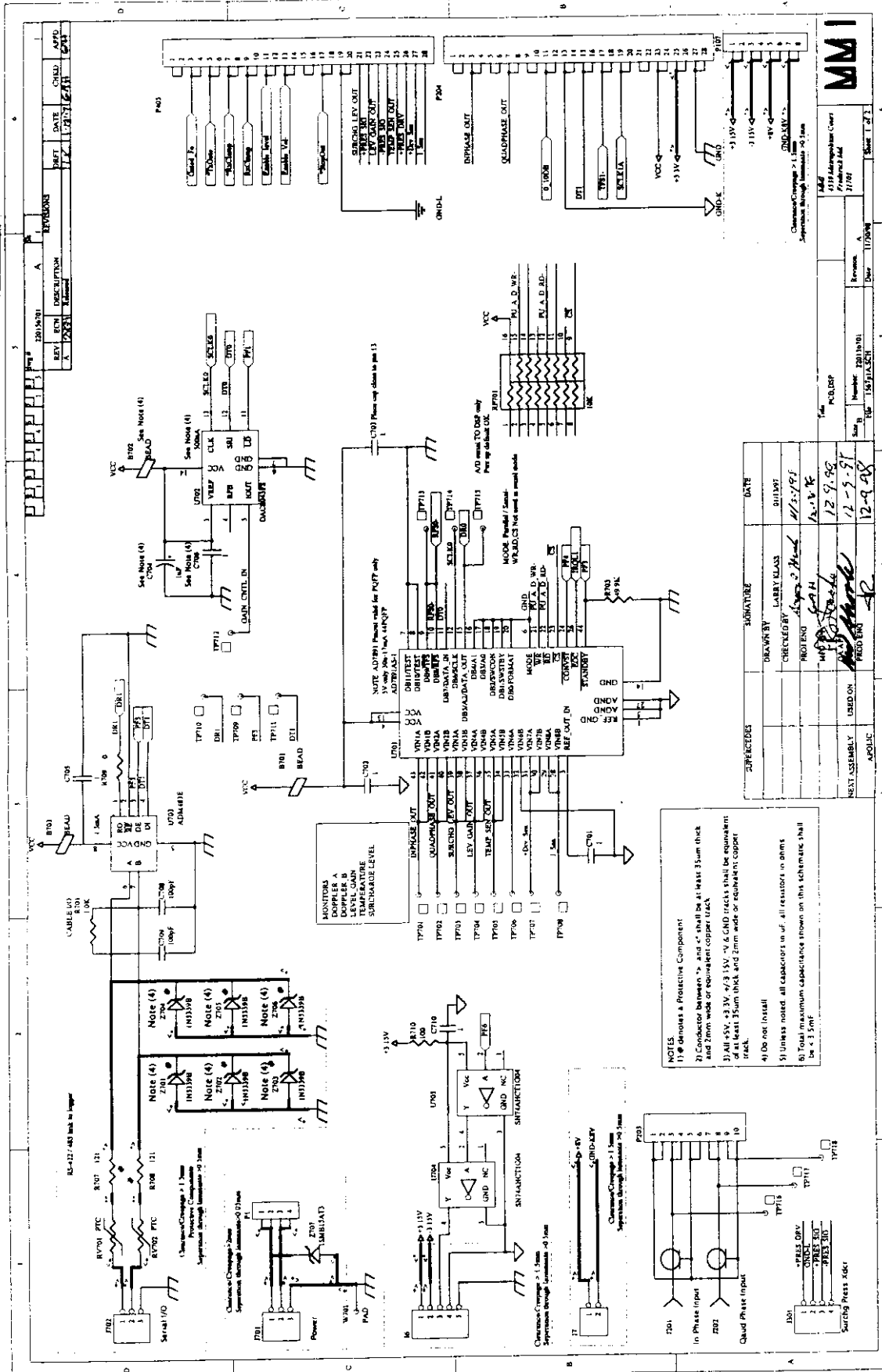
ZIP Microprocessor Card
11/11/78

ZIP To LCD
ZIP To Touch Screen
ZIP To Keyboard

ZIP To Keyboard

ZIP To Keyboard





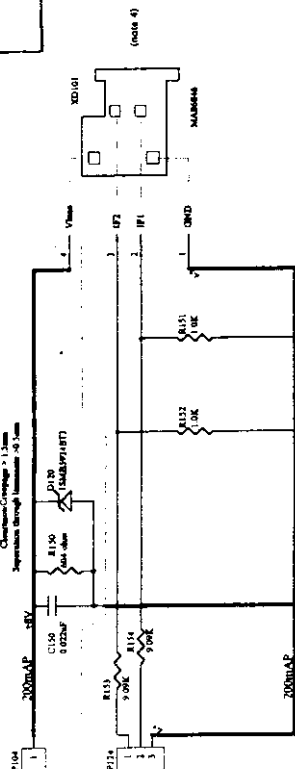
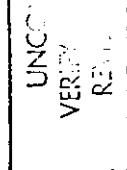
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2078	2077	2077	12-5-99
2079	2078	2078	12-5-99
2080	2079	2079	12-5-

NOTES:

- 1) ϕ denotes a Protective Component
- 2) Conductor between "s" and "c" shall be at least 35um thick and 2mm wide or equivalent copper track
- 3) All +5V, +4.7V, +4.3V, -0.6V GND tracks shall be equivalent of at least 35um thick and 2mm wide or equivalent copper track.
- 4) Do not install
- 5) Unless noted, all capacitors to be of all resistors in ohms
- 6) Total maximum capacitance shown on this schematic shall be 4.3 5mf

[illegible]

REV	ECN	DESCRIPTION	DEPT	DATE	CHLD	APPD
A	2504	Released	CH	9-13-78	R	GM



NOTES

1) * denotes a Protective Component

2) Conductor between "s" and "e" shall be at least 35um thick and 2mm wide or equivalent copper track

3) All 4V & GND tracks shall be equivalent of at least 35um thick and 2mm wide or equivalent copper track

4) Do not install

5) Unless noted all capacitors in µF, all resistors in ohms

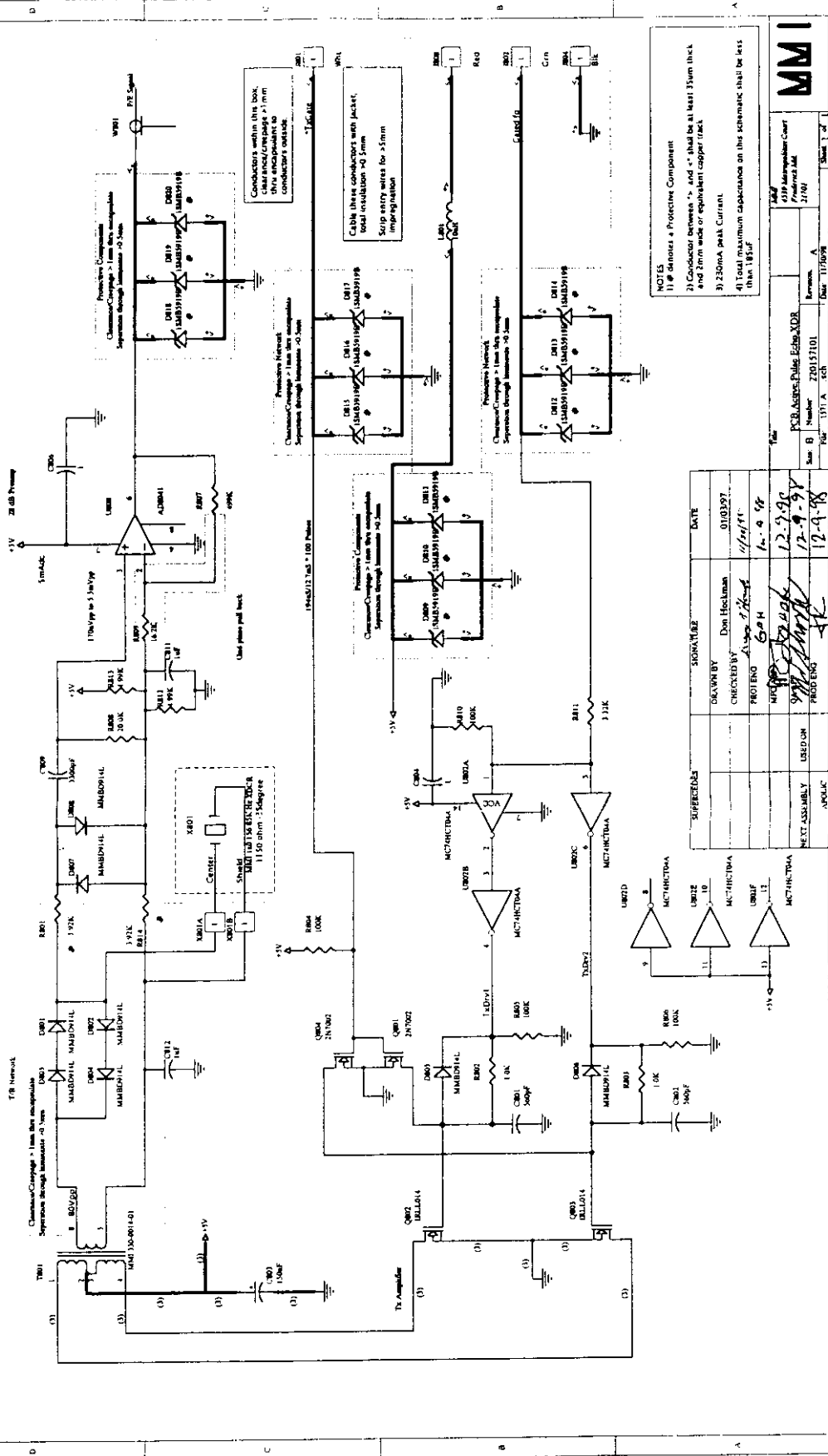
6) Total maximum capacitance is specified by Note 6) on 220

1568 Q2

SERVICES	SIGNATURE	DATE
DRAWN BY	Chris Walsh	03/1/97
CHECKED BY	JB	12-9-98
PROJECT	644	4-6-78
UNIT NO.	100-3-04	12-9-98
USED ON	100-3-04	12-9-98
APPROVED	ATC	12-9-98

Title	PCN, Xerox Quad		JAG	619 Metropolitan Court Fostered Ad. 21701	
Sz B	Number	280146501	Bureau	A	
Sz	34 A	nch	Date	11/20/94	

REV	ZCH	DESCRIPTION	DEFT	DATE	CHLD	APPD
A	2221	Released		04-03-74	1211	0001

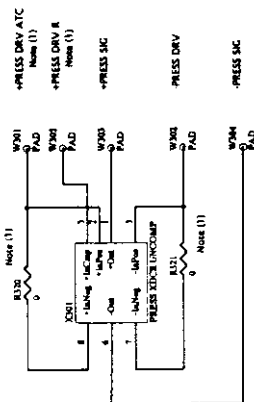


SUPPLIER'S	SHOW NAME	DATE
DECEMBER BY	DON HOCHMAN	010197
CHECKED BY	A. J. H. S.	11-1-97
PROD NO	GEM	11-0-97
USED UN	12-2-98	12-2-98
EXT ASSNABLE	12-4-98	12-4-98
PROD#	AT	AT

NOTES
1) @ denotes a Protective Component
2) Conductor between "+" and "-" shall be at least 35um thick and 2mm wide or equivalent copper track
3) 230mA peak current
4) Total maximum capacitance on this schematic shall be less than 100pF

REV	NO	DESCRIPTION	DATE	CHKD	AMTD
✓		035876	11/13	298	112

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 VERSION 1.0
 REVISION 1.0



Notes:

- a) For ATC use with MMI 321001002 and MMI 321001001: Install R320 and R321, connect +Press Drv wire to W301, N.C. on W305.

SERIAL NO.	DATE	SIGNATURE	DATE
	060297	Don Heckman	
	14/11/8	Don Heckman	
	14/11/8	PROG ENG	
	12-11-78	PROG ENG	
	02-11-78	PROG ENG	
	12-11-78	PROG ENG	

Title		PCB, Surcharges, Sensor		JAN 1997		4130 Montgomery Court Fremont, Md. 21762		MM	
Accession	Box	Number	Volume	Date	Sheet				
File	1570	A	220157001	A	170096				
		sch							

Title		PCB, Surcharges, Sensor		JAN 1997		4130 Montgomery Court Fremont, Md. 21762		MM	
Accession	Box	Number	Volume	Date	Sheet				
File	1570	A	220157001	A	170096				
		sch							

REV	BY	DATE	CHKD	APPD
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REV	BY	DATE	CHKD	APPD
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REV	BY	DATE	CHKD	APPD
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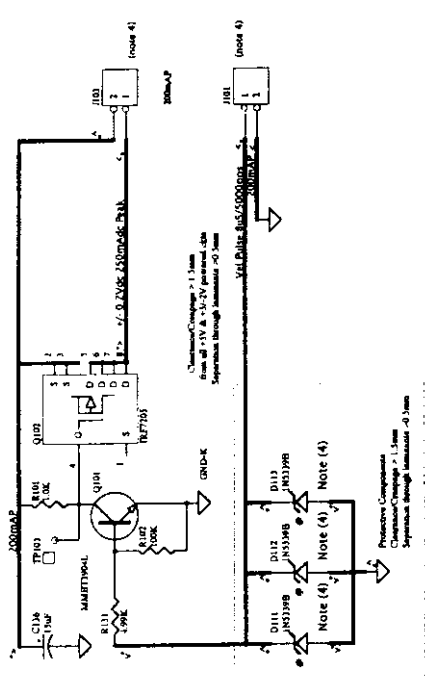
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REV	BY	DATE	CHKD	APPD
1	1	12/17/78	2	2

REV	BY	DATE	CHKD	APPD
1	1	12/17/78	2	2



UNION
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REVISION

- NOTES
- 1) Denotes a Protective Component
 - 2) Conductor between "+" and "-" shall be at least 35um thick and 2mm wide or equivalent copper track.
 - 3) All +V & GND tracks shall be equivalent of at least 35um thick and 2mm wide or equivalent copper track.
 - 4) Do not install
 - 5) Unless noted all capacitors in uf, all resistors in ohms
 - 6) Total maximum capacitance is specified via Note (6) on 220 1588 02

SUPPLIES	DATE
REVIEW	01/02/79
CHECKED BY	Don Hinkley
PROD END	12-10-78
USED ON	12-10-78
PROD END	12-10-78
PROD END	12-10-78

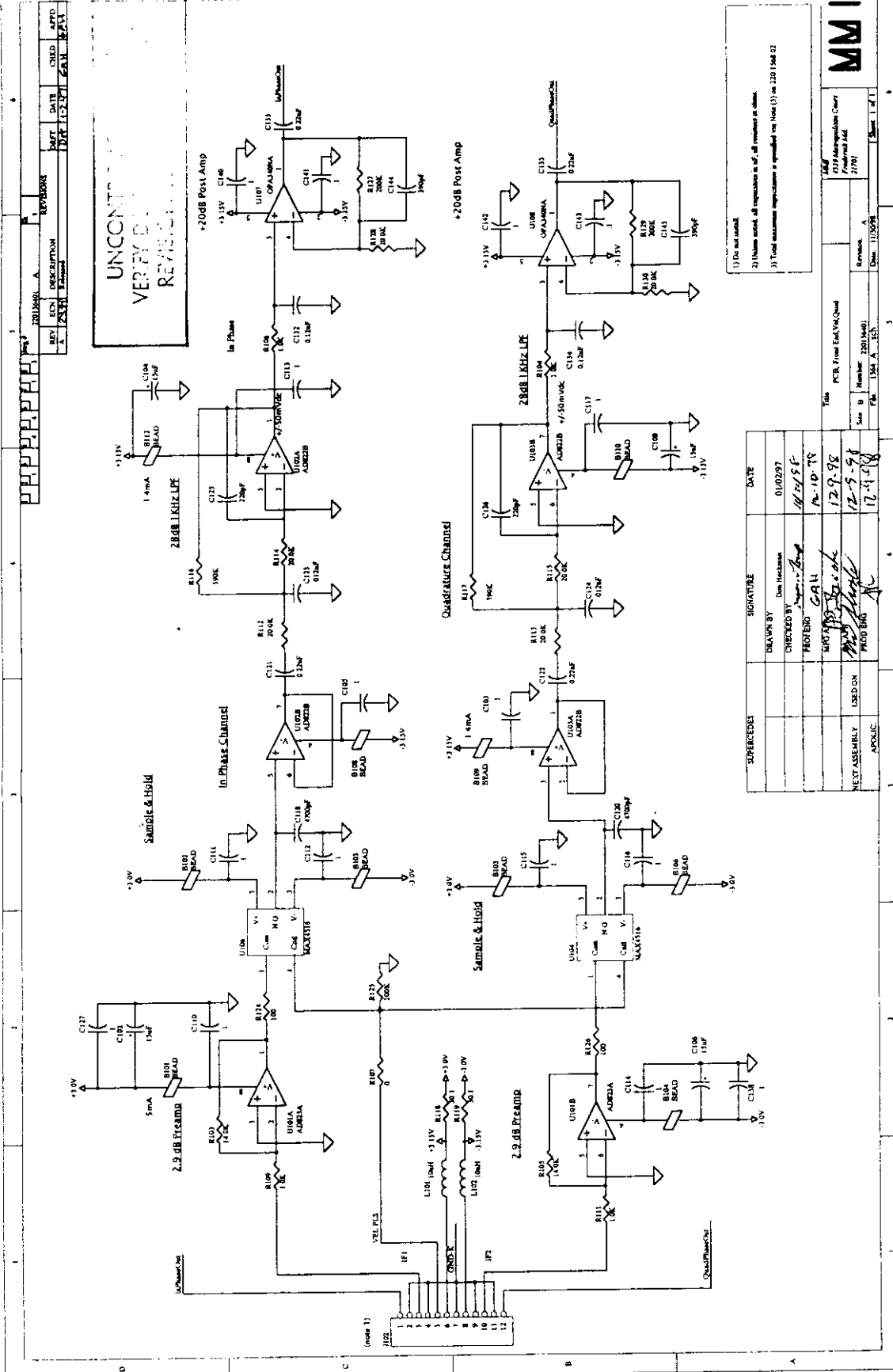
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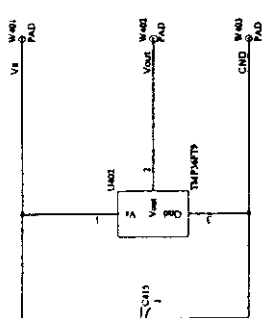


1) Do not exceed
 2) Values noted, all impedances in Ω , all resistors in Ω , all capacitors in pF
 3) Total maximum impedance is specified in Note (3) on 220 1508 02

SUPPLIES	SIGNATURE	DATE
DRAGON BY	Don Henderson	01/02/97
CHECKED BY	PROFESSOR	11/10/97
PROFESSOR	CALL	11/10/97
TEST ASSEMBLY	USED ON	12/5/97
APPLIC		12/5/97

UNCONTROLLED VIDEO RECEIVER
 PCB Part No. MM1
 Revision A
 Date 11/02/97
 PCB From End User
 Date 12/5/97
 PCB Number 220 1508 02
 PCB Part No. MM1

MM1
 PCB Part No. MM1
 Revision A
 Date 11/02/97
 PCB From End User
 Date 12/5/97
 PCB Number 220 1508 02
 PCB Part No. MM1

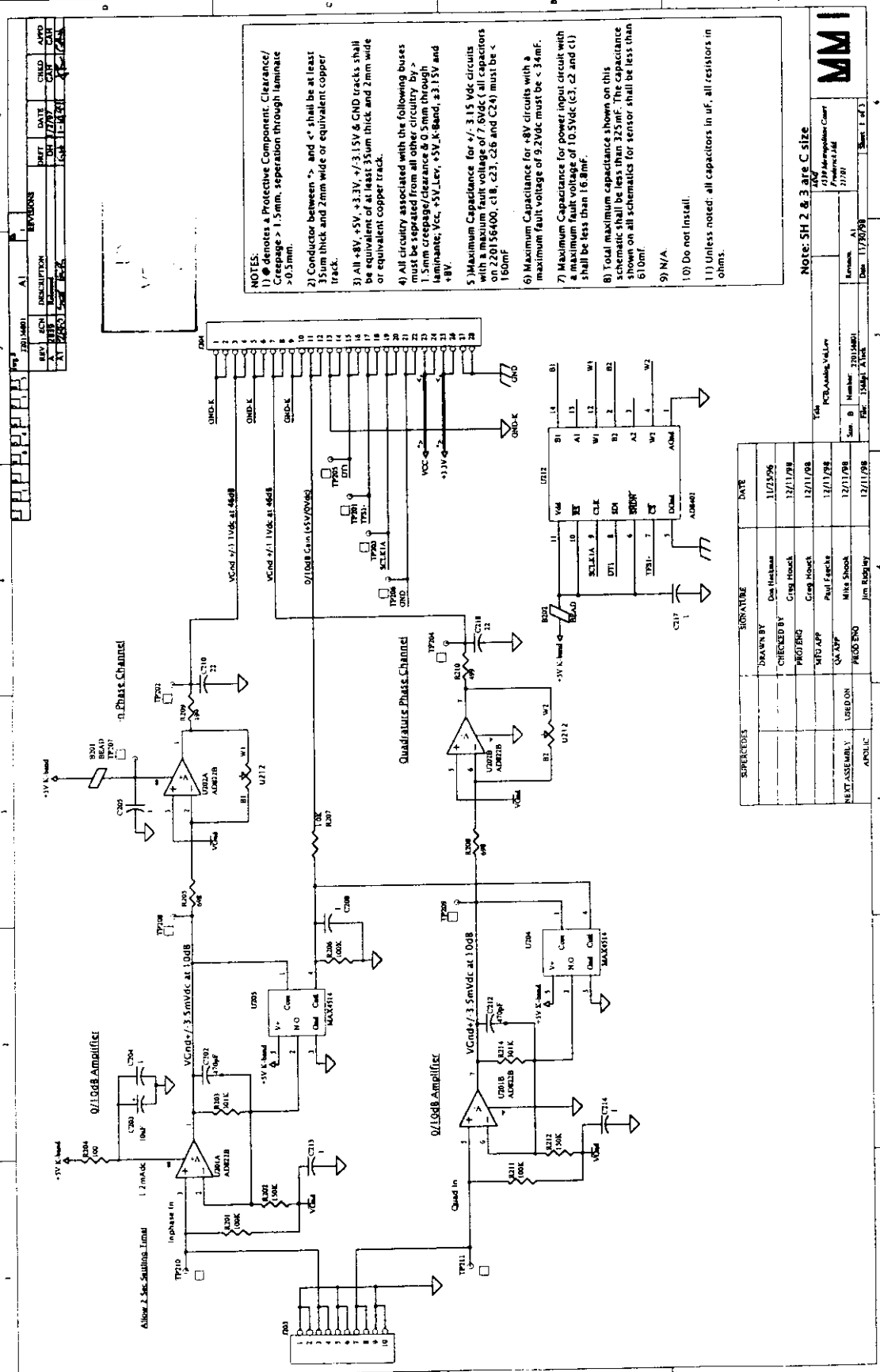
UNCONT. VERIFY & REVISION			
V_{cc} PAD V_{out} PAD GND PAD			

REV: 1
 QTY: 1
 DATE: 11/11/98
 BY: [Signature]
 CHKD: [Signature]
 APPD: [Signature]

Title: PCB Temp. Sensor
 Part Number: 2201-56901
 Rev: 1500 A
 Date: 11/20/98

MFG: [Signature]
 DATE: 12-9-98
 TIME: 12:55

SUPPLIES	SIGNATURE	DATE
DRAWN BY	Don Heckman	05/05/98
CHECKED BY	[Signature]	11/11/98
PROD ENG	[Signature]	12/11/98
MFG	[Signature]	12-9-98
USED ON	[Signature]	12-9-98
NEXT ASSEMBLY	[Signature]	12-9-98
APPLIC.	[Signature]	12-9-98



- NOTES:**
- 1) Φ denotes a Protective Component. Clearance/Creepage > 1.5mm, separation through laminate > 0.3mm.
 - 2) Conductor between ">" and "<" shall be at least 35um thick and 2mm wide or equivalent copper track.
 - 3) All <8V, <5V, <3.3V, <1.5V & GND tracks shall be equivalent of at least 35um thick and 2mm wide or equivalent copper track.
 - 4) All circuitry associated with the following buses must be separated from all other circuitry by > 1.5mm creepage/clearance & 0.5mm through laminate; Vcc, +5V, Lev, +5V, K-band, <1.5V and <8V.
 - 5) Maximum Capacitance for <1.5V Vdc circuits with a maximum fault voltage of 7.8Vdc (all capacitors on U2201, U2400, U218, U23, U26 and U24) must be < 100mf.
 - 6) Maximum Capacitance for <8V circuits with a maximum fault voltage of 9.2Vdc must be < 34mf.
 - 7) Maximum Capacitance for power input circuit with a maximum fault voltage of 10.5Vdc (U23, U2 and U21) shall be less than 16.8mf.
 - 8) Total maximum capacitance shown on this schematic shall be less than 325mf. The capacitance shown on all schematics for sensor shall be less than 610mf.
 - 9) N/A
 - 10) Do not install.
 - 11) Unless noted: all capacitors in uf, all resistors in ohms.

Note: SH 2 & 3 are C size



SUPERSEDES	DATE
DRAWN BY: Dan Heine	11/15/96
CHECKED BY: C. H. Hock	12/17/98
PACKED BY: C. H. Hock	12/17/98
WFO APP: Paul Fegle	12/17/98
QA APP: Mike Shook	12/17/98
USED ON: APOC	12/17/98
APPLIC: Jim Rupp	

PCB Assembly Value	Sum B	Number	PCB Assembly Value
12/17/98	20114001	12/17/98	12/17/98
12/17/98	12/17/98	12/17/98	12/17/98