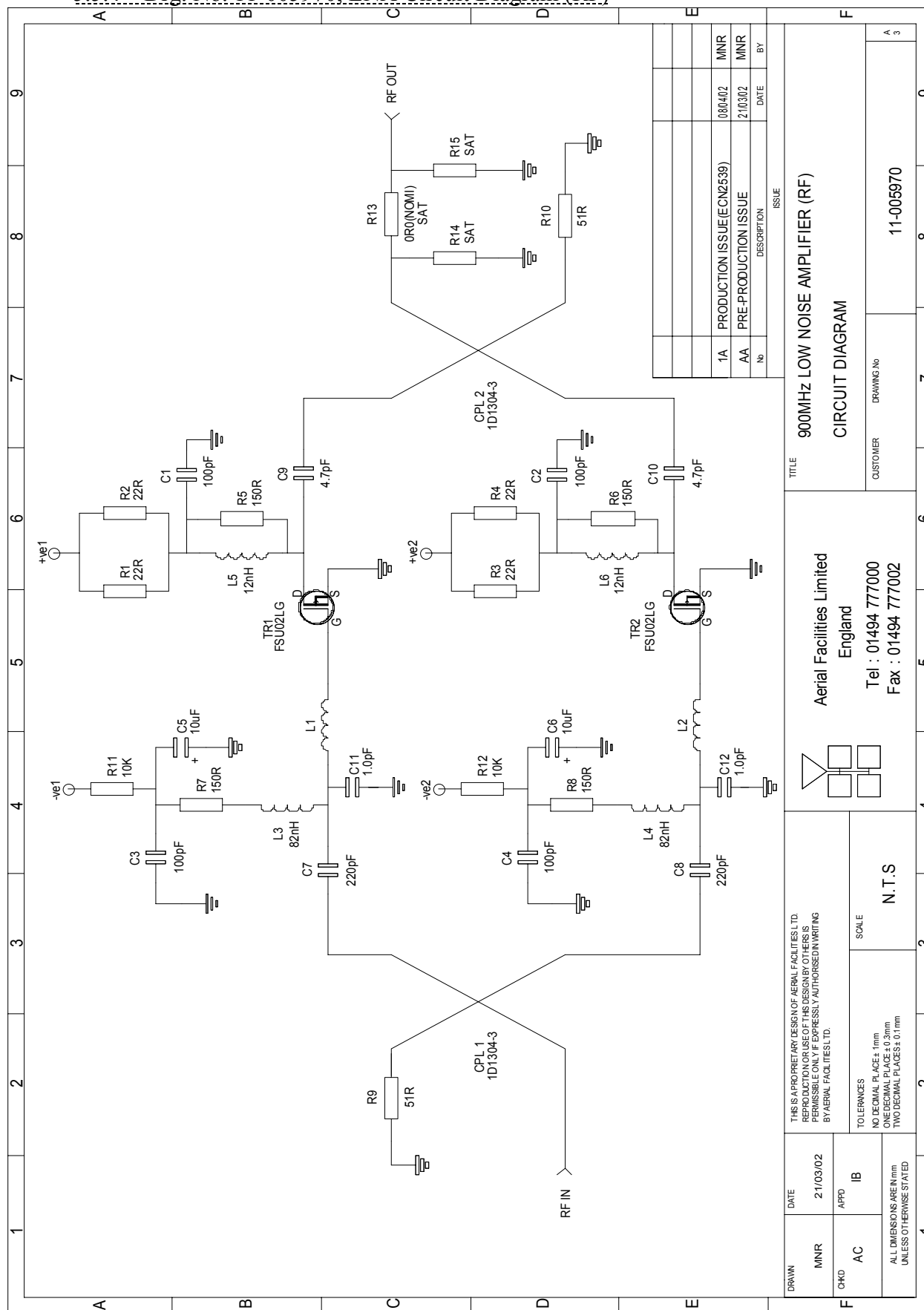


5.5.4 Drg. Nō. 11-005970, LNA Circuit Diagram (RF)



Aerial Facilities Limited

www.AerialFacilities.com

Technical Literature

H/book Number:-60-056100HBKM

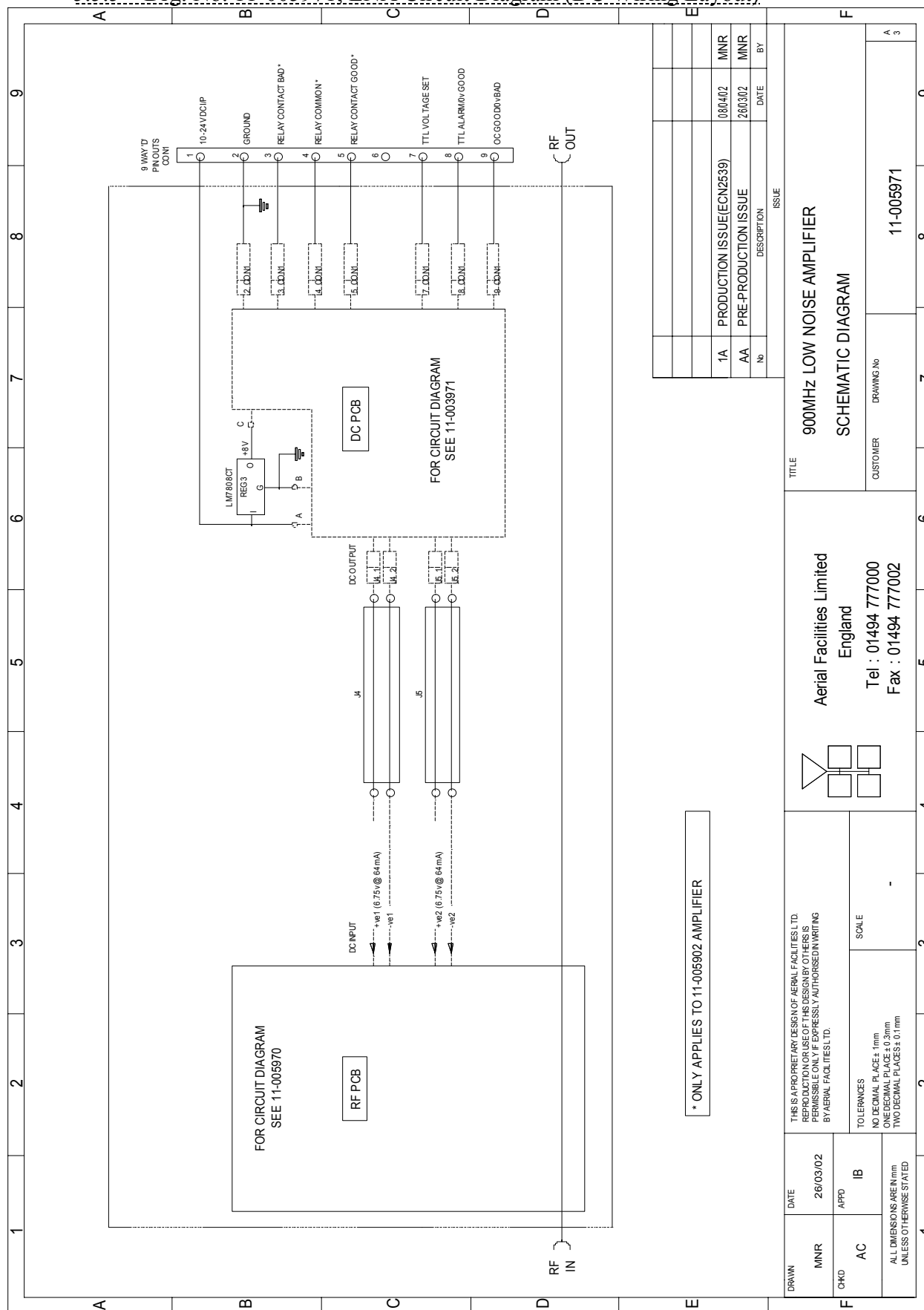
800MHz BDA (Pasadena Figuero Tunnel)
 Maintenance Handbook

Issue No:-1

Date:-15/10/2003

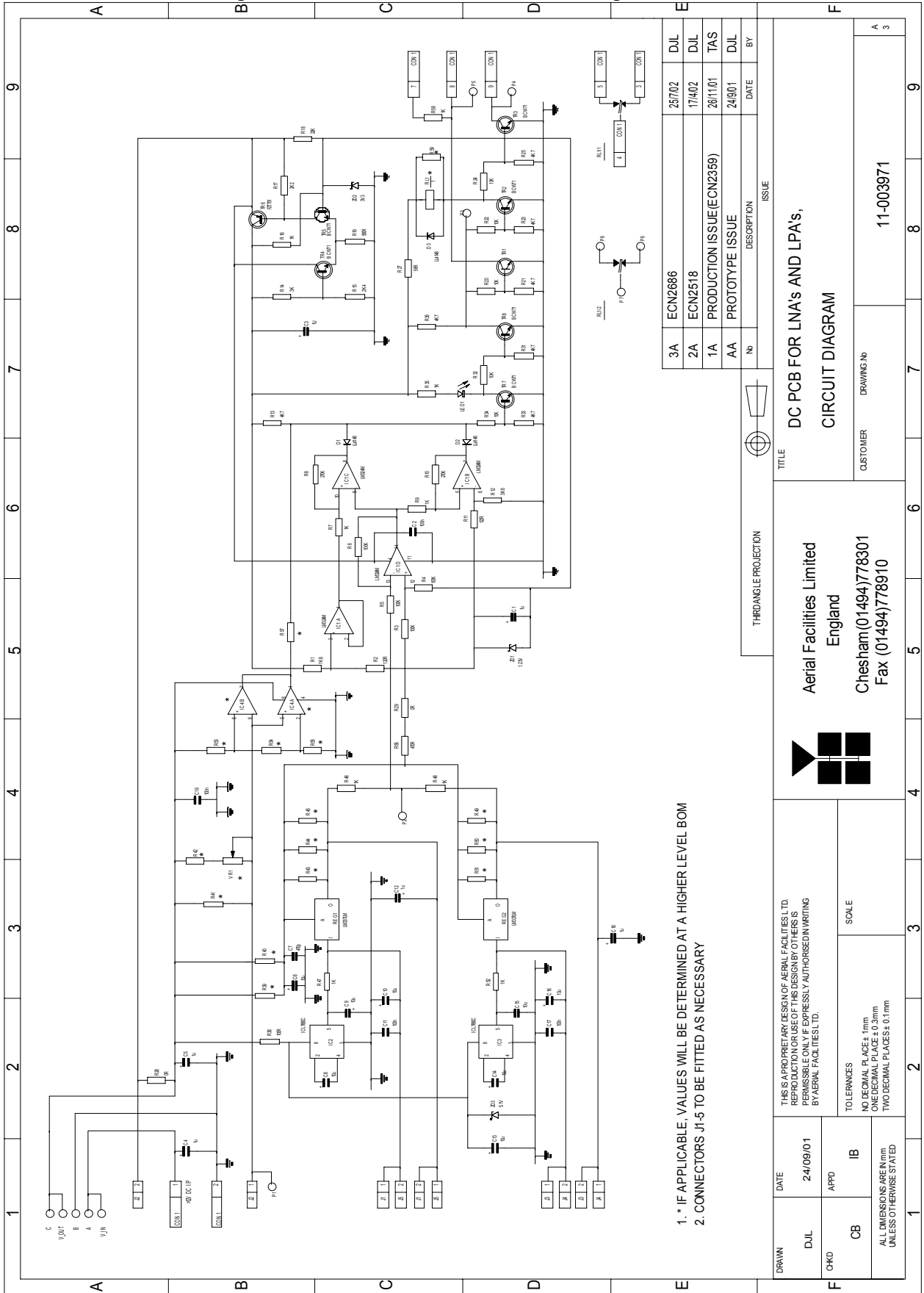
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5.5.5 Drg. Nō. 11-005971, LNA Circuit Diagram (DC Wiring Layout)



* ONLY APPLIES TO 11-005902 AMPLIFIER

5.1.5.6 Drg. No. 11-003971, LNA DC Circuit Diagram



1. * IF APPLICABLE, VALUES WILL BE DETERMINED AT A HIGHER LEVEL BOM
2. CONNECTORS J1-5 TO BE FITTED AS NECESSARY

3A	ECN2686	25/02	DJL
2A	ECN2518	17/02	DJL
1A	PRODUCTION ISSUE (ECN2359)	26/10/1	TAS
AA	PROTOTYPE ISSUE	24/01	DJL
No	DESCRIPTION	DATE	BY



THIRD ANGLE PROJECTION

TITLE	DC PCB FOR LNA's AND LPA's, CIRCUIT DIAGRAM
CUSTOMER	DRAWING No 11-003971

Aerial Facilities Limited
England
Chesham (01494) 778301
Fax (01494) 778910

DRAWN	DATE	24/09/01
DJL	APPRO	IB
CB	SCALE	NO DECIMAL PLACES ± 1mm ONE DECIMAL PLACE ± 0.1mm TWO DECIMAL PLACES ± 0.01mm UNLESS OTHERWISE STATED