

Circuit Description

The 2.4GHz Wireless digital Receiver is a low cost and high performance digital wireless receiver applications which intergrates M-JPEG codec and controller, which mainly to Encoder with base band processor ,and the using oscillator is 12MHz and 32.768KHz .The protable digital audio solutions WM8731, and the using oscillator is 16MHz. The intergrates M-JPEG codec, which controlled by the 2.4GHz wireless module MCU. when the MCU sent the signal to the intergrates M-JPEG codec, the intergrates M-JPEG codec will control the CMOS Sensor, the IR LED&MIC, the DAC and P/A, the GPIO &Control, the SDRAM and the RF module.

When it received the signal from antenna, the signal will deliver to the GPIO control devicer, and through the GPIO to the intergrates M-JPEG codec. The intergrates M-JPEG codec will deal with all the signal and control the command to the other parts of the MCU or module, and sent the final signal to TV or VCR.