

APPENDIX 3: Photographs of test setup

Conducted Emission **Chip Antenna**



Photo 1

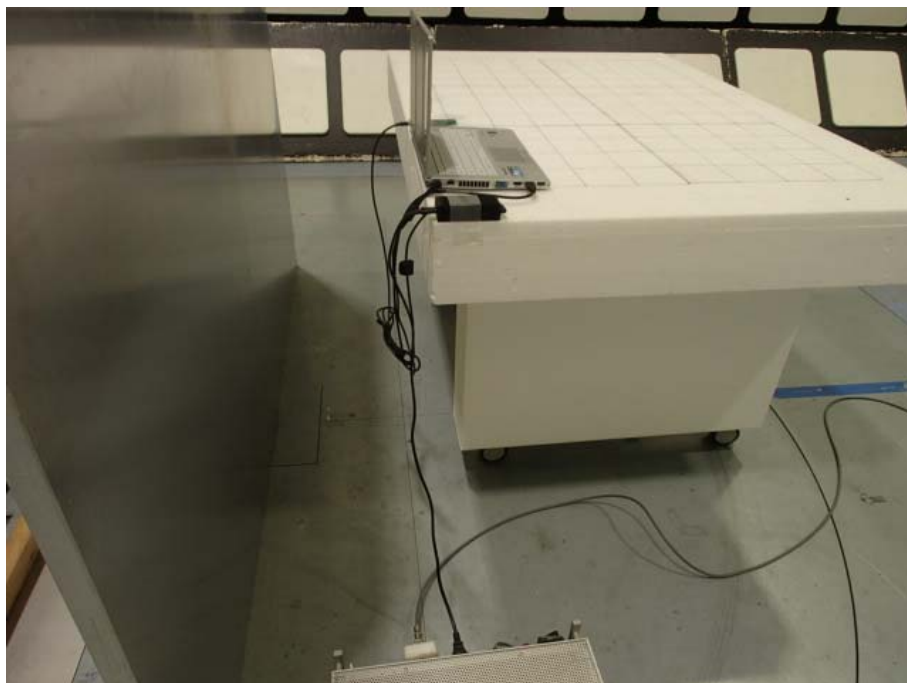


Photo 2

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Conducted Emission
PCB Antenna

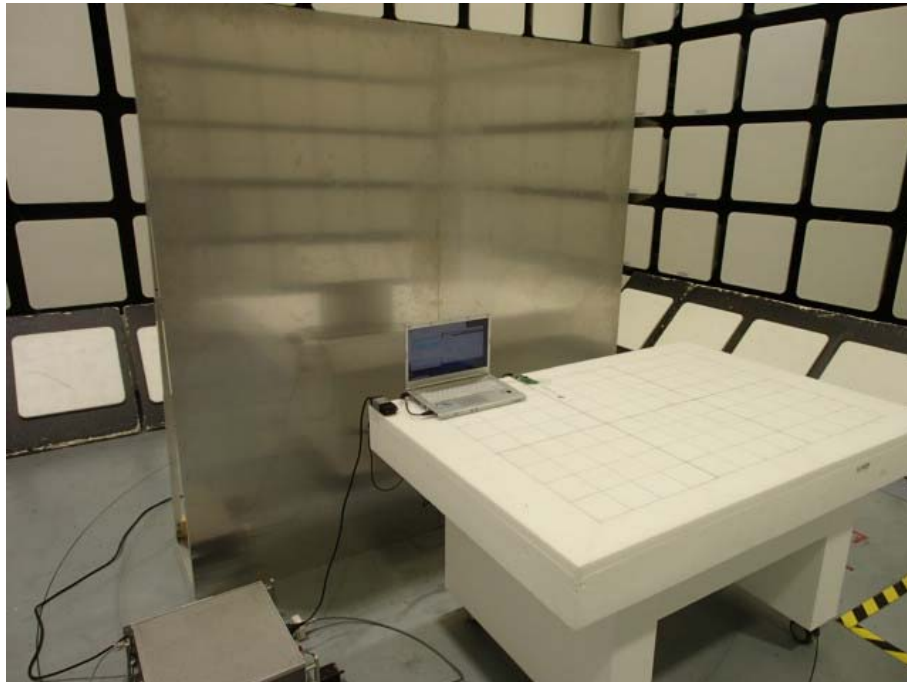


Photo 1

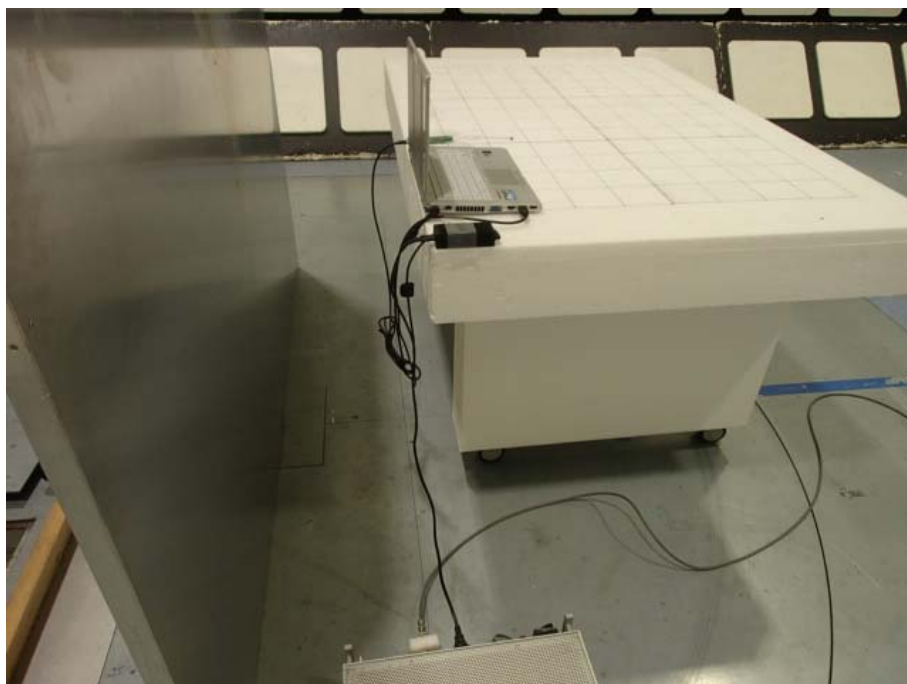


Photo 2

Radiated Spurious Emission
Chip Antenna



Photo 1

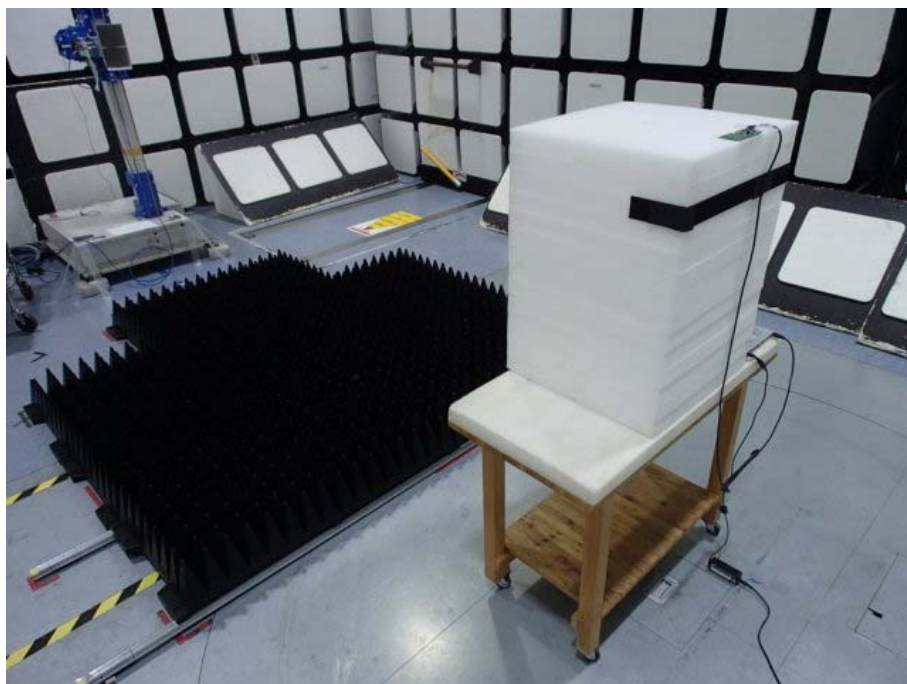


Photo 2

Radiated Spurious Emission
PCB Antenna

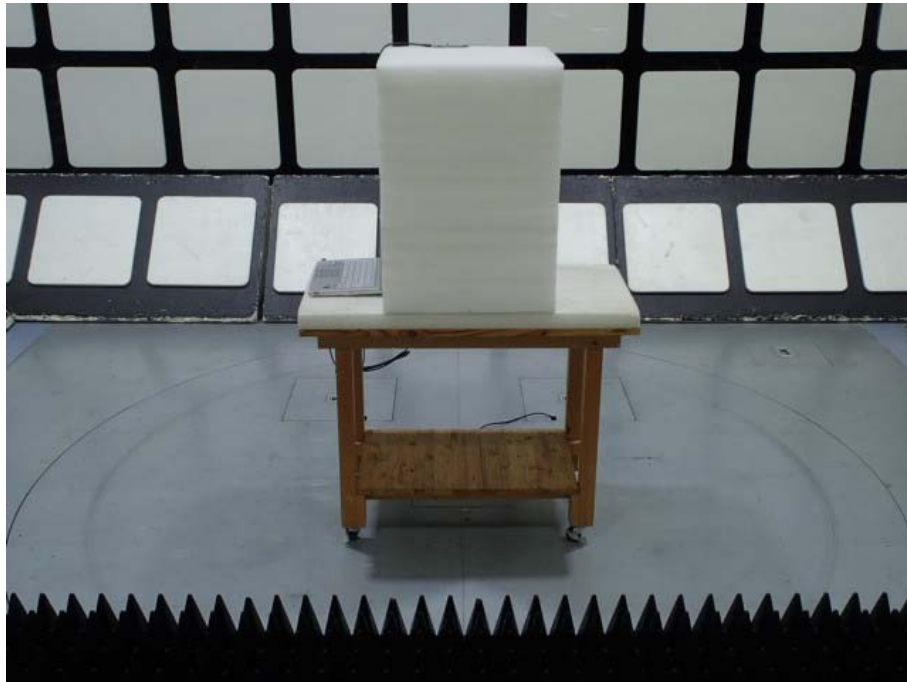


Photo 1

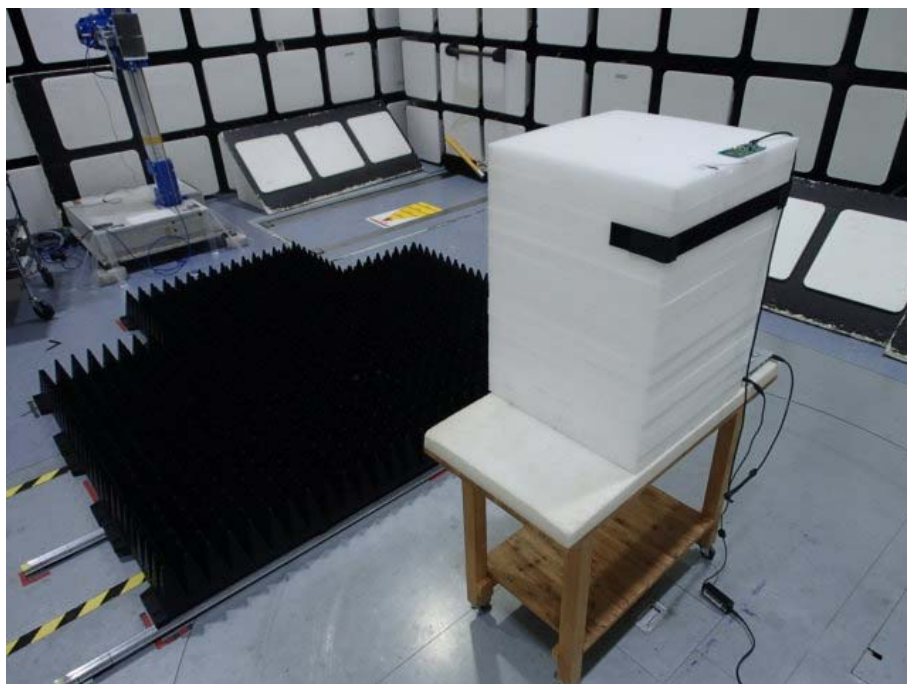


Photo 2

Worst Case Position
Chip Antenna

Below 1 GHz: (Horizontal: X-axis/ Vertical:X-axis)

Above 1 GHz: (Horizontal: X-axis/ Vertical:X-axis)

X-axis



Y-axis



Z-axis



Worst Case Position
PCB Antenna

Module

Below 1 GHz: (Horizontal: X-axis/ Vertical:X-axis)

Above 1 GHz: (Horizontal: X-axis/ Vertical:Y-axis)

Antenna

Below 1 GHz: (Horizontal: X-axis/ Vertical:X-axis)

Above 1 GHz: (Horizontal: X-axis/ Vertical:Y-axis)

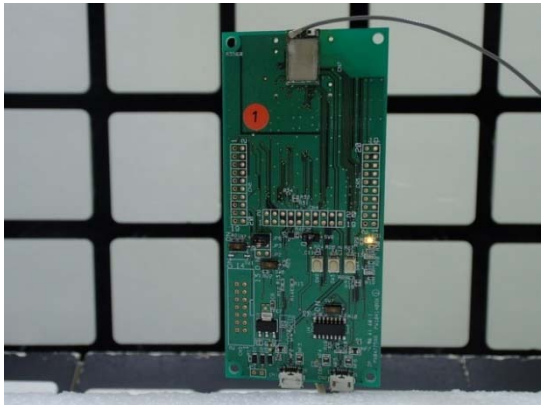
Module
X-axis



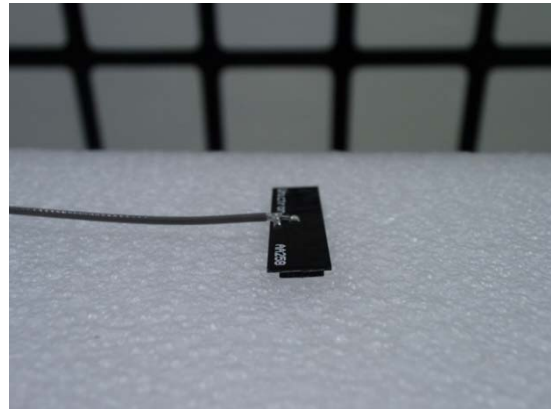
Y-axis



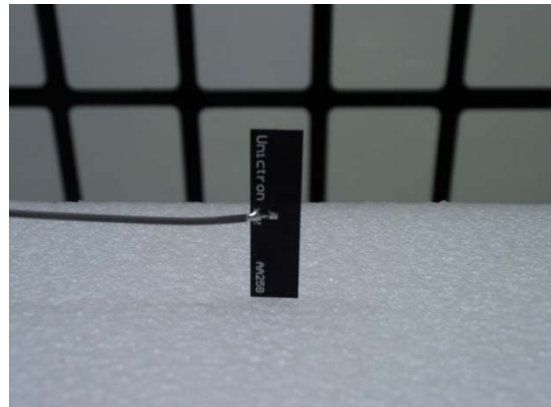
Z-axis



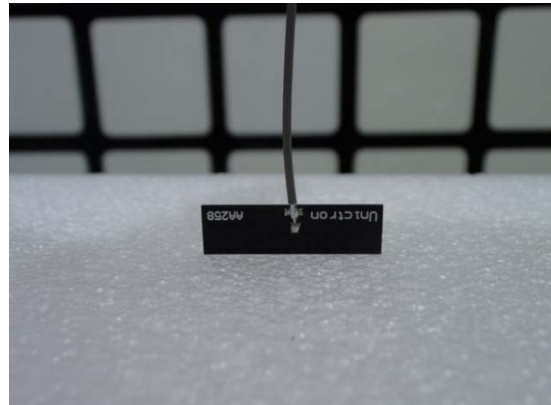
Antenna
X-axis



Y-axis



Z-axis



End of Report