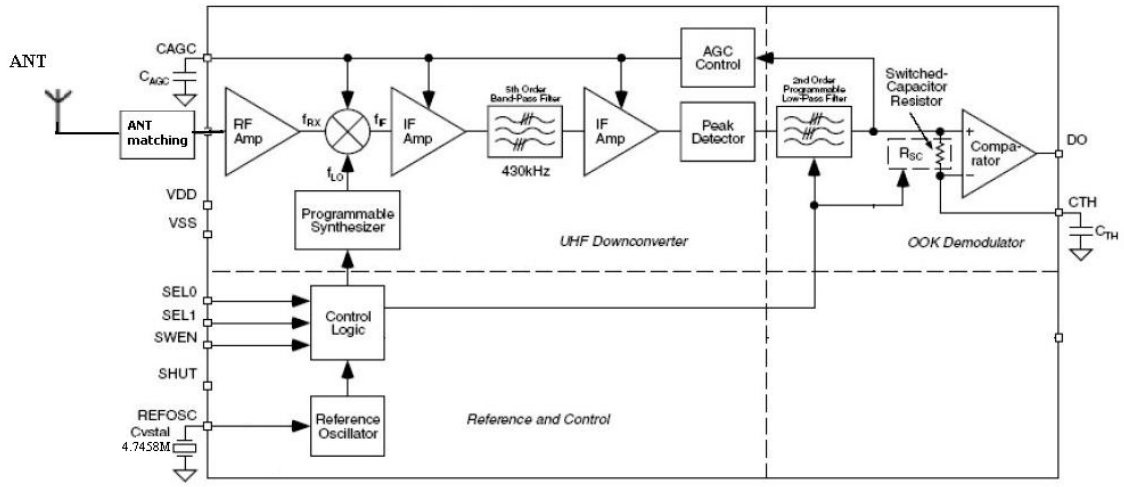

It is consisted with four sections: Ant matching net working, UHF downconverter, Resference and control, and OOK demodulator. Also shown in the figure are two capacitors (CTH, CAGC) and one timing component, usually a crystal. With the exception of a supply decoupling capacitor, and antenna impedance matching network, these are the only external components needed to assemble a complete UHF receiver.

For optimal performance is highly recommended that the MICRF002 is impedance matched to the antenna, the matching network will add an additional two or three components.

Four control inputs are shown in the block diagram: SEL0, SEL1, SWEN, and SHUT. Using these logic inputs, the user can control the operating mode and selectable features of the IC. These inputs are CMOS compatible, and are internally pulled-up. IF Band-pass Filter Roll-off response of the IF Filter is 5th order, while the demodulator data filter exhibits a 2nd order response. There needs a local frequency generator for any super-heterodyne receiver. In this circuit it is based on PLL type circuit inside and a crystal 4.7458 MHz(special for 315 MHz) outside.