ATTACHMENT H.

- Operational Description

CIRCUIT DESCRIPTION

1. Power section(IC4,Q7,8) will be operated and checked the battery voltage from IC1(KS88C2148) when battery power is supplied.

2. If the Battery power is checked normally, Memory(IC2,3) and DSP(IC4) is initialized.

3. After checking of memory & DSP, Back-light is turned on and 'SYSTEM START' message is displayed on LCD. And also, the data saved in memory will be read by CPU and displayed on LCD.

- At this time, the main clock of CPU is X1(6MHz) and Sub clocks for Display and save mode are X2(32.768KHz).

4. At stand-by status, REC-CNT(Q5,6) is run and REC Indicator LED is turned on if S1(REC/STOP) is turned on.

- When recording starts, MIC related circuit is run and the voice signal will be amplified by IC1(NJM2100V) and AGC circuit (Q4,D1,D4).
And then the amplified signal is input to A/D convector of IC3(MSM7702).
The data converted by IC3(MSM7702) will be saved in memory through IC4(D6571E) & IC1(KS88C2148).

5. If you turn off S1, REC-CNT is OFF and changed to stand-by status.

6. If S2(PLAY/STOP) is on, SPK-CNT(Q5,6) is turned on. Then the data saved in Memory is read and out-put through speaker by IC2(NJM2113R) after D/A converted through DSP(IC4) and IC3.

7. RF related Operation:
- IC1 read the data received from ANT(pin15,16) Port of IC6(nRF403) and ON/OFF the REC.

8. If one minute is passed at stand-by status, the unit is changed to save mode and CPU turns off the LCD
At save mode, the power supply is run by VCC(IC4) only and IC1(KS88C2148) is run by X2 instead of X1.
At this time, IC1 frequently check the Key in-put and data of IC6.