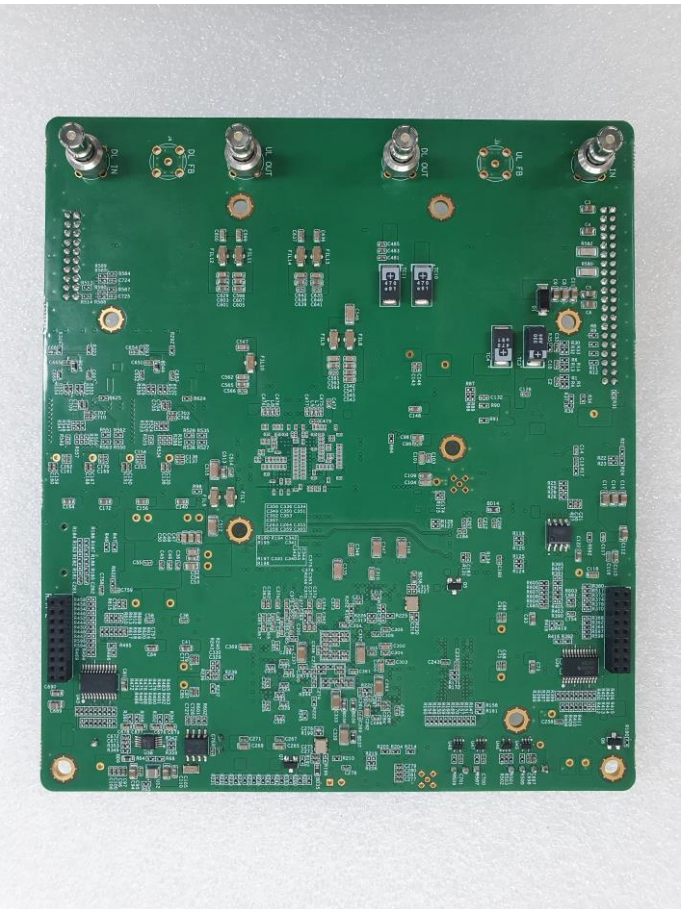
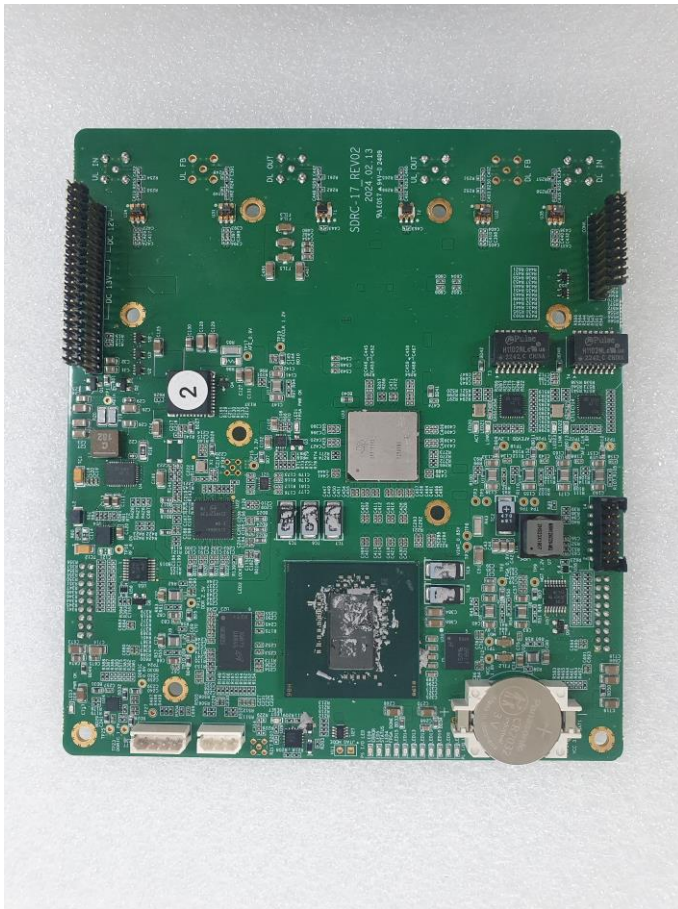


INTERNAL PHOTOGRAPHS

■ INTERNAL PHOTO (DSP)

Top

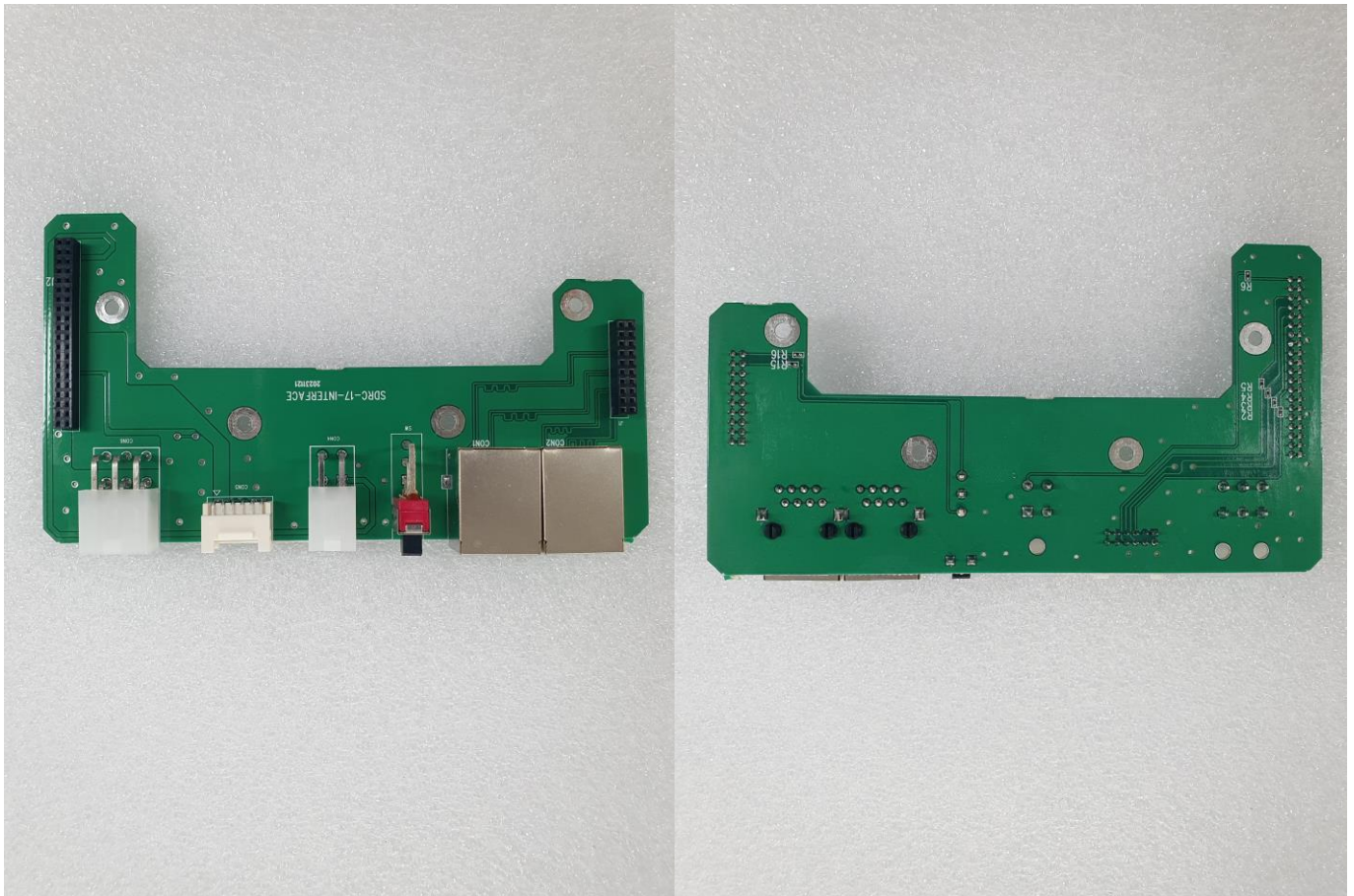
Bottom



■ INTERNAL PHOTO (INTERFACE)

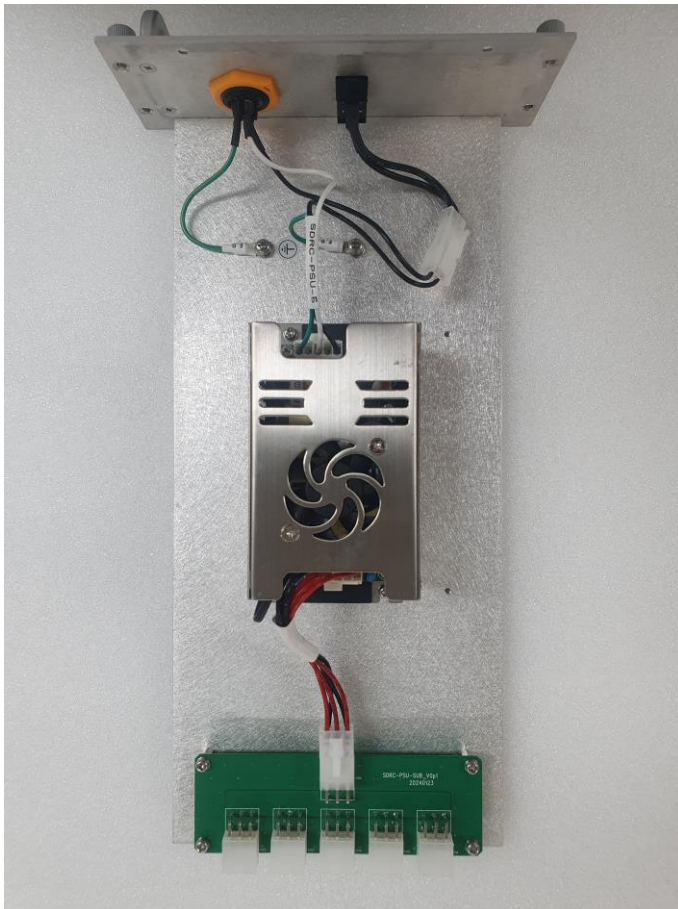
Top

Bottom

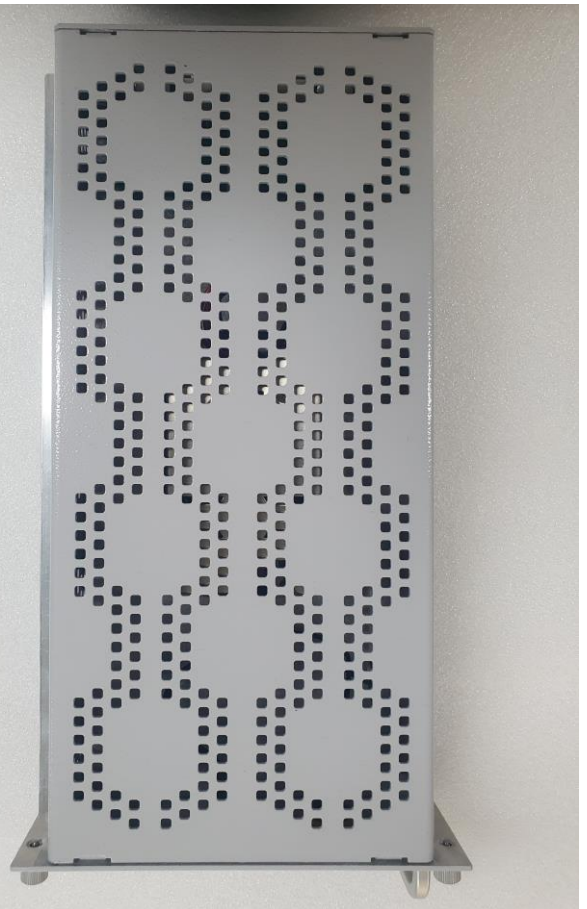


■ INTERNAL PHOTO (PSU-5)

Internal View



Top



■ INTERNAL PHOTO (PSU-5)

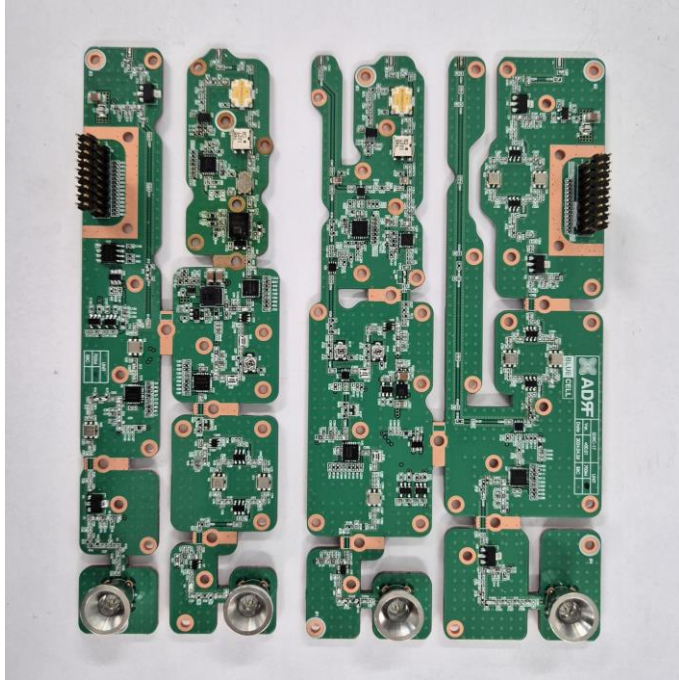
Front

Rear

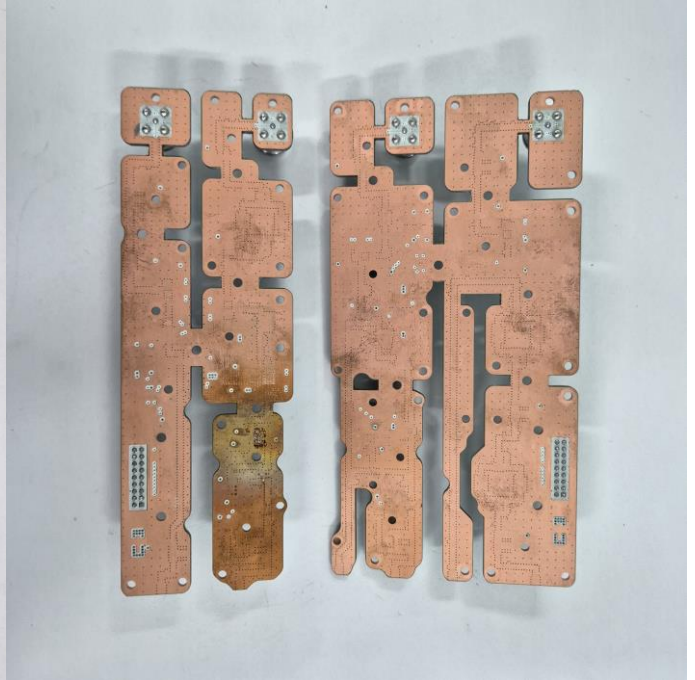


■ INTERNAL PHOTO (RF Module: 700)

PCS Top

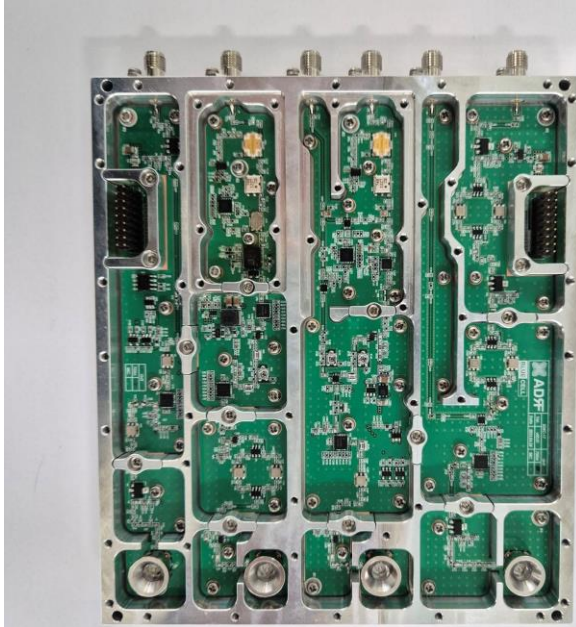


PCB Bottom

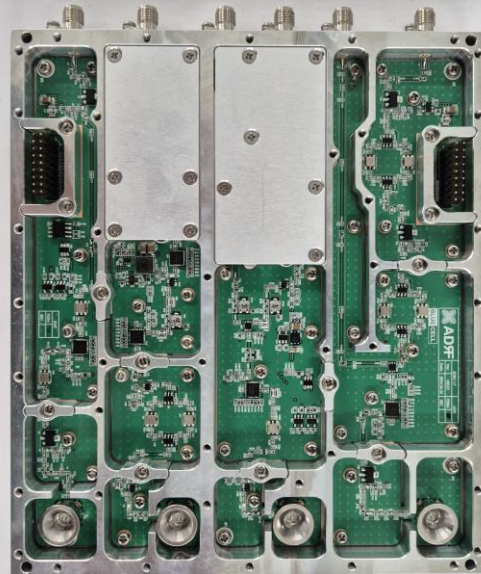


■ INTERNAL PHOTO (RF Module: 700)

Internal View 1

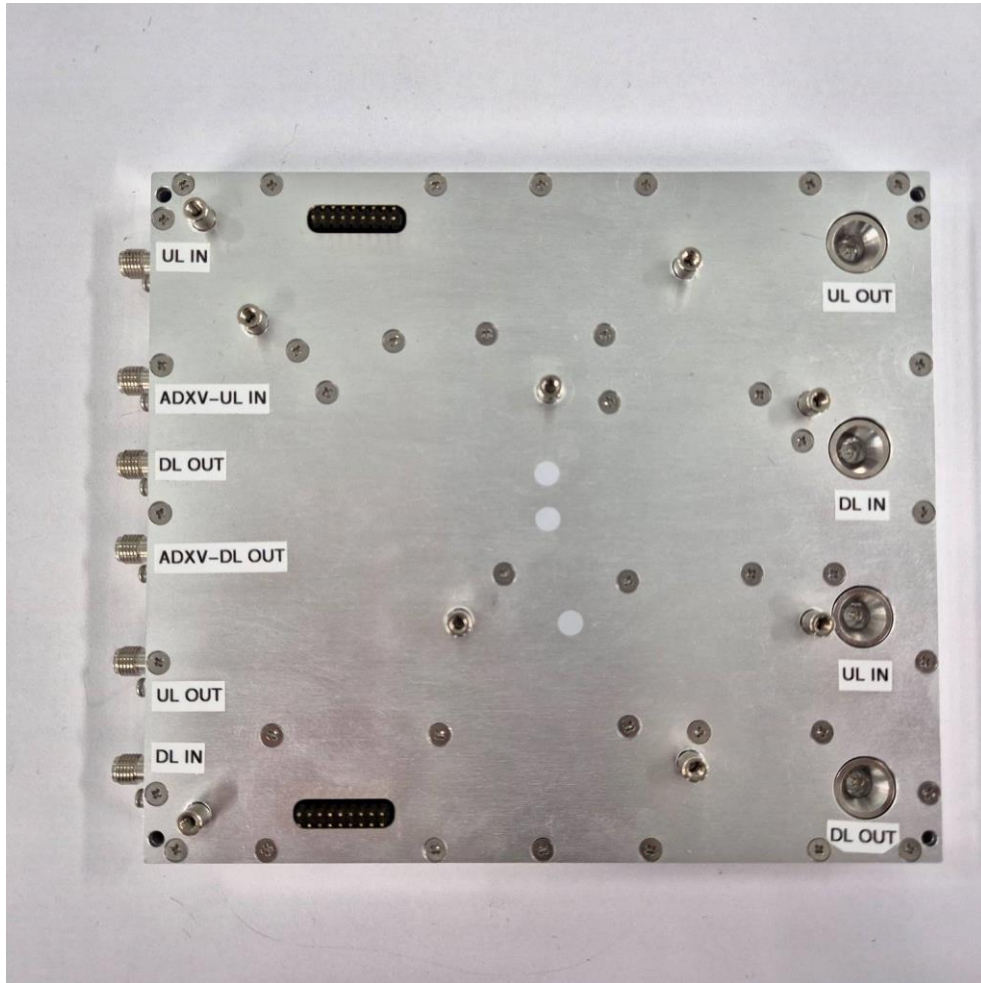


Internal View 2



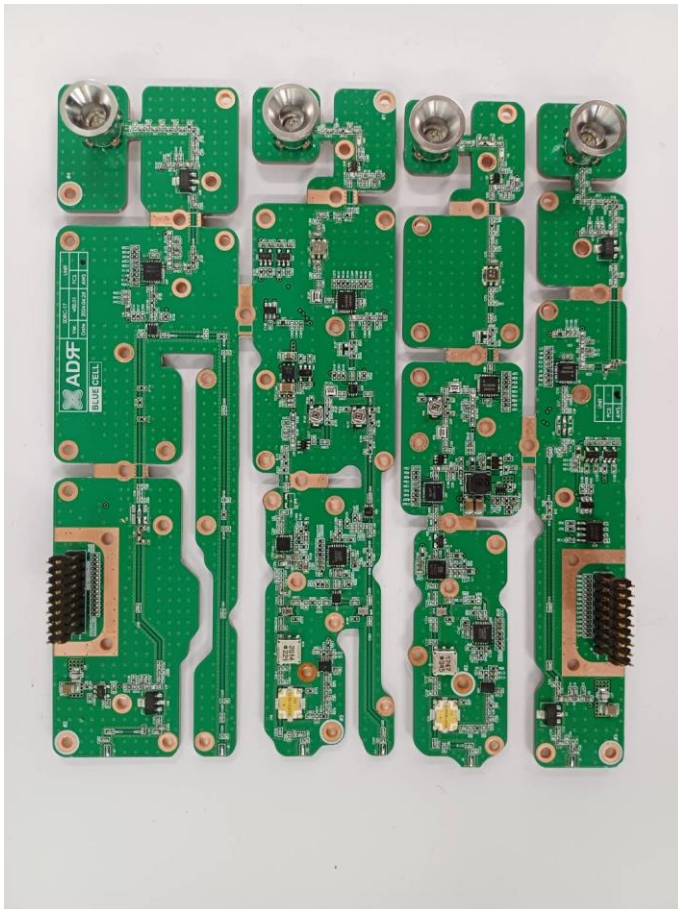
■ INTERNAL PHOTO (RF Module: 700)

Front

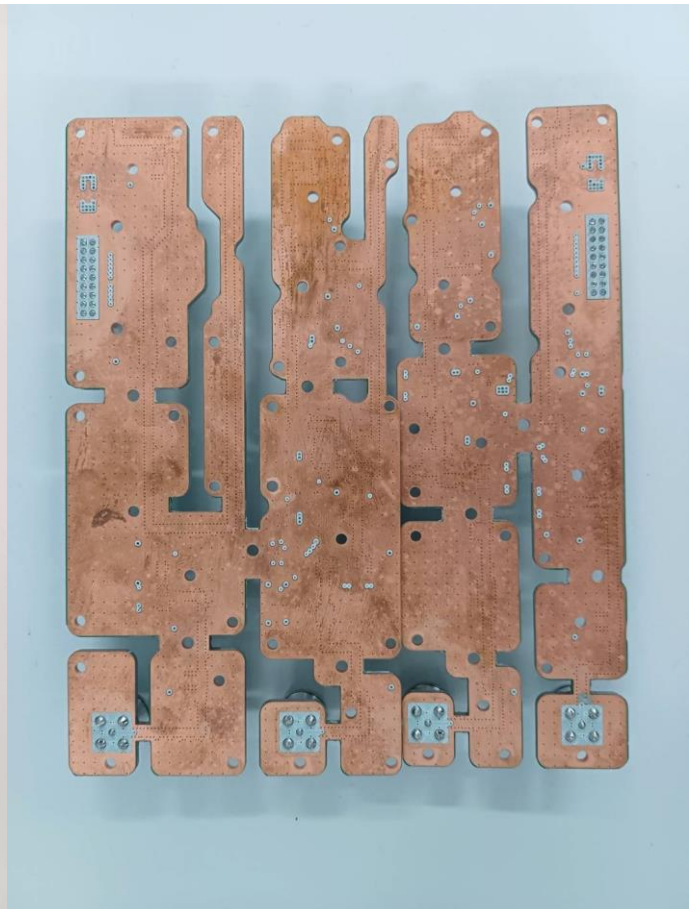


■ INTERNAL PHOTO (RF Module: AWS)

PCB Top



PCS Bottom

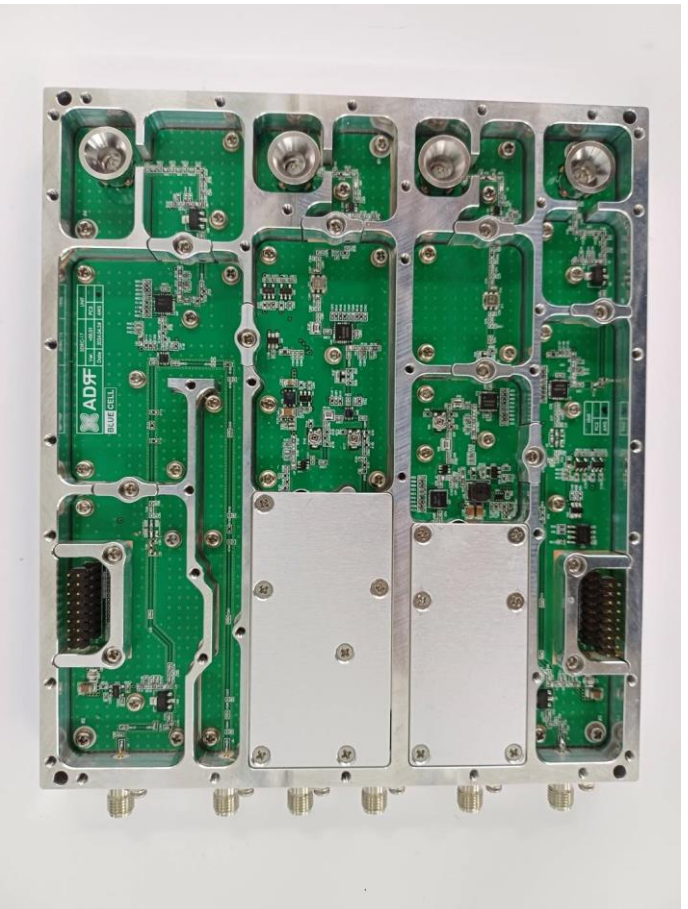


■ INTERNAL PHOTO (RF Module: AWS)

Internal View 1

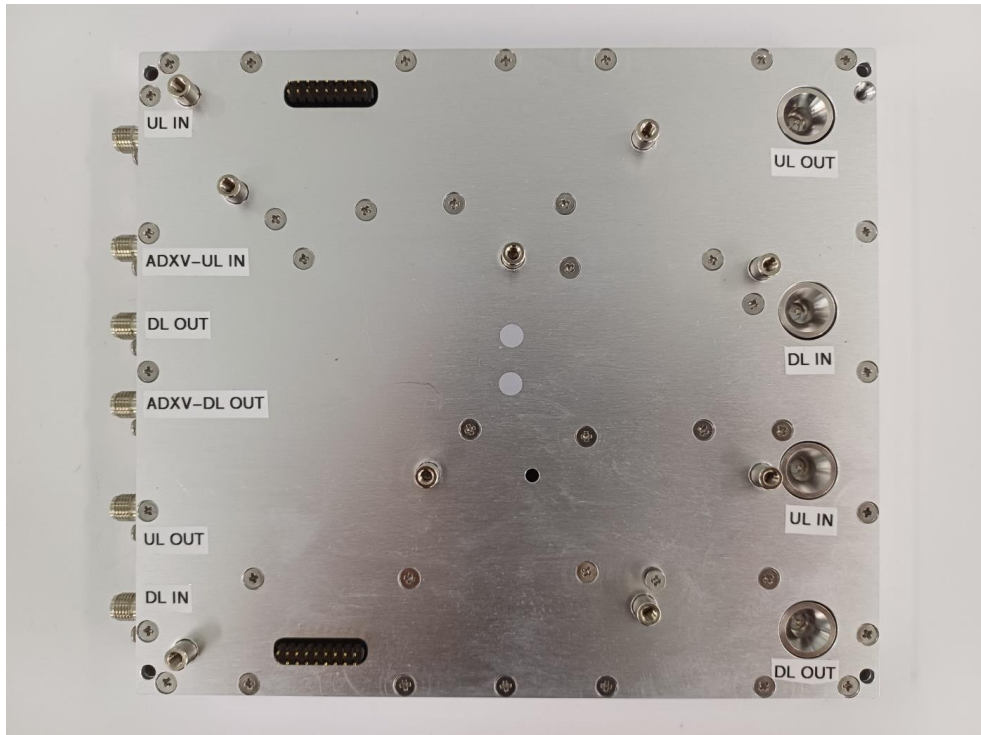


Internal View 2



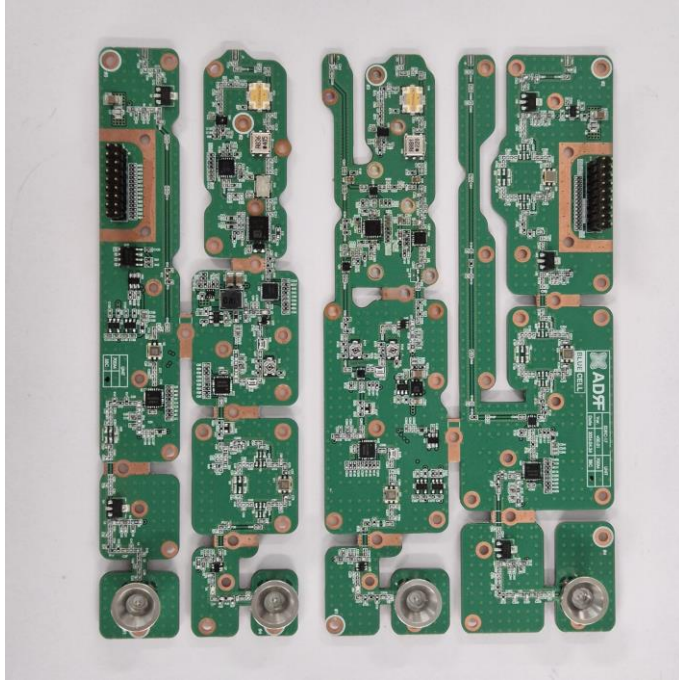
■ INTERNAL PHOTO (RF Module: AWS)

Front



■ INTERNAL PHOTO (RF Module: CELL)

PCB Top



PCB Bottom



■ INTERNAL PHOTO (RF Module: CELL)

Internal View 1

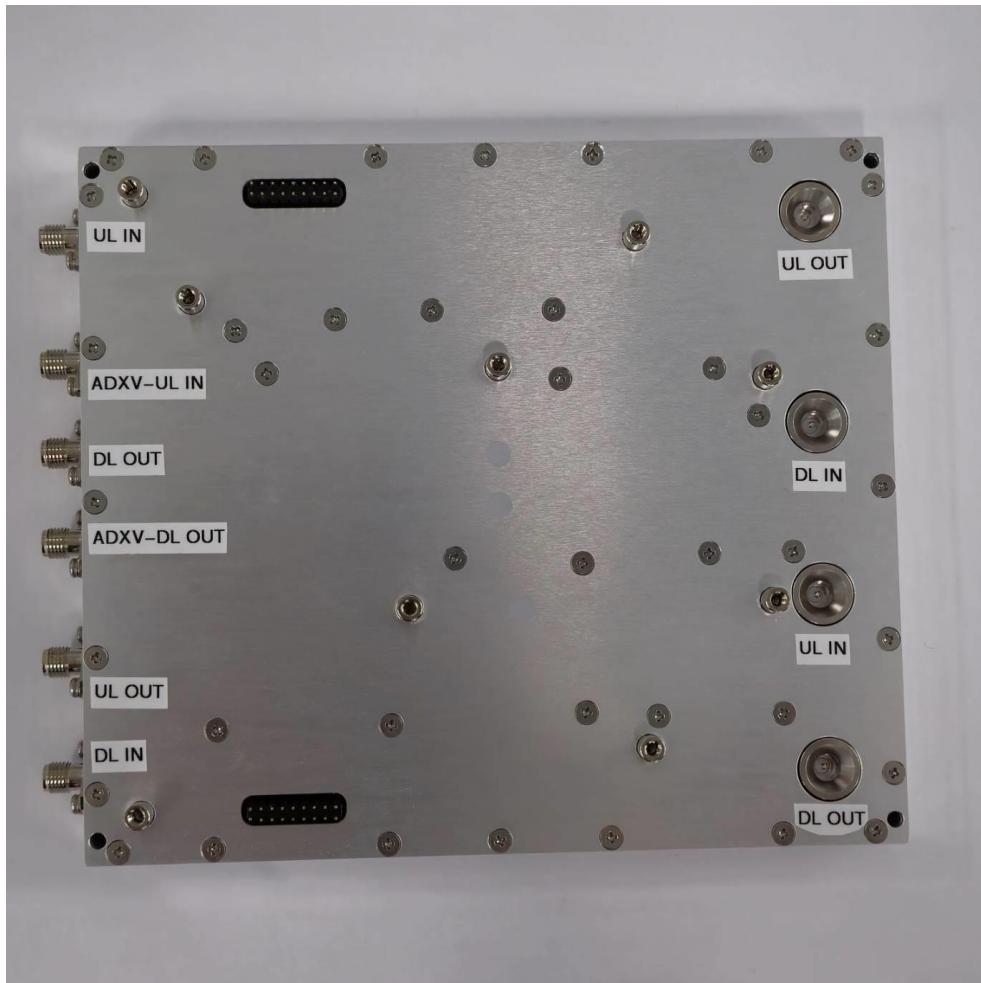


Internal View 2



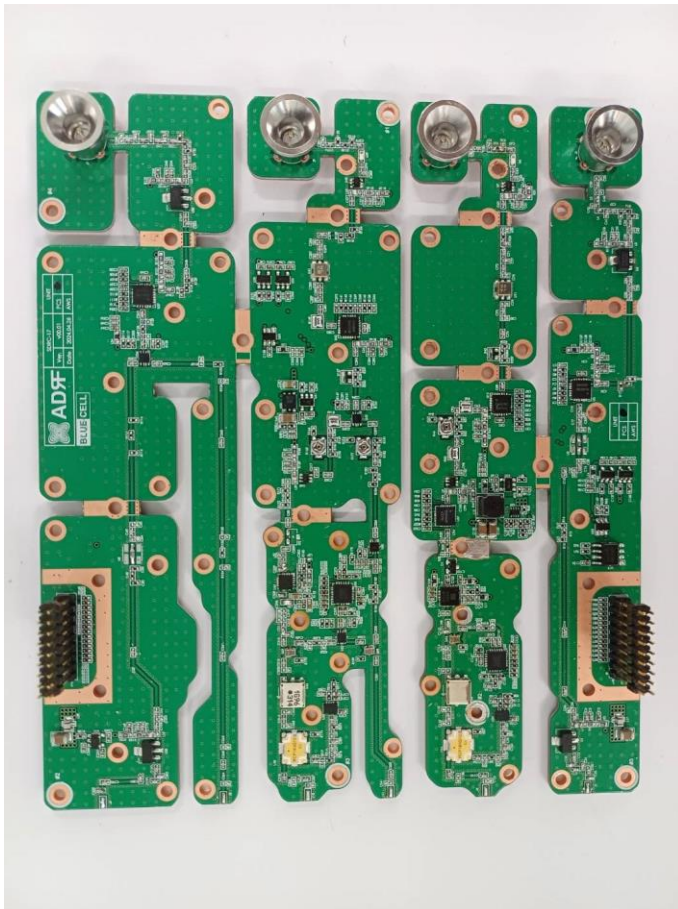
■ INTERNAL PHOTO (RF Module: CELL)

Front

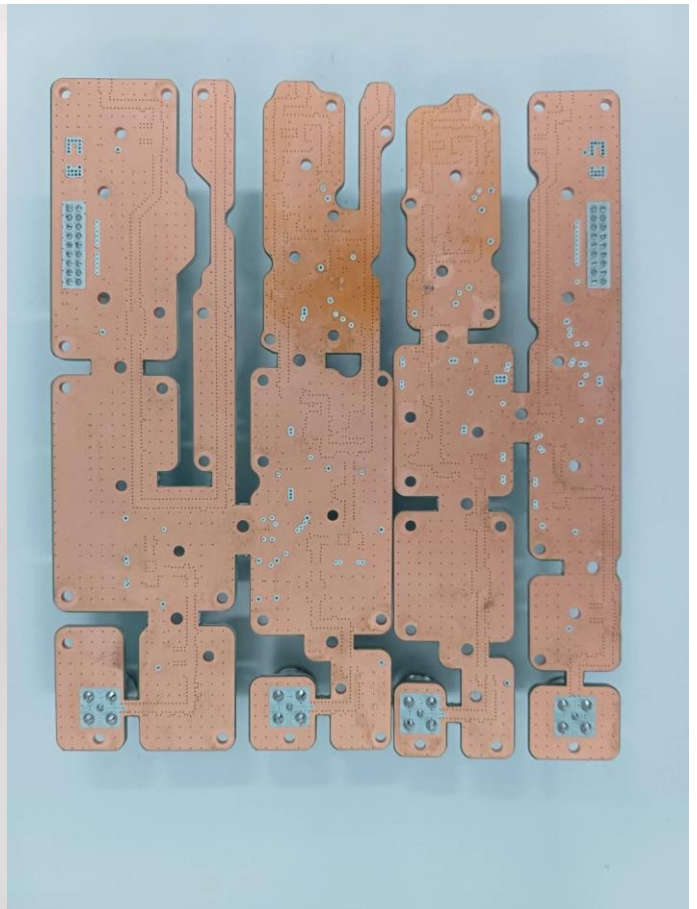


■ INTERNAL PHOTO (RF Module: PCS)

PCB Top

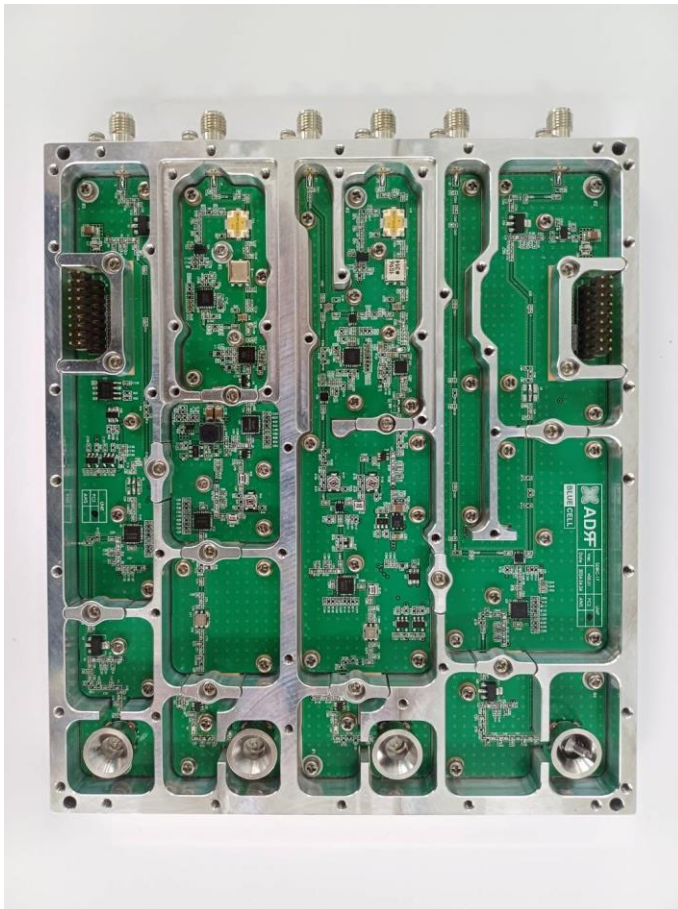


PCB Bottom

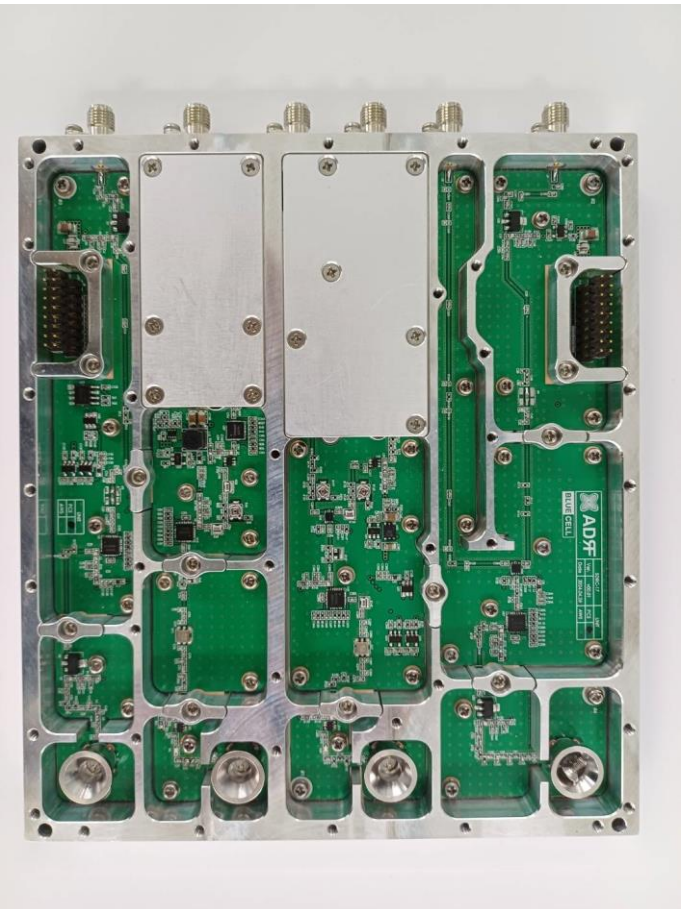


■ INTERNAL PHOTO (RF Module: PCS)

Internal View 1

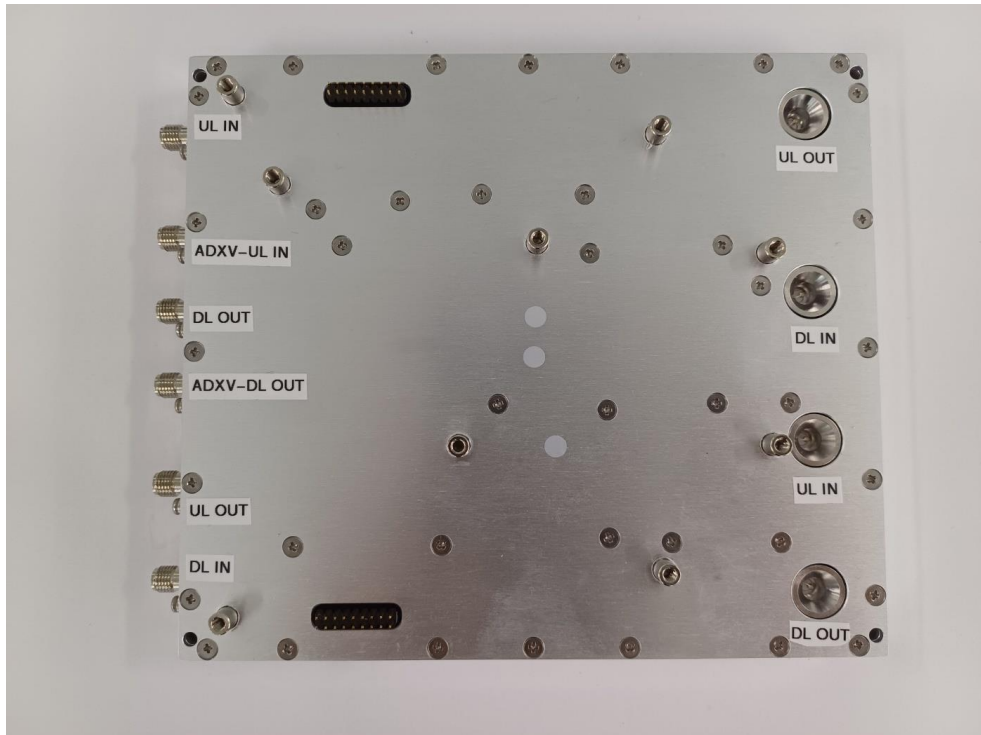


Internal View 2



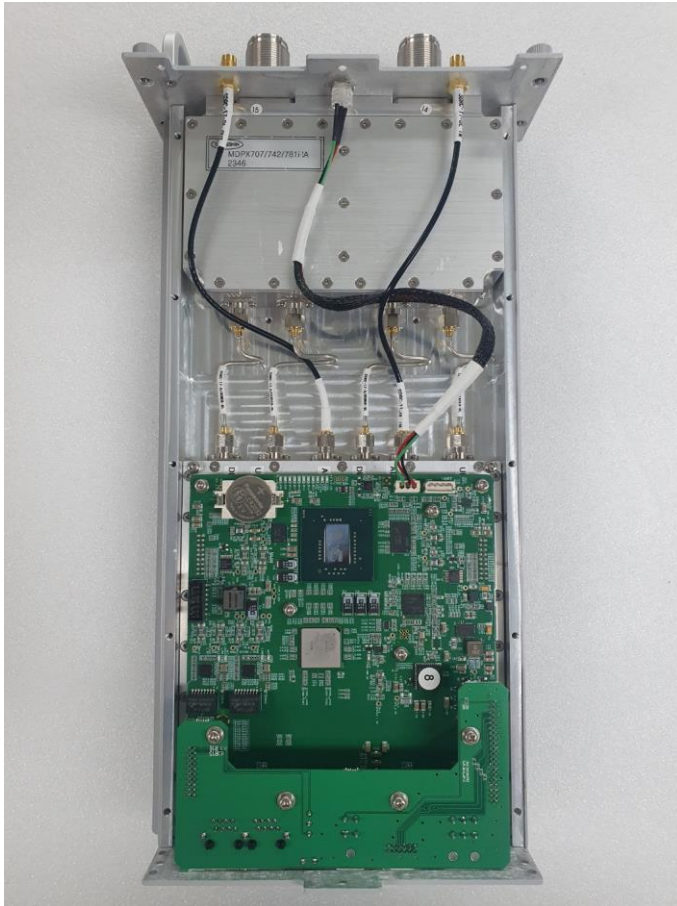
■ INTERNAL PHOTO (RF Module: PCS)

Front



■ INTERNAL PHOTO (Internal View)

700



AWS

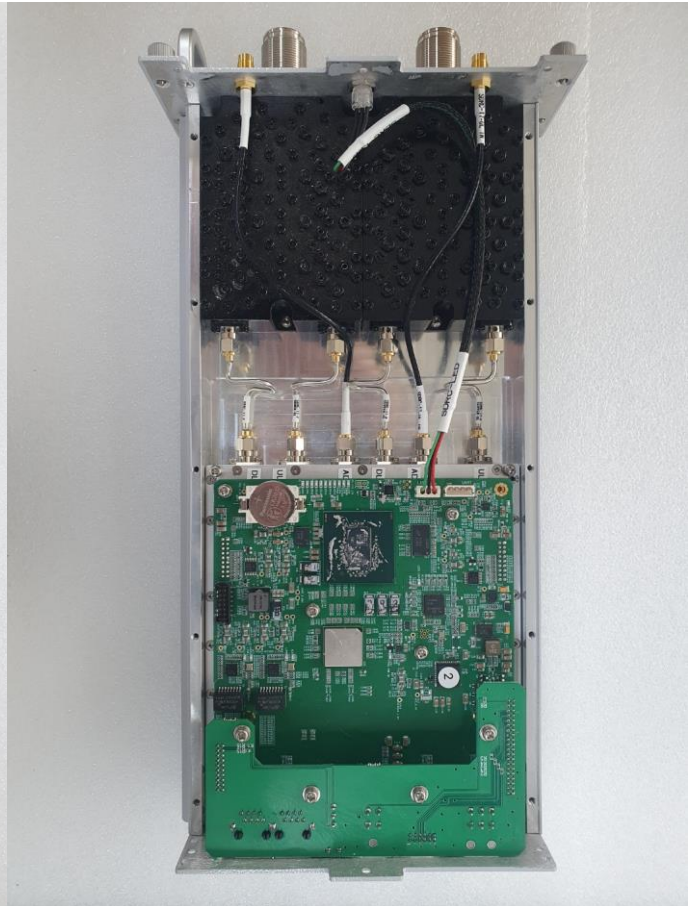


■ INTERNAL PHOTO (Internal View)

CELL



PCS



■ INTERNAL PHOTO (Filter View: 700)

Port 1

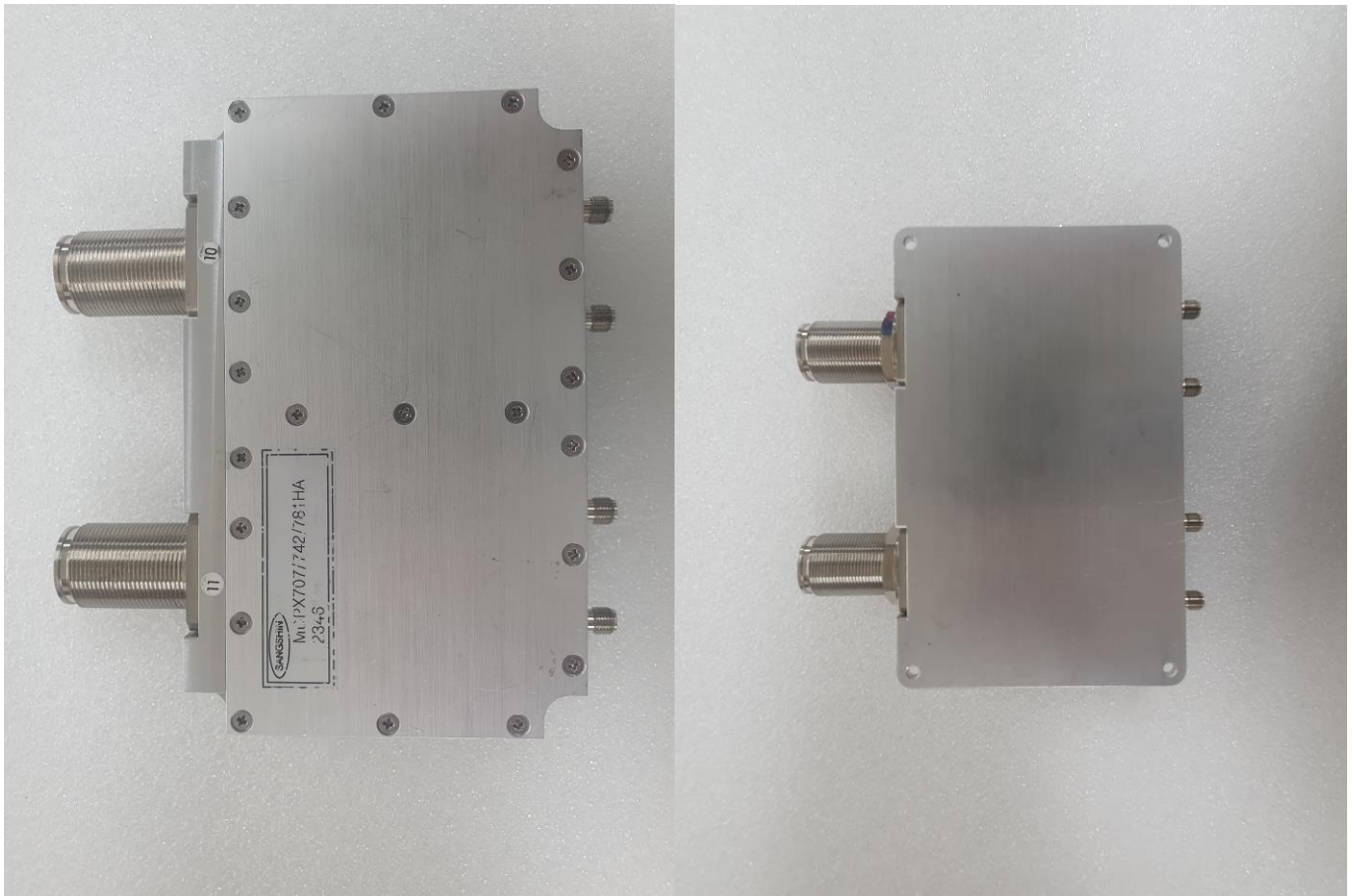
Port 2



■ INTERNAL PHOTO (Filter View: 700)

Top

Bottom



■ INTERNAL PHOTO (Filter View: AWS)

Port 1

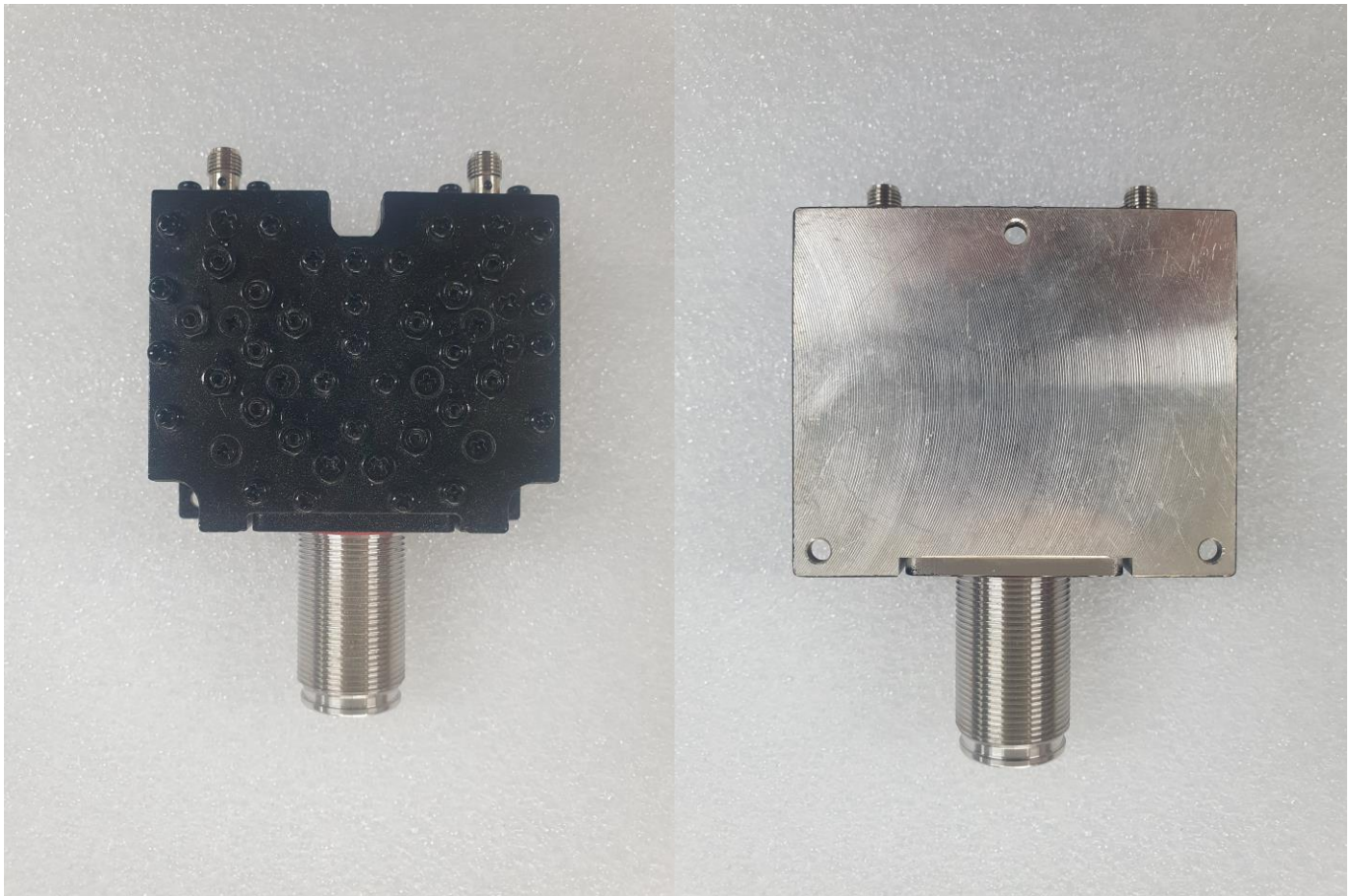
Port 2



■ INTERNAL PHOTO (Filter View: AWS)

Top

Bottom



■ INTERNAL PHOTO (Filter View: CELL)

Port 1

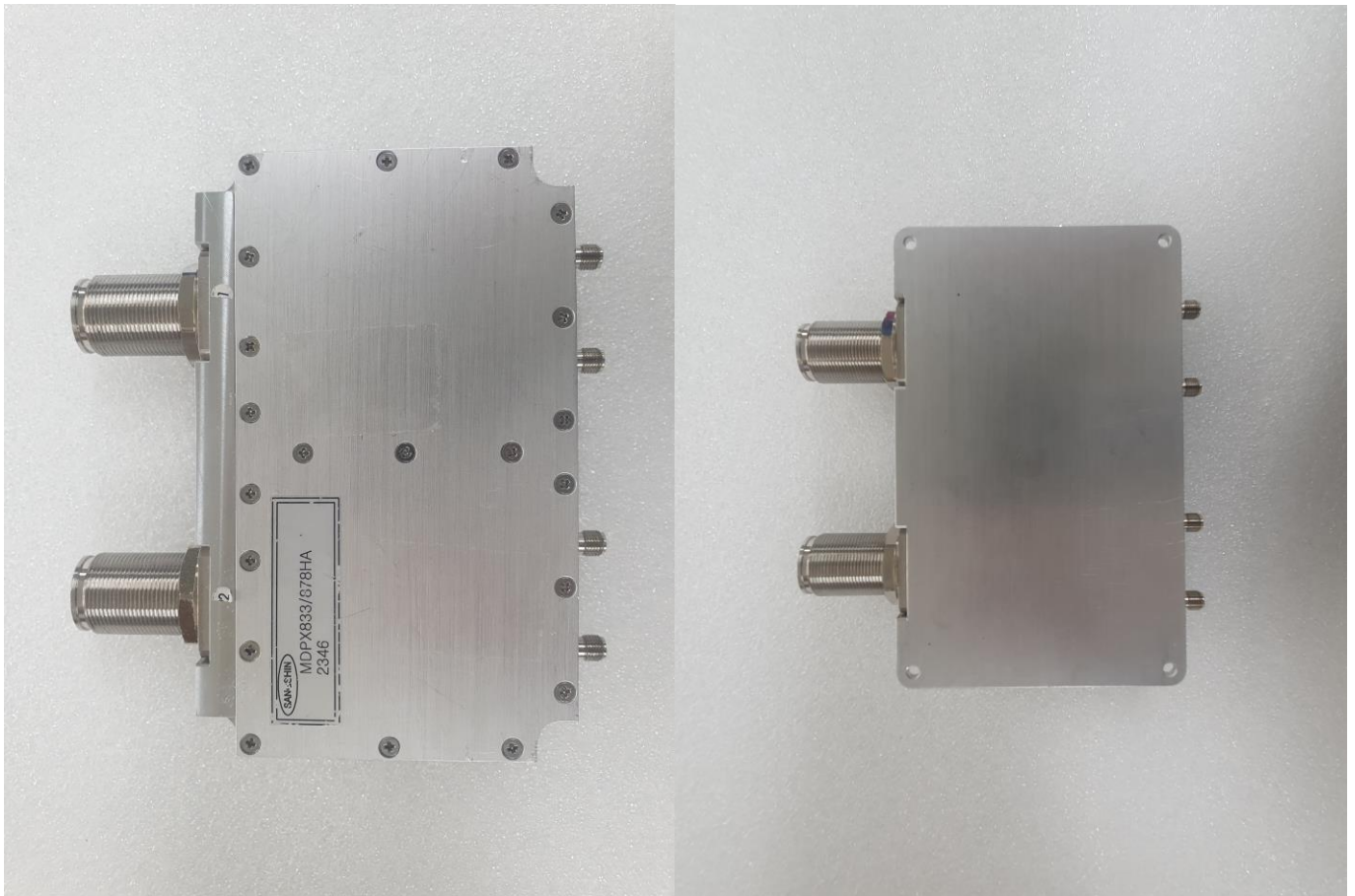
Port 2



■ INTERNAL PHOTO (Filter View: CELL)

Top

Bottom



■ INTERNAL PHOTO (Filter View: PCS)

Port 1



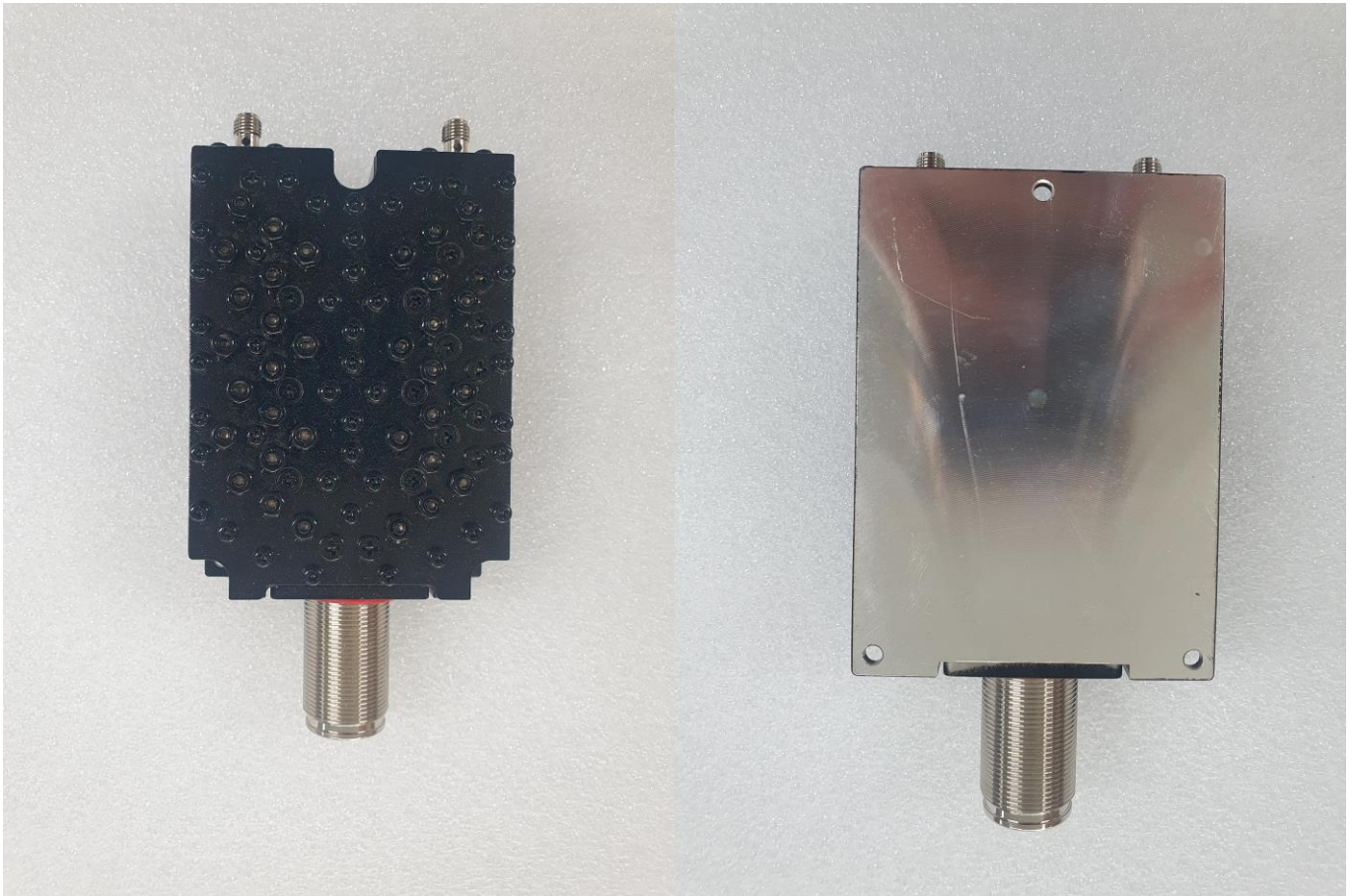
Port 2



■ INTERNAL PHOTO (Filter View: PCS)

Top

Bottom



End of Report