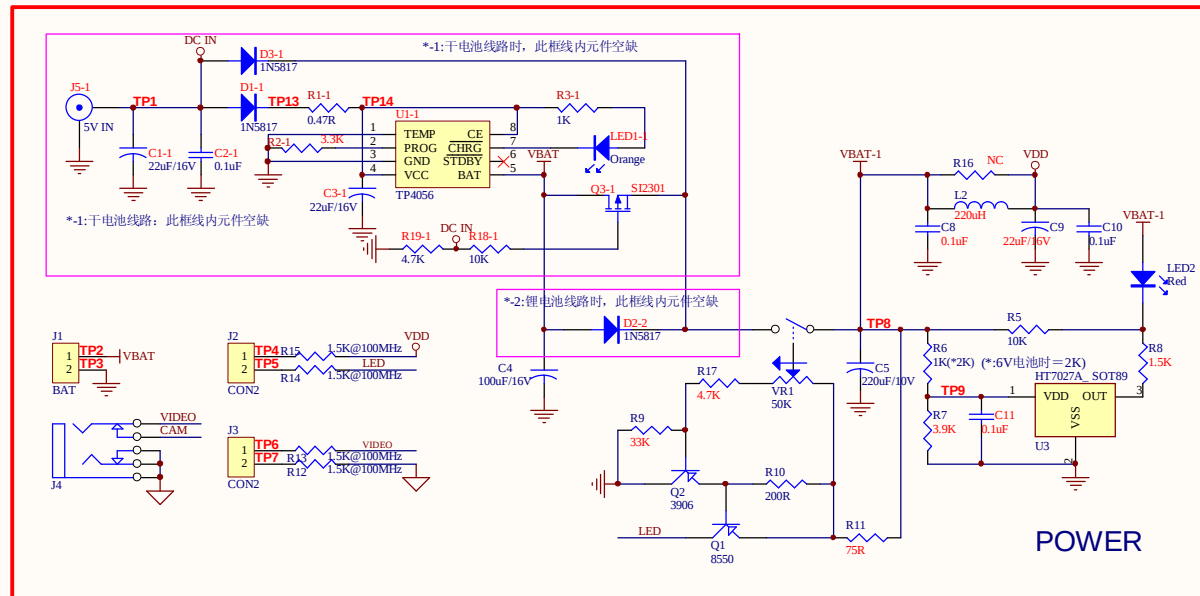




(2) Circuit Diagram

