

The video decoder IC TW9900 digitizes the input composite-video-baseband signal and decodes it in digital domain then outputs the decoded signal(YCbCr 4:2:2) to main chip,ES11V01,in ITU-R656 format. The main chip ES11V01 is an ARM based processor which integrates H.264,MPEG4,M-JPEG encoders and peripheral control interfaces, it receives the CCIR656 video data and re-encodes into H.264,MPEG4, M-JPEG format depends on what remote access device requires, then output the encoded data to WiFi module via USB interface. The WiFi RF module integrates MAC/BBP and RF transceiver circuits on single PCB, supports IEEE802.11 b/g/n(1T1R) standards. There are also SDRAM and flash ROM on the system board for supporting the software running.