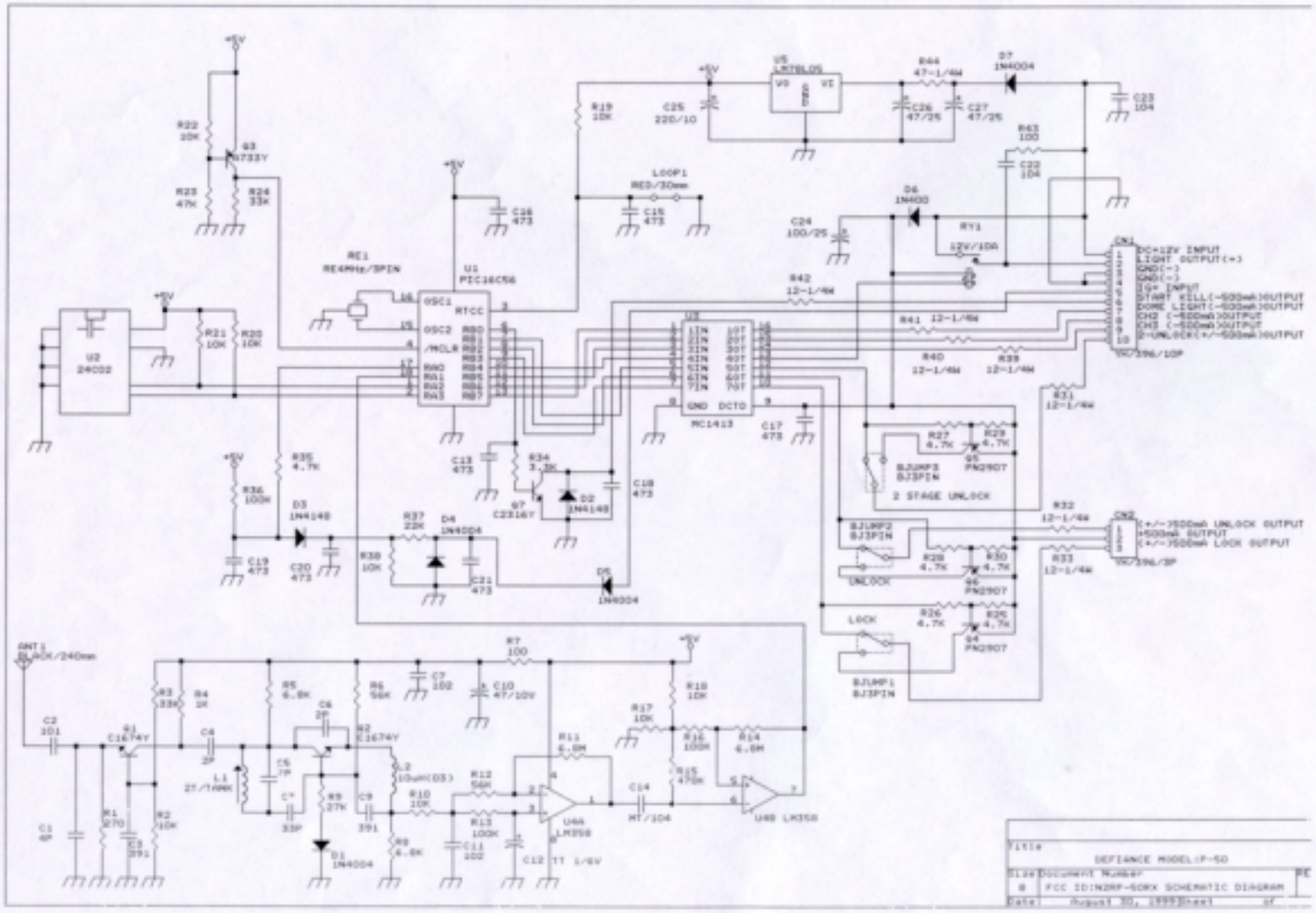
DEFIANCE MODEL:P-50
FCC ID:N2RP-50RX SCHEMATIC DIAGRAM
U1 PIC16C56
U2 24C02
U3 MC1413
U5 LM78L05
CN1 DC+12V INPUT
LIGHT OUTPUT(+)
GND(-)
GND(-)
IG+ INPUT
START KILL(-500mA)OUTPUT
DOME LIGHT(-500mA)OUTPUT
CH2 (-500mA)OUTPUT
CH3 (-500mA)OUTPUT
2-UNLOCK(+/-500mA)OUTPUT
CN2 (+/-)500mA UNLOCK OUTPUT
+500mA OUTPUT
(+/-)500mA LOCK OUTPUT
2 STAGE UNLOCK
UNLOCK
LOCK
BJUMP1 BJ3PIN
BJUMP2 BJ3PIN
BJUMP3 BJ3PIN
U4A LM358
U4B LM358
Q1 C1674Y
Q2 C1674Y
Q3 N733Y
Q7 C2316Y
Q4 PN2907
Q5 PN2907
Q6 PN2907
D1 1N4004
D2 1N4148
D3 1N4148
D4 1N4004
D5 1N4004
D6 1N400
D7 1N4004
RY1 12V/10A
LOOP1 RES/30mm
RE1 RE4MHz/3PIN
ANT1 BLACK/240mm
Title
Size B
Date August 30, 1999
Sheet of