

ZP-6L1
Pentium II 440LX ATX Motherboard
User's Manual
Version 0

Federal Communications Commission (FCC) Statement

RADIO FREQUENCY INTERFERENCE STATEMENT

This equipment has been tested and found to comply with the limits for a Class B digital device, pursuant to Part 15 of the FCC rules. These limits are designed to provide reasonable protection against harmful interference in a residential installation. This equipment generates, uses and can radiate radio frequency energy and if not installed and used in accordance with the instructions, may cause harmful interference to radio communications. However, there is no guarantee that interference will not occur in a particular installation. If this equipment does cause harmful interference to radio or television reception, which can be determined by turning the equipment off and on, the user is encouraged to try to correct the interference by one or more of the following measures:

- Reorient the receiving antenna.
- Increase the separation between the equipment and receiver.
- Connect the equipment into an outlet on a circuit different from that to which the receiver is connected.
- Consult the dealer or an experienced radio/TV technician for help.

Any special accessories needed for compliance must be specified in the instruction manual.

Warning: A shielded-type power cord is required in order to meet FCC emission limits and also to prevent interference to the nearby radio and television reception. It is essential that only the supplied power cord be used.

Use only shielded cables to connect I/O devices to this equipment.

You are cautioned that changes or modifications not expressly approved by the party responsible for compliance could void your authority to operate the equipment.

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Chapter 1 Introduction

This manual is designed to give you information on the ZP-6L1 motherboard. It is divided into the following sections:

- Introduction
- Specifications
- Hardware Description
- Configuring the Motherboard
- Installation
- BIOS and System Setup
- LANDesk User's Guide

Checklist

Please check that your package is complete and contains the items below. If you discover damaged or missing items, please contact your dealer.

- The ZP-6L1 Motherboard
- 1 IDE ribbon cable
- 1 floppy ribbon cable
- 1 CD containing Intel LANDesk software, PIIX4 Bus Master IDE driver and utilities, or 1 diskette containing PIIX4 Bus Master IDE driver and flash utility. *Your package comes with the CD containing Intel's LANDesk software if your motherboard supports the onboard hardware monitoring IC.*
- *NOTE: LANDesk software is option.*

Chapter 2 Specifications

The ZP-6L1 is a high-performance ATX 440LX motherboard with a Slot 1 connector for Pentium® II processors. It offers flexibility in terms of CPU frequency and main memory type and size. The main features of the motherboard consist of the following:

CPU Socket

Slot 1

Processor

Intel Pentium II 200/233/266/300/333/366/400MHz

L2 Cache

CPU integrated L2 cache

CPU Voltage

Switching voltage regulator on board supporting multiple voltage ranging 1.8V-3.5V

Main Memory

Three 168-pin DIMM sockets

Memory types: Extended Data Output (EDO) DRAM, SDRAM (Synchronous DRAM)

Chipset

Intel 82440LX with built-in PCI-IDE

BIOS

Award BIOS with ISA Plug and Play (PnP) extension, DMI, bootable CD-ROM and power-management features

Power Connector

ATX power supply connector (*Use an ATX power supply with 3.3V power.*)

PCI Bus Master IDE Controller (Ultra DMA/33)

Onboard PCI Bus Master IDE (Ultra DMA/33) controller with two connectors for up to four IDE devices in two channels, supporting faster data transfer rates, enhanced IDE devices such as Tape Backup and CD-ROM drives, PIO Mode 3/4 and Bus Mastering Ultra DMA/33 (*You have to install the Bus Master IDE driver to enable this feature.*)

Super I/O

Onboard super I/O is a Winbond W83977TF that provides:

- Two 16550 UART compatible serial ports
- One parallel port (ECP/EPP compatible)
- One floppy controller (2.88MB compatible)
- One IrDA port
- Keyboard Controller

Mouse Connector

PS/2 type

Keyboard Connector

PS/2 type

USB Connector

2 ports onboard

Win95-shut-off

Allows shut-off control from within Windows 95

Modem-ring-on

Supports PC powering on through an external modem.

DMI BIOS Support

Desktop Management Interface (DMI) allows users to download system hardware-level information such as CPU type, CPU speed, and internal/external frequencies and memory size.

Onboard AGP Slot

The AGP (Accelerated Graphics Port) slot supports AGP compliant VGA cards to achieve rich 3D and video graphics display. AGP is a platform bus specification that enables 3D graphics capabilities including support for z-buffering, alpha blending and faster texture mapping.

Expansion Slots

Four PCI 32-bit slots
Three ISA 16-bit slots
One AGP slot

Form Factor

ATX, 12" x 7.5" (30.7cm x 19cm)

Chapter 3 Hardware Description

This chapter briefly describes each of the major features of the ZP-6L1 motherboard. The layout of the board in Figure 1 shows the location of the key components. The topics covered in this chapter are as follows:

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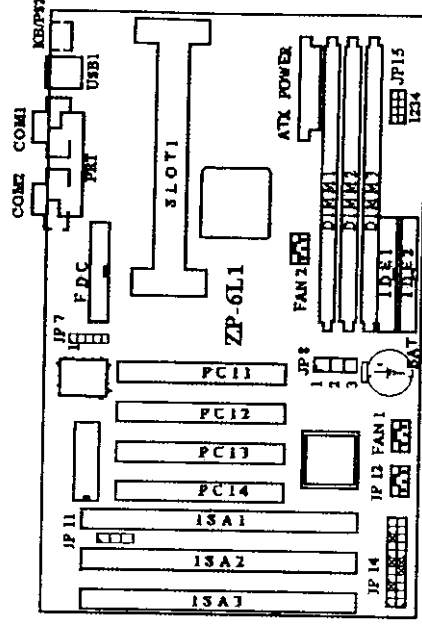


Figure 1: Layout of the ZP-6L1 Motherboard

3.1 Processor

The ZP-6L1 motherboard is designed to take a Pentium II processor running 233MHz/266MHz/300/333/366/400MHz with its SLOT 1 processor connector.

3.2 L2 Cache

The L2 cache is integrated in the Pentium II processor. The private L2 cache bus is not connected to package pins, rather its signals are routed between the two cavities using standard package techniques. The CPU to serve all L1 cache misses, the characteristic of a CPU-cache bus, and is utilized 90-95 percent of the time uses the high-performance bandwidth.

3.3 Main Memory

The ZP-6L1 motherboard supports three 168-pin DIMM (Dual In-line Memory Module) sockets to form a memory configuration from 8MB to 512MB. DIMM modules can be 8MB, 16MB, 32MB, 64MB and 128MB in SDRAM or EDO DRAM. In populating the DIMM sockets, DIMM1, DIMM2 and DIMM3. Refer to the following table on how to configure the memory.

168-pin DIMM (3.3V) SDRAM or EDO DRAM

Bank0 (DIMM1)	Bank1 (DIMM2)	Bank2 (DIMM3)	Total Memory
8MB	----	----	8MB
16MB	----	----	16MB
32MB	----	----	32MB
64MB	----	----	64MB
128MB	----	----	128MB
8MB	8MB	----	16MB
16MB	8MB	----	24MB
32MB	8MB	----	40MB
64MB	8MB	----	72MB
128MB	8MB	----	136MB
8MB	8MB	8MB	24MB
16MB	8MB	8MB	32MB
32MB	8MB	8MB	48MB
64MB	8MB	8MB	80MB
128MB	8MB	8MB	144MB

168-pin DIMM (3.3V) SDRAM or EDO DRAM (continued)

Bank0 (DIMM1)	Bank1 (DIMM2)	Bank2 (DIMM3)	Total Memory
16MB	16MB	----	32MB
32MB	16MB	----	48MB
64MB	16MB	----	80MB
128MB	16MB	----	144MB
16MB	16MB	8MB	40MB
32MB	16MB	8MB	56MB
64MB	16MB	8MB	88MB
128MB	16MB	8MB	152MB
16MB	16MB	16MB	48MB
32MB	16MB	16MB	64MB
64MB	16MB	16MB	96MB
128MB	16MB	16MB	160MB
32MB	32MB	----	64MB
64MB	32MB	----	96MB
64MB	32MB	----	96MB
128MB	32MB	----	160MB
32MB	32MB	8MB	72MB
64MB	32MB	8MB	104MB
128MB	32MB	8MB	168MB
32MB	32MB	16MB	80MB
64MB	32MB	16MB	112MB
128MB	32MB	16MB	176MB
32MB	32MB	32MB	96MB
64MB	32MB	32MB	128MB
128MB	32MB	32MB	192MB
64MB	64MB	----	128MB
128MB	64MB	----	192MB
64MB	64MB	8MB	136MB
128MB	64MB	8MB	200MB
64MB	64MB	16MB	144MB
128MB	64MB	16MB	208MB
64MB	64MB	32MB	160MB
128MB	64MB	32MB	224MB
64MB	64MB	64MB	192MB
128MB	64MB	64MB	256MB
128MB	128MB	128MB	384MB

3.4 BIOS

The BIOS on the ZP-6L1 motherboard provides the standard BIOS functions plus the following additional features:

1. ISA Plug and Play (PnP) Extension

Unlike PCI cards that are Plug and Play, ISA cards require setting jumpers to resolve hardware conflicts. To make a computer system PnP, an ISA PnP standard is established and supported by new operating systems, such as Windows 95. Under Windows 95, the motherboard BIOS must have an ISA PnP extension to support new ISA PnP cards.

2. Power Management

The power management feature provides power savings by slowing down the CPU clock, turning off the monitor screen and stopping the HDD spindle motor. The BIOS fully conforms to ACPI (Advanced Configuration and Power Interface) specification.

3.5 I/O Port Address Map

Each peripheral device in the system is assigned a set of I/O port addresses, which also becomes the identity of the device. There is a total of 1K port address space available. The following table lists the I/O port addresses used on the motherboard.

Address	Device Description
000h - 01Fh	DMA Controller #1
020h - 03Fh	Interrupt Controller #1
040h - 05Fh	Timer
060h - 06Fh	Keyboard Controller
070h - 07Fh	Real Time Clock, NMI
080h - 09Fh	DMA Page Register
0A0h - 0BFh	Interrupt Controller #2
0C0h - 0DFh	DMA Controller #2
0F0h	Clear Math Coprocessor Busy Signal
0F1h	Reset Math Coprocessor
1F0h - 1F7h	IDE Interface
2F8h - 2FFh	Serial Port #2(COM2)
378h - 3FFh	Parallel Port #1(LPT1)
3F0h - 3F7h	Floppy Disk Controller
3F8h - 3FFh	Serial Port #1(COM1)

3.6 DMA Channels

There are seven DMA channels available on the motherboard. The floppy controller uses only DMA2. In the case that ECP mode on the parallel port is utilized, DMA1 or DMA3 will be used.

3.7 Interrupt Request (IRQ) Lines

There are a total of 15 IRQ lines available on the motherboard. Peripheral devices use an interrupt request to notify the CPU for the service required. The following table shows the IRQ lines used by the devices on the motherboard:

Level	Function
IRQ0	System Timer Output
IRQ1	Keyboard
IRQ2	Interrupt Cascade
IRQ8	Real Time Clock
IRQ9	Software Redirected to Int 0Ah
IRQ10	Reserved
IRQ11	Reserved
IRQ12	Reserved
IRQ13	Co-Processor
IRQ14	Primary IDE
IRQ15	Secondary IDE
IRQ3	INTERRUPT
IRQ4	Serial Port #1
IRQ5	INTERRUPT
IRQ6	Floppy Disk Controller
IRQ7	Parallel Port #1

3.8 Onboard PCI-IDE

The PCI-IDE controller is a part of the 82440LX PCIset. It supports PIO mode 3/4 and bus mastering Ultra DMA/33. The peak transfer rate of PIO mode 3/4 can be as high as 17MB/sec. Using HDDs that support Ultra DMA/33, the peak transfer rate can reach 33MB/sec. There are two IDE connectors - primary IDE and secondary IDE. With two devices per connector, up to four IDE devices can be supported.

3.9 Onboard Multi-I/O

The onboard multi-I/O chip, W83977TF, supports a keyboard controller (AMI copyright), two serial ports, one parallel port, one floppy controller and one IrDA port. The serial ports are 16550 UART compatible. The parallel port features high-speed EPP/ECP mode. The floppy controller supports up to 2.88 MB format.

3.10 Onboard AGP Slot

The AGP (Accelerated Graphics Port) slot supports AGP compliant VGA cards to achieve rich 3D and video graphics display. AGP is a platform bus specification that enables 3D graphics capabilities including support for z-buffering, alpha blending and faster texture mapping.

3.11 Optional Winbond W83781D Hardware Monitoring IC

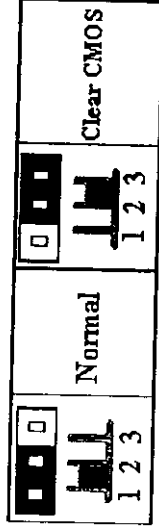
The W83781D is a hardware status monitoring IC that is used to monitor several hardware parameters including power supply voltages, fan speeds, and temperatures, which are very important for a high-end computer system to work stable and properly. The 48-pin LQFP-package IC is fully software compatible with Intel LDCM.

Chapter 4 Configuring the Motherboard

The following sections describe the necessary procedures and proper jumper settings to configure the ZP-6L1 motherboard. For the locations of the jumpers, refer to Figure 2.

- 4.1 CPU Frequency Select: JP15..... 13
- 4.2 Clear CMOS Select: JP8 14

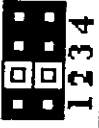
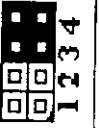
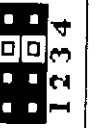
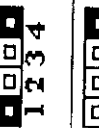
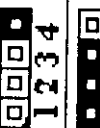
The following examples show the conventions used in this chapter.



4.1 CPU Frequency Select: JP15

JP15 sets the ratio of the bus clock to the CPU internal clock.

Refer to the following table for the correct setting to match the CPU frequency.

Pentium II CPU FREQ.	JP15	NUMBER OF PINS
Pentium II 200MHz	 1 2 3 4	1 2 3 4 ON OFF ON ON
Pentium II 233MHz	 1 2 3 4	1 2 3 4 OFF OFF ON ON
Pentium II 266MHz	 1 2 3 4	1 2 3 4 ON ON OFF ON
Pentium II 300MHz	 1 2 3 4	1 2 3 4 OFF ON OFF ON
Pentium II 333MHz	 1 2 3 4	1 2 3 4 ON OFF OFF ON
Pentium II 366MHz	 1 2 3 4	1 2 3 4 OFF OFF OFF ON
Pentium II 400MHz	 1 2 3 4	1 2 3 4 ON ON ON OFF

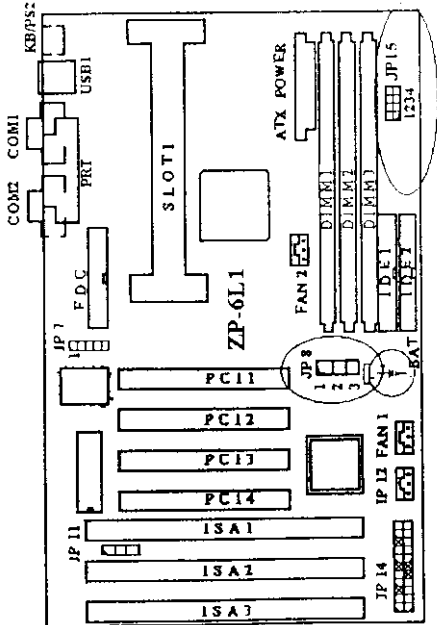


Figure 2: Jumper Location on the ZP-6L1

4.2 Clear CMOS Select: JP8

Use JP8, a 3-pin header, to clear the contents of the CMOS RAM. Do not clear the CMOS RAM unless it is absolutely necessary. You will lose your password, etc.

JP8	Jumper Setting	Function
	pin 1-2 : short	Normal
	pin 2-3 : short	Clear CMOS

NOTE: To clear CMOS, the ATX-power connector should be disconnected from the motherboard.

Chapter 5 Installation

This chapter describes the interface that the ZP-6L1 provides for creating a working system. Refer to Figure 3 for the location of the connectors.

The following items are covered in this chapter:

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5.12 JP14: Front Bezel Connectors	22
5.13 JP12: Wake on LAN Connector	24

5.1 I/O Connectors

The I/O connectors connect the ZP-6L1 to the most common peripherals. To attach cables to these connectors, carefully align Pin 1 of the cables to that of the connectors. Refer to Figure 4 for the location and orientation of the connectors.

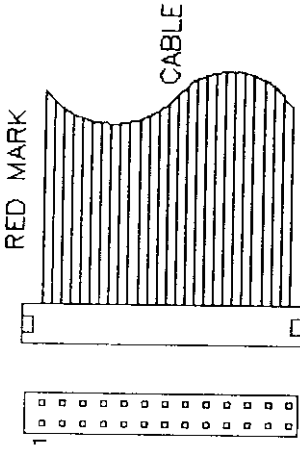
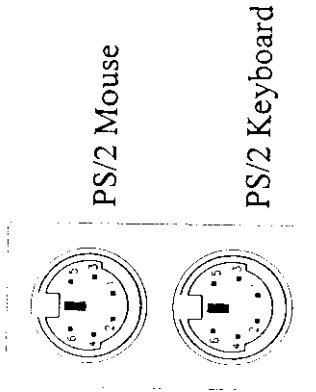


Figure 4: Orientation of the I/O Connector

5.2 J1: PS/2 Keyboard and PS/2 Mouse Connectors



Below are the pin-out assignments of the connectors.

Pin	Keyboard	Signal	Mouse	Signal
1	Keyboard data		1	Mouse data
2	N.C.		2	N.C.
3	GND		3	N.C.
4	5V		4	5V
5	Keyboard clock		5	Mouse Clock
6	N.C.		6	N.C.

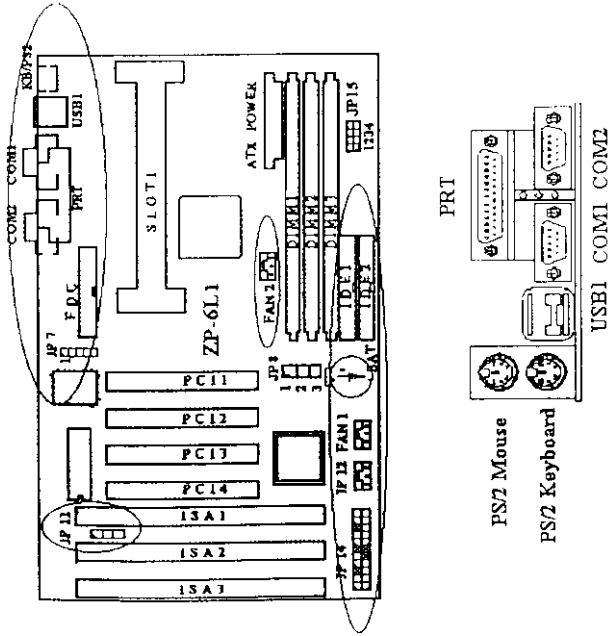


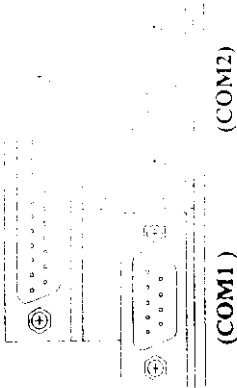
Figure 3: Connector Location on the ZP-6L1

5.3 COM1, COM2: Serial Ports

The onboard serial ports of the ZP-6L1 10, COM1, COM2, are two DB-9 external connectors. The following table shows the pin-out assignments of these connectors.

Pin #	Signal Name	Pin #	Signal Name
1	DCD, Data carrier detect	6	DSR, Data set ready
2	RXD, Receive data	7	RTS, Request to send
3	TXD, Transmit data	8	CTS, Clear to send
4	DTR, Data terminal ready	9	RI, Ring indicator
5	GND, ground	10	Not Used

PRT (Parallel Port)



5.4 PRT: Parallel Port Connector

PRT is a DB-25 external connector as seen in the above figure. The following table describes the pin-out assignments of this connector.

Signal Name	Pin #	Pin #	Signal Name
Line printer strobe	1	14	Auto Feed
PD0, parallel data 0	2	15	Error
PD1, parallel data 1	3	16	Initialize
PD2, parallel data 2	4	17	Select
PD3, parallel data 3	5	18	Ground
PD4, parallel data 4	6	19	Ground
PD5, parallel data 5	7	20	Ground
PD6, parallel data 6	8	21	Ground
PD7, parallel data 7	9	22	Ground
ACK, acknowledge	10	23	Ground
Busy	11	24	Ground
Paper empty	12	25	Ground
Select	13	N/A	N/A

5.5 USB1: USB Connector

USB1 is the standard USB external connector consisting of two ports. USB support allows connections of up to 64 plug and play external peripherals per channel. The following table shows the pin outs of these ports.

Port	Pin	Signal Name
PORT2	1	Vcc
	2	USB-
	3	USB+
PORT1	4	Ground

5.6 FDC: Floppy Drive Connector

FDC of the ZP-6L1 is a 34-pin header and will support up to 2.88MB floppy drives. The following table shows its pin-out assignments.

Signal Name	Pin #	Pin #	Signal Name
Ground	1	2	RM/LC
Ground	3	4	No connect
Ground	5	6	No connect
Ground	7	8	Index
Ground	9	10	Motor enable 0
Ground	11	12	Drive select 1
Ground	13	14	Drive select 0
Ground	15	16	Motor enable 1
Ground	17	18	Direction
Ground	19	20	Step
Ground	21	22	Write data
Ground	23	24	Write gate
Ground	25	26	Track 00
Ground	27	28	Write protect
Ground	29	30	Read data
Ground	31	32	Side 1 select
Ground	33	34	Diskette change



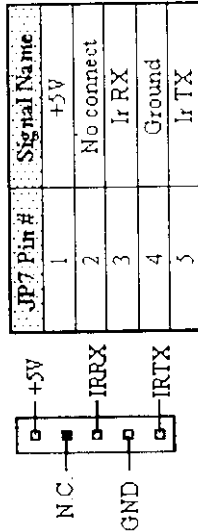
5.7 PW: ATX Power Supply Connector

PW is a 20-pin ATX power supply connector. Refer to the following table for the pin out assignments.

Signal Name	Pin #	Pin #	Signal Name
3.3V	11	1	3.3V
-12V	12	2	3.3V
Ground	13	3	Ground
PS-ON	14	4	+5V
Ground	15	5	Ground
Ground	16	6	+5V
Ground	17	7	Ground
-5V	18	8	Power good
+5V	19	9	5VSB
+5V	20	10	+12V

5.8 JP7: IrDA Connector

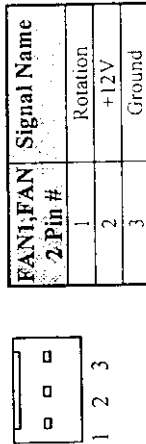
This connector is used for an IrDA connector that supports infrared



wireless communication.

5.9 FAN1,FAN2: CPU Fan Power Connector

FAN1,FAN2 is a 3-pin header for the CPU fan power connector. The fan must be a 12V fan.



5.10 IDE1,IDE2: EIDE Connectors

IDE1: Primary IDE Connector

Signal Name	Pin #	Pin #	Signal Name
Reset IDE	1	2	Ground
Host data 7	3	4	Host data 8
Host data 6	5	6	Host data 9
Host data 5	7	8	Host data 10
Host data 4	9	10	Host data 11
Host data 3	11	12	Host data 12
Host data 2	13	14	Host data 13
Host data 1	15	16	Host data 14
Host data 0	17	18	Host data 15
Ground	19	20	Key
DRQ0	21	22	Ground
Host IOW	23	24	Ground
Host IOR	25	26	Ground
IOCHRDY	27	28	Host ALE
DACK0	29	30	Ground
IRQ14	31	32	No connect
Address 1	33	34	No connect
Address 0	35	36	Address 2
Chip select 0	37	38	Chip select 1
Activity	39	40	Ground



IDE2: Secondary IDE Connector

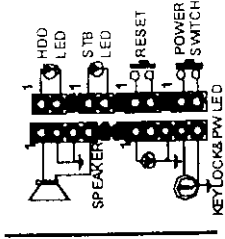
Signal Name	Pin #	Pin #	Signal Name
Reset IDE	1	2	Ground
Host data 7	3	4	Host data 8
Host data 6	5	6	Host data 9
Host data 5	7	8	Host data 10
Host data 4	9	10	Host data 11
Host data 3	11	12	Host data 12
Host data 2	13	14	Host data 13
Host data 1	15	16	Host data 14
Host data 0	17	18	Host data 15
Ground	19	20	Key
DRQ0	21	22	Ground
Host IOW	23	24	Ground
Host IOR	25	26	Ground
IOCHRDY	27	28	Host ALE
DACK1	29	30	Ground
MIRQ0	31	32	No connect
Address 1	33	34	No connect
Address 0	35	36	Address 2
Chip select 0	37	38	Chip select 1
Activity	39	40	Ground



5.12 JPI4: Front Bezel Connectors

There are 6 connectors on the mainboard for switches and indicator lights on the system front panel.

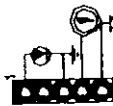
The front bezel of the case has a control panel, which provides light indication of the computer activities and switches to change the computer status. J1 is a 22-pin header that provides interfaces for the following functions.



Pin Assignment
 1. Speaker out
 2. Ground
 3. Ground
 4. +5V

This 4-pin connector connects to the case-mounted speaker

KEYLOCK&PW LED



Pin Assignment
 1. LED Cathode
 2. N.C.
 3. LED Anode (Ground)
 4. Keylock
 5. Ground

This 5-pin connector connects to the case-mounted keylock switch and the power LED. The keylock switch is used to lock the keyboard for security purpose.

HDD LED Connector



Pin Assignment
 1. LED Anode
 2. LED Cathode

This 2-pin connector connects to the case-mounted HDD LED to indicate the hard disk activity.

STB LED Connector



Pin Assignment
 1. LED Cathode
 2. LED Anode (Ground)

This 2-pin connector connects to the case-mounted STB LED which indicates the ATX power supply standby +5V power status

Reset Connector



Pin Assignment
 1. Power Good
 2. Ground

This 2-pin connector connects to the case-mounted reset switch and is used to reboot the system

Power Switch Connector



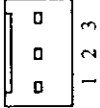
Pin Assignment
 1. Power On/Off
 2. Ground

This 2-pin connector connects to the case-mounted Power switch <CTRL><ALT><+/-> keys to change the speed may be used if necessary.

5.13 JP12: Wake on LAN Connector

JP12 is a 3-pin header for the Wake on LAN function on the motherboard. The following table shows the pin out assignments of this connector. Wake on LAN will function properly only with an ATX power supply with 5VSB that has 200mA.

JP12 Pin #	Signal Name
1	+5VSB
2	Ground
3	Wake on LAN



Chapter 6 BIOS and System Setup

This chapter describes the different settings available in the Award BIOS. The topics covered in this chapter are as follows:

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6.1 BIOS Introduction

The Award BIOS (Basic Input/Output System) installed in your computer system's ROM supports Intel/Cyrix/AMD processors in a standard IBM-AT compatible I/O system. The BIOS provides critical low-level support for standard devices such as disk drives, serial and parallel ports. It also adds virus and password protection as well as special support for detailed fine-tuning of the chipset controlling the entire system.

6.2 BIOS Setup

The Award BIOS provides a Setup utility program for specifying the system configurations and settings. The BIOS ROM of the system stores the Setup utility. When you turn on the computer, the Award BIOS is immediately activated. Pressing the key immediately allows you to enter the Setup utility. If you are a little bit late pressing the key, POST (Power On Self Test) will continue with its test routines, thus preventing you from invoking the Setup. If you still wish to enter Setup, restart the system by pressing the "Reset" button or simultaneously pressing the <Ctrl>, <Alt> and <Delete> keys. You can also restart by turning the system Off and back On again. The following message will appear on the screen:

Press to Enter Setup

In general, you press the arrow keys to highlight items, <Enter> to select the <PgUp> and <PgDn> keys to change entries, <F1> for help and <Esc> to quit.

When you enter the Setup utility, the Main Menu screen will appear on the screen. The Main Menu allows you to select from various setup functions and exit choices.

ROM PCI/ISA BIOS
CMOS SETUP UTILITY
AWARD SOFTWARE, INC.

STANDARD CMOS SETUP	INTEGRATED PERIPHERALS
BIOS FEATURES SETUP	SUPERVISOR PASSWORD
CHIPSET FEATURES SETUP	USER PASSWORD
POWER MANAGEMENT SETUP	IDE HDD AUTO DETECTION
PNP/PCI CONFIGURATION	HDD LOW LEVEL FORMAT
LOAD BIOS DEFAULTS	SAVE & EXIT SETUP
LOAD SETUP DEFAULTS	EXIT WITHOUT SAVING
ESC : Quit	↑ ↓ → ← : Select Item
F10 : Save & Exit Setup	(Shift) F2 : Change Color
Time, Date, Hard Disk Type	

The section below the setup items of the Main Menu displays the control keys for this menu. Another section at the bottom of the Main Menu just below the control keys section displays information on the currently highlighted item in the list.

NOTE: After making and saving system changes with Setup, you find that your computer cannot boot, the Award BIOS supports an override to the CMOS settings that resets your system to its default.

We strongly recommend that you avoid making any changes to the chipset defaults. These defaults have been carefully chosen by both Award and your system manufacturer to provide the absolute maximum performance and reliability.

6.3 Standard CMOS Setup

"Standard CMOS Setup" choice allows you to record some basic hardware configurations in your computer system and set the system clock and error handling. If the motherboard is already installed in a working system, you will not need to select this option. You will need to run the Standard CMOS option, however, if you change your system hardware configurations, the onboard battery fails, or the configuration stored in the CMOS memory was lost or damaged.

ROM PC/ISA BIOS
STANDARD CMOS SETUP
AWARD SOFTWARE, INC.

Date (mm/dd/yy) : Mon, Dec 11 1996
Time (hh:mm:ss) : 00:00:00

HARD DISKS	TYPE	SIZE	CYL	HEAD	PRECOMP	LANDZ	SECTOR	MODE
Primary Master	Auto	0	0	0	0	0	0	Auto
Primary Slave	None	0	0	0	0	0	0	-----
Secondary Master	None	0	0	0	0	0	0	-----
Secondary Slave	None	0	0	0	0	0	0	-----

Drive A	1.44M, 3.5in	
Drive B	None	
Floppy 3 Mode Support	Disabled	
Video	EGA/VGA	
Hard On	All Errors	

	Base Memory	Extended Memory	Other Memory	Total Memory	PU/PD/+/ -	Modify
540K						
15360K						
384K						
16384K						

ESC Quit ↑ ↓ → ← Select Item (Shift) F2 Change Color F1 Help

At the bottom of the menu are the control keys for use on this menu. If you need any help in each item field, you can press the <F1> key. It will display the relevant information to help you. The memory display at the lower right-hand side of the menu is read-only. It will adjust automatically according to the memory changed. The following describes each item of this menu.

Date

The date format is:

Day : Sun to Sat
Month : 1 to 12
Date : 1 to 31
Year : 1994 to 2079

To set the date, highlight the "Date" field and use the PageUp/ PageDown or +/- keys to set the current time.

Time

The time format is: Hour : 00 to 23
Minute : 00 to 59
Second : 00 to 59

To set the time, highlight the "Time" field and use the <PgUp>/ <PgDn> or +/- keys to set the current time.

Primary HDDs / Secondary HDDs

The onboard PCI IDE connectors provide Primary and Secondary channels for connecting up to four IDE hard disks or other IDE devices. Each channel can support up to two hard disks; the first is the "Master" and the second is the "Slave".

To enter the specifications for a hard disk drive, you must select first a "Type". There are 45 predefined types and 4 user definable types are for Enhanced IDE BIOS. Types 1 to 45 are predefined. Type "User" is user-definable. For the Primary Master/Slave as well as Secondary Master/Slave, you can select "Auto" under the TYPE and MODE fields. This will enable auto detection of your IDE drives and CD-ROM drive during POST.

Press <PgUp>/<PgDn> to select a numbered hard disk type or type the number and press the <Enter> key. The hard disk will not work properly if you enter incorrect information for this field. If your hard disk drive type is not matched or listed, you can use Type User to define your own drive type manually. If you select Type User, related information is asked to be entered to the following items.

CYLS : Number of cylinders

HEAD : Number of read/write heads

PRECOMP : Write precompensation

LANDZ : Landing zone

SECTOR : Number of sectors

SIZE : Automatically adjust according to the configuration

MODE (for IDE HDD only) : Auto

Normal (HD < 528MB)

Large (for MS-DOS only)

LBA (HD > 528MB and supports

Logical Block Addressing)

NOTE: The specifications of your drive must match with the drive table. The hard disk will not work properly if you enter incorrect information in these fields. If your hard disk drive type is not matched or listed, you can use Type User to define your own drive type manually.

Drive A / Drive B

These fields identify the types of floppy disk drive A or drive B that has been installed in the computer. The available specifications are:

360KB	1.2MB	720KB	1.44MB	2.88MB
5.25 in.	5.25 in.	3.5 in.	3.5 in.	3.5 in.

Floppy 3 Mode Support

This is the Japanese standard floppy drive. The standard stores 1.2MB in a 3.5-inch diskette. You have four options to choose:

- Disabled
- No 3 mode floppy drive installed. (default)
- Installed 3 mode drive at drive A.
- Installed 3 mode drive at drive B.
- Installed 3 mode drive at drive A and B.

Video

This field selects the type of video display card installed in your system. You can choose the following video display cards:

- EGA/VGA
- For EGA, VGA, SEGA, SVGA
- Or PGA monitor adapters. (Default)
- CGA 40
- Power up in 40 column mode.
- CGA 80
- Power up in 80 column mode.
- MONO
- For Hercules or MDA adapters.

Halt On

This field determines whether the system will halt if an error is detected during power up.

No errors	Whenever the BIOS detect a non-fatal error, the system will stop and you will be prompted.
All errors	The system boot will not be halted for any error that may be detected. (default)
All, But Keyboard	The system boot will not be halted for a keyboard error; it will stop for all other errors.
All, But Diskette	The system boot will not be halted for a disk error; it will stop for all other errors.
All, But Disk/Key	The system boot will not be halted for a keyboard or disk error; it will stop for all others.

6.4 BIOS Features Setup

This section allows you to configure and improve your system and allows you to set up some system features according to your preference.

ROM / PCI ISA BIOS
BIOS FEATURES SETUP
AWARD SOFTWARE, INC.

Virus Warning	: Disabled	Video BIOS Shadow	: Enabled
CPU Internal Cache	: Enabled	C8000-CBFFF Shadow	: Disabled
External Cache	: Enabled	CC000-CFFFF Shadow	: Disabled
Quick Power-On Self Test	: Enabled	D0000-D3FFF Shadow	: Disabled
Boot Sequence	: A, C, SCSI	D4000-D7FFF Shadow	: Disabled
Swap Floppy Drive	: Disabled	D8000-DBFFF Shadow	: Disabled
Boot Up Floppy Drive	: Enabled	DC000-DEFFF Shadow	: Disabled
Boot Up Numlock Status	: On		
Boot Up System Speed	: High		
Gate A20 Option	: Fast		
Typematic Rate Setting	: Disabled		
Typematic Rate (chars/Sec)	: 6		
Typematic Delay (Msec)	: 250		
Security Option	: Setup		
PCI/VGA Palette Snoop	: Disabled		
OS Select For DRAM>64MB	: Non-OS2		

Virus Warning

This item protects the boot sector and partition table of your hard disk against accidental modifications. If an attempt is made, the BIOS will halt the system and display a warning message. If this occurs, you can either allow the operation to continue or run an anti-virus program to locate and remove the problem.

NOTE: Many disk diagnostic programs, which attempt to access the boot sector table, can cause the virus warning. If you will run such a program, disable the Virus Warning feature.

CPU Internal Cache / External Cache

Cache memory is additional memory that is much faster than conventional DRAM (system memory). CPUs from 486-type on up contain internal cache memory, and most, but not all, modern PCs have additional (external) cache memory. When the CPU requests data, the system transfers the requested data from the main DRAM into cache memory, for even faster access by the CPU. These items allow you to enable (speed up memory access) or disable the cache function. By default, these items are *Enabled*.

Quick Power On Self Test

This choice speeds up the Power On Self Test (POST) after you power up the system. If it is set to *Enabled*, BIOS will skip some items. By default, this choice is *Enabled*.

Boot Sequence

This field determines the drive that the system searches first for an operating system. The options are :

A, C, SCSI	D, A, SCSI	SCSI, C, A
C, A, SCSI	E, A, SCSI	C only
C, CDROM, A	F, A, SCSI	LS/ZIP, C
CDROM, C, A	SCSI, A, C	

The default value is *A, C, SCSI*.

Swap Floppy Drive

This item allows you to determine whether to enable Swap Floppy Drive or not. When enabled, the BIOS swaps floppy drive assignments so that Drive A becomes Drive B, and Drive B becomes Drive A. By default, this field is set to *Disabled*.

Boot Up Floppy Seek

When enabled, the BIOS will seek whether or not the floppy drive installed has 40 or 80 tracks. 360K type has 40 tracks while 760K, 1.2M and 1.44M all have 80 tracks. By default, this field is set to *Enabled*.

Boot Up NumLock Status

This allows you to activate the NumLock function after you power up the system. By default, the system boots up with *NumLock* On.

Boot Up System Speed

This has no function and selects the default system speed (*High*).

Gate A20 Option

This field allows you to select how Gate A20 is worked. The Gate A20 is a device used to address memory above 1 MB. By default, this field is set to *Fast*.

Typematic Rate Setting

When disabled, continually holding down a key on your keyboard will generate only one instance. When enabled, you can set the two Typematic controls listed next. By default, this field is set to *Disabled*.

Typematic Rate (Chars/Sec)

When the Typematic rate is enabled, the system registers repeated keystrokes speeds. You can select speed range from 6 to 30 characters per second. By default, this item is set to *6*.

Typematic Delay (Msec)

When the Typematic rate is enabled, this item allows you to set the time interval for displaying the first and second characters. By default, this item is set to *250msec*.

Security Option

This field allows you to limit access to the System and Setup. The default value is *Setup*. When you select *System*, the system prompts for the User Password every time you boot up. When you select *Setup*, the system always boots up and prompts for the Supervisor Password only when the Setup utility is called up.

PCI/VGA Palette Snoop

Some non-standard VGA display cards may not show colors properly. This field allows you to set whether MPEG ISA/VESA VGA Cards can work with PCI/VGA or not. When this field is enabled, a PCI/VGA can work with a MPEG ISA/VESA VGA card. When this field is disabled, a PCI/VGA cannot work with a MPEG ISA/VESA Card.

OS Select for DRAM > 64MB

This option allows the system to access greater than 64MB of DRAM memory when used with OS/2 that depends on certain BIOS calls to access memory. The default setting is *NON-OS/2*.

Video BIOS Shadow

This item allows you to change the Video BIOS location from ROM to RAM. Video Shadow will increase the video speed.

C8000 - CBFFF Shadow/DC000 - DFFFF Shadow

Shadowing a ROM reduces the memory available between 640KB to 1024KB. These fields determine whether optional ROM will be copied to RAM or not.

6.5 Chipset Features Setup

This Setup menu controls the configuration of the motherboard chipset.

ROM BIOS		CPU Speed	
Auto Configuration	: Enabled	CPU Warning Temperature	: 200 MHz
DRAM Speed Selection	: 60ns	Current System Temp.	: 56°C/151°F
MA Wait State	: Slow	Current CPU Temp.	: 27°C/80°F
EDO RAS# To CAS# Delay	: 3	Current Fan1 Speed	: 41°C/105°F
EDO RAS# Precharge Time	: 4	Current Fan2 Speed	: 2045 RPM
EDO DRAM Read Burst	: X333		
EDO DRAM Write Burst	: X333		
DRAM Integrity Mode	: Non-ECC		
CPU to PCI IDE Posting	: Enabled	VCCP (V)	: 1.98 V
System BIOS Cacheable	: Enabled	VCC3 (V)	: 3.45 V
Video RAM Cacheable	: Disabled	+12 V	: 12.46 V
8 Bit I/O Recovery Time	: 4	-5V	: -5.21 V
16 Bit I/O Recovery Time	: 3		
Memory Hole At 1MB	: Disabled	VTT (V)	: 1.50 V
Passive Release	: Enabled		
Delayed Transaction	: Disabled		
AGP Aperture Size (MB)	: 64		
SDRAM RAS-to-CAS Delay	: Slow		
SDRAM CAS Latency Time	: Slow		
	: 3		

ESC: Quit		↑ ↓ → ← : Select Item	
F1: Help		PUP/DOWN: Modify	
F5: Old Values		(Shift) F2: Color	
F6: Load BIOS Defaults			
F7: Load Setup Defaults			

Auto Configuration

This field predefined values for DRAM, cache timing according to CPU type and system clock. When this field is enabled, the predefined items will become read-only.

The default value is Enabled.

Enable	For 50 - 60ns EDO DRAM Timing.
Disable	For slow speed DRAM Timing.

DRAM Cycle Speed Selection

This field sets the DRAM cycle speed. The options are 60ns and 70ns. By default, this field is set to 60ns.

The default value is Slow.

Slow	For normal DRAM operation.
Fast	For Fastest DRAM timing operation.

MA Wait State

This field sets the Wait State of the memory address. The settings are *Fast* and *Slow*. By default, this field is set to *Slow*.

CPU to PCI IDE Posting

When enabled, the CPU to PCI IDE posting cycles are treated as normal I/O write transactions. The default setting is *Enabled*.

System BIOS Cacheable

When enabled, access to the system BIOS ROM addressed at F0000H-FFFFFH are cached, provided that the cache controller is disabled.

SDRAM RAS-to-CAS Delay

This field lets you insert a timing delay between the CAS and RAS strobe signals, used when DRAM is written to, read from, or refreshed. *Fast* gives faster performance; and *Slow* gives more stable performance. The default setting is *Slow*.

Slow	For 67 / 83 MHz SDRAM DIMM module.
Fast	For 100 MHz SDRAM DIMM module.

SDRAM RAS Precharge Time

If an insufficient number of cycles is allowed for the RAS to accumulate its charge before DRAM refresh, the refresh may be incomplete and the DRAM may fail to retain data. *Fast* gives faster performance; and *Slow* gives more stable performance. The default setting is *Slow*.

Slow	For 67 / 83 MHz SDRAM DIMM module.
Fast	For 100 MHz SDRAM DIMM module.

SDRAM CAS Latency Time

When synchronous DRAM is installed, the number of clock cycles of CAS latency depends on the DRAM timing. Do not reset this field from the default value specified by the system designer.

3	For 67 / 83 MHz SDRAM DIMM module.
2	For 100 MHz SDRAM DIMM module.

CPU Type select

This option sets the CPU type.

In this field, the CPU type is indicated like this: CPU type select

You can choose the following settings:

200MHz, 233MHz, 266MHz, 300MHz, 333MHz, 400MHz

It recommended that you choose the specifications of all present CPUs are included in the default settings.

After setting up the parameters and you leave the BIOS SETUP, and you have verified that the system can be booted, do not press the reset button or turn off the power supply. Otherwise the BIOS will not read correctly, the parameters will fail and you must enter "CPU type select" again to set up again.

CPU Warning Temperature

This field sets the threshold temperature at which an alert is sounded through the system's speaker. The onboard thermal sensor to prevent the CPU from overheating monitors the CPU temperature.

Current CPU/Chips Block Temp. / Current System Temp.

Video BIOS Cacheable

When enabled, access to video BIOS addressed at C0000H to C7FFFH are cached, provided that the cache controller is disabled.

Video RAM Cacheable

Selecting *Enabled* allows caching of the video BIOS ROM at C0000h to C7FFFh, resulting in better video performance. However, if any program writes to this memory area, a memory access error may result.

8 Bit I/O Recovery Time

This option specifies the length of the delay (in sysclks) inserted between consecutive 8-bit I/O operations. The settings are 1, 2, 3, 4, 5, 6, 7, or 8. The default setting is 4.

16 Bit I/O Recovery Time

This option specifies the length of the delay (in sysclks) inserted between consecutive 16-bit I/O operations. The settings are 1, 2, 3, 4, 5, 6, 7, or 8. The default setting is 3.

Memory Hole at 15MB Addr.

In order to improve performance, certain space in memory can be reserved for ISA cards. This field allows you to reserve 15MB to 16MB memory address spaces to ISA expansion cards. This makes memory from 15MB and up unavailable to the system. Expansion cards can only access memory up to 16MB. By default, this field is set to *Disabled*.

Passive Release

When enabled, CPU to PCI bus accesses are allowed during passive release. Otherwise, the arbiter only accepts another PCI master access to local DRAM.

Delayed Transaction

The chipset has an embedded 32-bit posted write buffer to support delay transactions cycles. Select *Enabled* to support compliance with PCI specification version 2.1. The default setting is *Disabled*.

AGP Aperture Size (MB)

The field sets aperture size of the graphics. The aperture is a portion of the PCI memory address range dedicated for graphics memory address space. Host cycles that hit the aperture range are forwarded to the AGP without any translation. The options available are 4M, 8M, 16M, 32M, 64M, 128M and 256M. The default setting is 64M.

These read-only fields reflect the functions of the optional hardware thermal sensor that monitors the chip blocks and system temperatures to ensure the system runs stable.

Current Fan 1,2 Speed

These optional and read-only fields show the current speeds in RPM (revolution per minute) for the Fan 1 and Fan 2 as monitored by the optional hardware monitoring IC.

VCCP / VTT / VCC3

These optional and read-only fields show the current voltages in the voltage regulators and power supply as monitored by the optional hardware monitoring IC.

6.6 Power Management Setup

The Power Management Setup allows you to save energy of your system effectively. It will shut down the hard disk and turn off video display after a period of inactivity.

ROM POWISA BIOS (2886JGE)
POWER MANAGEMENT SETUP
AWARD SOFTWARE, INC.

Power Management		** Reload Global Timer Events **	
PM Control by APM	: Disabled	IRQ3 (3-7, 9-16), NMI	: Disabled
Video Off Method	: Yes	Primary IDE 0	: Enabled
Video Off After	: V/H SYNC +Blank	Primary IDE 1	: Enabled
MODEM Use IRQ	: Standby	Secondary IDE 0	: Disabled
Doze Mode	: 3	Secondary IDE 1	: Disabled
Standby Mode	: Disabled	Floppy Disk	: Disabled
Suspend Mode	: Disabled	Serial Port	: Enabled
HDD Power Down	: Disabled	Parallel Port	: Disabled
Throttle Duty Cycle	: 62.5%		
VGA Active Monitor	: Enabled		
IRQ 8 Break Suspend	: Disabled		
Soft-Off by PWR-BTTN	: Instant-Off		
Resume by Ring	: Disabled		
Resume by Alarm	: Disabled		
		ESC : Quit	↑ ↓ → ← : Select Item
		F1 : Help	PUR/DV+/- : Modify
		F5 : Old Values	(Shift) F2 : Color
		F6 : Load BIOS Defaults	
		F7 : Load Setup Defaults	

Power Management

This field allows you to select the type of power saving management modes. There are four selections for Power Management.

- Disabled
 - Min. Power Saving
 - Max. Power Saving
 - User Define
 - No power management
 - Minimum power management
 - Maximum power management. Only available for SL CPU
- Each of the ranges are from 1 min. to 1hr. Except for HDD Power Down which ranges from 1 min. to 15 min. (Default)

NOTE: In order to enable the CPU overheat protection feature, the Power Management field should not be set to Disabled.

PM Control by APM

This field allows you to use the Advanced Power Management device to enhance the Max. Power Saving mode and stop the CPU's internal clock. If the Max. Power Saving is not enabled, this will be preset to NO.

Video Off Method

This field defines the Video Off features. There are three options.

V/H SYNC + Blank Default setting, blank the screen and turn off vertical and horizontal scanning.

DPMS Allows the BIOS to control the video display card if it supports the DPMS feature.

Blank Screen This option only writes blanks to the video buffer.

Video Off After

As the system moves from lesser to greater power-saving modes, select the mode in which you want the monitor to blank.

Modem Use IRQ

This field names the interrupt request (IRQ) line assigned to the modem (if any) on your system. Activity of the selected IRQ always awakens the system. By default, the IRQ is set to 3.

Doze Mode

When enabled, and after the set time of system inactivity, the CPU clock will run at a slower speed while all other devices still operate at full speed.

Standby Mode

After the selected period of system inactivity, the fixed disk drive and the video shut off while all other devices still operate at full speed.

Suspend Mode

When enabled, and after the set time of system inactivity, all devices except the CPU will be shut off.

HDD Power Down

When enabled, and after the set time of system inactivity, the hard disk drive will be powered down while all other devices remain active.

Throttle Duty Cycle

When the system enters Doze mode, the CPU clock runs only part of the time. You may select the percent of time that the clock runs.

VGA Active Monitor

When enabled, any video activity restarts the global timer for Standby mode. The default setting is **Enabled**.

IRQ 8 Break Suspend

You can enable or disable monitoring of IRQ8 (the Real Time Clock) so it does not awaken the system from Suspend mode.

Soft-Off by PWR-BTTN

This field defines the power-off mode when using an ATX power supply. The Instant-Off mode allows powering off immediately upon pressing the power button. In the Delay 4 Sec mode, the system powers off when the power button is pressed for more than four seconds or places the system in a very low-power-usage state, with only enough circuitry receiving power to detect power button activity or Resume by Ring activity (see next field) when pressed for less than 4 seconds. The default value is **Instant-Off**.

Resume by Ring

An input signal on the serial Ring Indicator (RI) line (in other words, an incoming call on the modem) awakens the system from a soft off state.

Resume by Alarm

This allows a computer to be turned on automatically through the timer set in the BIOS to make the system more schedulable. By default, this field is set to **Disabled**.

Reload Global Timer Events

When enabled, an event occurring on each listed device restarts the global timer for Standby mode.

PCI IDE IRQ Map To
This field lets you select PCI IDE IRQ mapping or PC AT (ISA) interrupts. If your system does not have one or two PCI IDE connectors on the system board, select values according to the type of IDE interface(s) installed in your system (PCI or ISA). Standard ISA interrupts for IDE channels are IRQ14 for primary and IRQ15 for secondary.

Used MEM base Addr
Select a base address for the memory area used by any peripheral that requires high memory. The default setting is N/A.

ROM PC/ISA BIOS CHIPSET FEATURES SETUP AWARD SOFTWARE INC.		PCI IDE IRQ Map To	
PNP OS Installed		Primary IDE INT#	PCI-AUTO
Resources Controlled by		Secondary IDE INT#	A
Reset Configuration Data		Used MEM base Addr	B
IRQ-3 assigned to	: No		
IRQ-4 assigned to	: Manual		
IRQ-5 assigned to	: Disabled		
IRQ-7 assigned to	: Legacy ISA		
IRQ-9 assigned to	: Legacy ISA		
IRQ-10 assigned to	: Legacy ISA		
IRQ-11 assigned to	: PC/ISA PnP		
IRQ-12 assigned to	: PC/ISA PnP		
IRQ-14 assigned to	: PC/ISA PnP		
IRQ-15 assigned to	: PC/ISA PnP		
DMA-0 assigned to	: PC/ISA PnP		
DMA-1 assigned to	: PC/ISA PnP		
DMA-3 assigned to	: PC/ISA PnP		
DMA-5 assigned to	: PC/ISA PnP		
DMA-6 assigned to	: PC/ISA PnP		
DMA-7 assigned to	: PC/ISA PnP		

PNP OS Installed
This field allows you to specify if the operating system installed in your system is plug and play aware.
NOTE: Operating systems such as DOS, OS/2, and Windows 3.x do not use PnP

Resources Controlled by
This PnP BIOS can configure all of the boot and compatible devices automatically. However, this capability needs you to use a PnP operating system such as Windows 95. The default value is *Manual*.

Reset Configuration Data
This field allows you to determine whether to reset the configuration data or not. The default value is *Disabled*.

IRQ3/4/5/9/10/11/12/14/15, DMA0/1/3/5/6/7 assigned to
These fields allow you to determine the IRQ/DMA assigned to the ISA bus and is not available to any PCI slot.

6.7 PNP/PCI Configuration

This option configures the PCI bus system. All PCI bus systems on the system use INT#, thus all installed PCI cards must be set to this value.

6.8 Load BIOS Defaults

This option allows you to load the troubleshooting default values permanently stored in the BIOS ROM. These default settings are non-optimal and disable all high-performance features.

ROM PCI/ISA BIOS CMOS SETUP UTILITY AWARD SOFTWARE, INC.	
STANDARD CMOS SETUP	INTEGRATED PERIPHERALS
BIOS FEATURES SETUP	SUPERVISOR PASSWORD
CHIPSET FEATURES SETUP	USER PASSWORD
POWER MANAGE	DETECTION
PNP/PCI CONFIG	FORMAT
LOAD BIOS DEF	UP
LOAD SETUP DEF	LOAD WITHOUT SAVING
ESC : Quit	
F10 : Save & Exit Setup	
Load BIOS Defaults except Standard CMOS Setup	

To load BIOS defaults value to CMOS SRAM, enter "Y". If not, enter "N".

6.9 Load Setup Defaults

This option allows you to load the default values to your system configuration. These default settings are optimal and enable all high performance features.

ROM PCI/ISA BIOS CMOS SETUP UTILITY AWARD SOFTWARE, INC.	
STANDARD CMOS SETUP	INTEGRATED PERIPHERALS
BIOS FEATURES SETUP	SUPERVISOR PASSWORD
CHIPSET FEATURES SETUP	USER PASSWORD
POWER MANAGE	DETECTION
PNP/PCI CONFIG	FORMAT
LOAD BIOS DEF	UP
LOAD SETUP DEF	LOAD WITHOUT SAVING
ESC : Quit	
F10 : Save & Exit Setup	
Load BIOS Defaults except Standard CMOS Setup	

To load SETUP defaults value to CMOS SRAM, enter "Y". If not, enter "N".

6.10 Integrated Peripherals

This option sets your hard disk configuration, mode and port.

ROM PCI/ISA BIOS CHIPSET FEATURES SETUP AWARD SOFTWARE, INC.	
IDE HDD Block Mode	Onboard Parallel Port
IDE Primary Master PIO	Parallel Port Mode
IDE Primary Slave PIO	
IDE Secondary Master PIO	
IDE Secondary Slave PIO	
IDE Primary Master UDMA	
IDE Primary Slave UDMA	
IDE Secondary Master UDMA	
IDE Secondary Slave UDMA	
On-Chip Primary PCI IDE	
On-Chip Secondary PCI IDE	
USB Keyboard Support	
Onboard FDC Controller	ESC : Quit
Onboard Serial Port 1	F1 : Help
Onboard Serial Port 2	F5 : Old Values
UART Mode Select	F6 : Load BIOS Defaults
	F7 : Load Setup Defaults

IDE HDD Block Mode

This field allows your hard disk controller to use the fast block mode to transfer data to and from your hard disk drive.

IDE Primary/Secondary Master/Slave PIO

These fields allow your system hard disk controller to work faster. Rather than have the BIOS issue a series of commands that transfer to or from the disk drive, PIO (Programmed Input/Output) allows the BIOS to communicate with the controller and CPU directly.

The system supports five modes, numbered from 0 (default) to 4, which primarily differ in timing. When Auto is selected, the BIOS will select the best available mode.

IDE Primary/Secondary Master/Slave UDMA

These fields allow your system to improve disk I/O throughput to 33Mb/sec with the Ultra DMA/33 feature. The options are *Auto* and *Disabled*.

On-Chip Primary/Secondary PCI IDE

The integrated peripheral controller contains an IDE interface with support for two IDE channels. Select *Enabled* to activate each channel separately.

USB Keyboard Support

Select *Enabled* if your system contains a Universal Serial Bus (USB) controller and you have a USB keyboard.

Onboard FDC Controller

Select *Enabled* if your system has a floppy disk controller (FDC) installed on the system board and you wish to use it. If you install an add-in FDC or the system has no floppy drive, select *Disabled* in this field. This option allows you to select the onboard FDD port.

Onboard Serial/Parallel Port

These fields allow you to select the onboard serial and parallel ports and their addresses. The default value for these ports is:

- Serial Port 1 3F8/IRQ4
- Serial Port 2 2F8/IRQ3
- Parallel Port 378H/IRQ7

UART Mode Select

This field determines the UART mode in your computer. The settings are *Normal*, *IrDA* and *ASKIR*. The default value is *Normal*.

Parallel Port Mode

This field allows you to determine parallel port mode function.

- SPP Normal Printer Port
- EPP Enhanced Parallel Port
- ECP Extended Capabilities Port

6.11 Supervisor / User Password

These two options set the system password. Supervisor Password sets a password that will be used to protect the system and Setup utility. User Password sets a password that will be used exclusively on the system. To specify a password, highlight the type you want and press <Enter>. The Enter Password: message prompts on the screen. Type the password, up to eight characters in length, and press <Enter>. The system confirms your password by asking you to type it again. After setting a password, the screen automatically returns to the main screen.

To disable a password, just press the <Enter> key when you are prompted to enter the password. A message will confirm the password to be disabled. Once the password is disabled, the system will boot and you can enter Setup freely.

ROM PCI/ISA BIOS
CMOS SETUP UTILITY
AWARD SOFTWARE, INC.

STANDARD CMOS SETUP	INTEGRATED PERIPHERALS
BIOS FEATURES SETUP	SUPERVISOR PASSWORD
CHIPSET FEATURES SETUP	USER PASSWORD
POWER MANAGEMENT	BIOS PASSWORD DETECTION
PNP/PCI CONFIGURATION	FORMAT
LOAD BIOS DEFAULTS	SETUP
LOAD SETUP DEFAULTS	SAVING
ESC : Quit	↑ ↓ → ← : Select Item
F10 : Save & Exit Setup	(Shift) F2 : Change Color
Change / Set / Disable Password	

Enter Password:

6.12 IDE HDD Auto Detection

This option detects the parameters of an IDE hard disk drive, and automatically enters them into the Standard CMOS Setup screen.

ROM PCI/ISA BIOS STANDARD CMOS SETUP AWARD SOFTWARE, INC.									
HARD DISKS	TYPE	SIZE	HEAD	PRECOMP	LANDZ	SECTOR	MODE		
Primary Master									
Select Primary Master Option (N=SKIP): N OPTIONS TYPE SIZE CYLS HEAD PRECOMP LANDZ SECTOR MODE 1 (V) 0 0 0 0 0 0 0 0 NORMAL									
NOTE: Some OSes (like SCO-UNIX) must use "NORMAL" for installation									
ESC: SKIP									

Up to four IDE drives can be detected, with parameters for each appearing in sequence inside a box. To accept the displayed entries, presses the "Y" key; to skip to the next drive, press the "N" key. If you accept the values, the parameters will appear listed beside the drive letter on the screen.

6.13 HDD Low Level Format

A professional should only use this option. Low-level formatting can cause irreparable damage to your hard disk. The procedures include selecting the drive you want to low-level format, determining the bad tracks, and proceeding with pre-formatting.

6.14 Save & Exit Setup

This option allows you to determine whether to accept the modifications or not. If you type "Y", you will quit the setup utility and save all changes into the CMOS memory. If you type "N", you will return to Setup utility.

ROM PCI/ISA BIOS CMOS SETUP UTILITY AWARD SOFTWARE, INC.	
STANDARD CMOS SETUP	INTEGRATED PERIPHERALS
BIOS FEATURES SETUP	SUPERVISOR PASSWORD
CHIPSET FEATURES SETUP	USER PASSWORD
POWER MANAGE	ETECTION
PNP/PCI CONF	FORMAT
LOAD BIOS DE	UP
LOAD SETUP DEFAULTS	EXIT WITHOUT SAVING
ESC : Quit	↑ ↓ → ← : Select Item
F10 : Save & Exit Setup	(Shift) F2 : Change Color
Save Data to CMOS & Exit Setup	

6.15 Exit Without Saving

Select this option to exit the Setup utility without saving the changes you have made in this session. Typing "Y" will quit the Setup utility without saving the modifications. Typing "N" will return you to Setup utility.

ROM PCI/ISA BIOS CMOS SETUP UTILITY AWARD SOFTWARE, INC.	
STANDARD CMOS SETUP	INTEGRATED PERIPHERALS
BIOS FEATURES SETUP	SUPERVISOR PASSWORD
CHIPSET FEATURES SETUP	USER PASSWORD
POWER MANAGE	ETECTION
PNP/PCI CONF	FORMAT
LOAD BIOS DE	UP
LOAD SETUP DEFAULTS	EXIT WITHOUT SAVING
ESC : Quit	↑ ↓ → ← : Select Item
F10 : Save & Exit Setup	(Shift) F2 : Change Color
Abandon all Data & Exit Setup	

Appendix

A. Slot 1 Retention Mechanism

1. To install the Slot 1 mechanism, fasten it with the four screws from underneath the motherboard. Refer to the drawing below.
2. To release the CPU module from the retention mechanism, press the two sides of the module. Refer to the drawing below.

