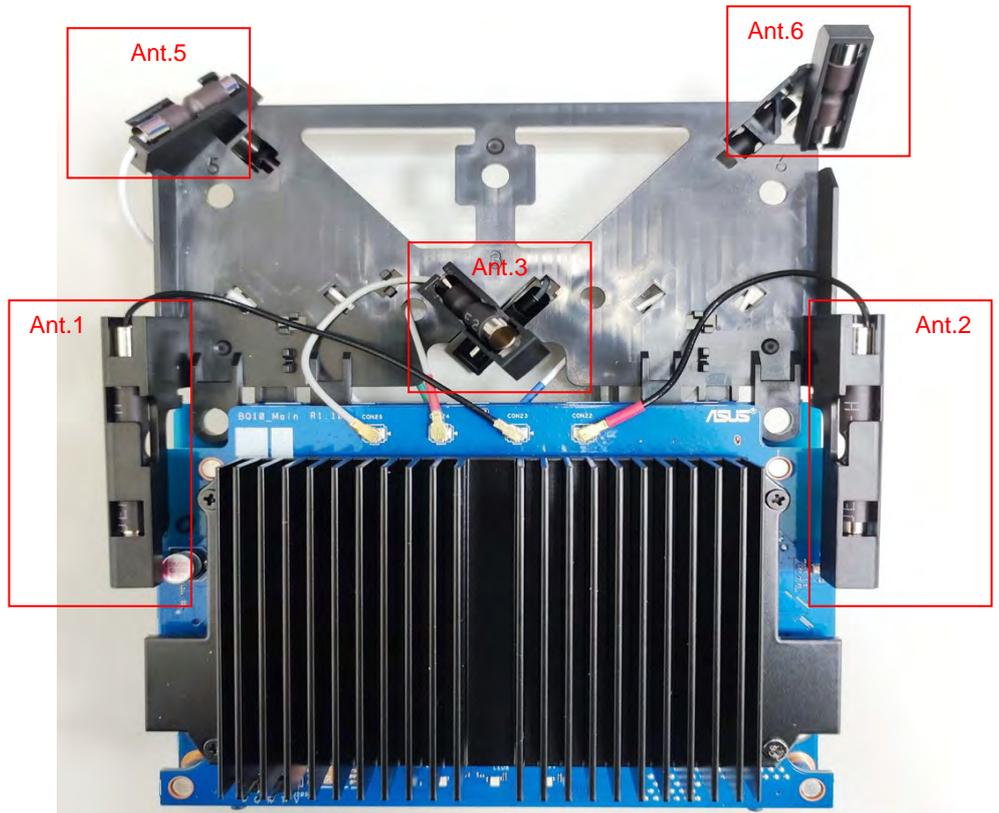
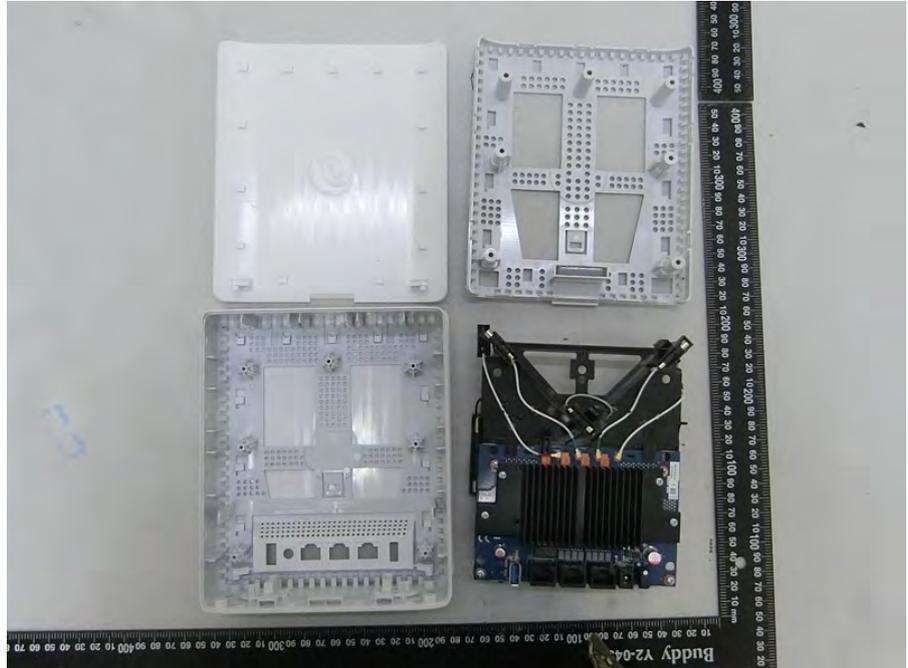
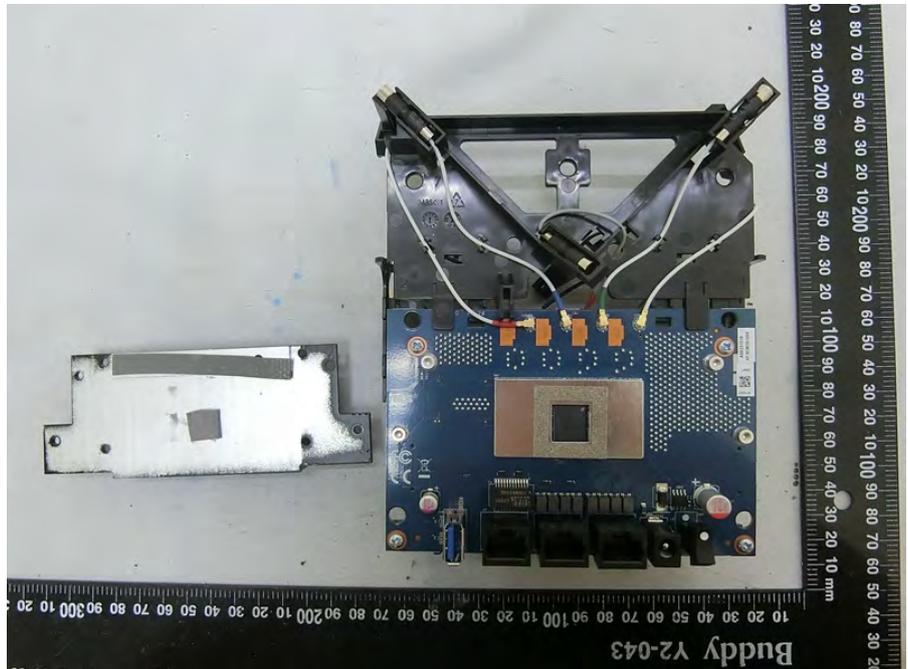
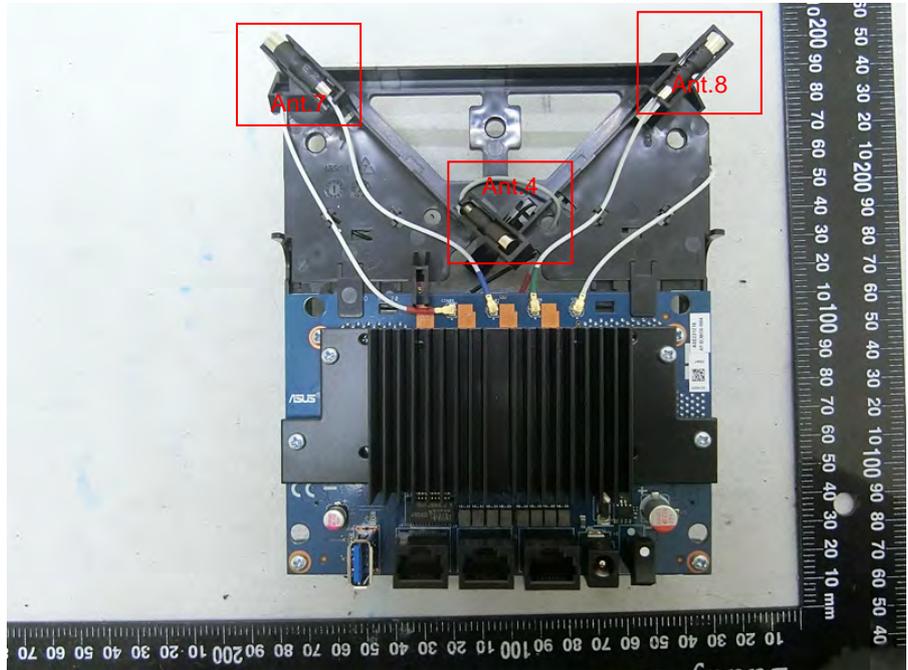
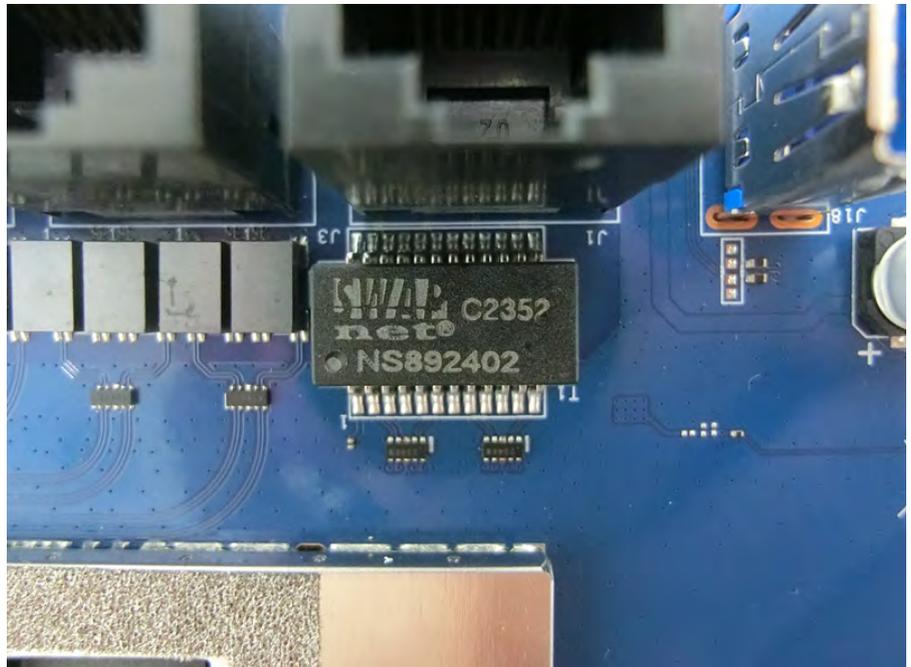
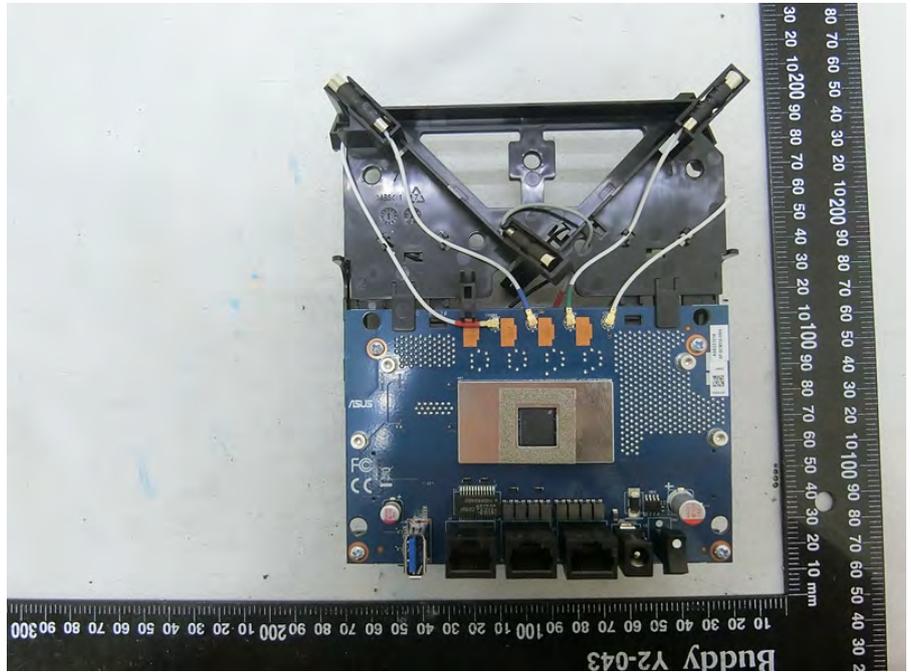
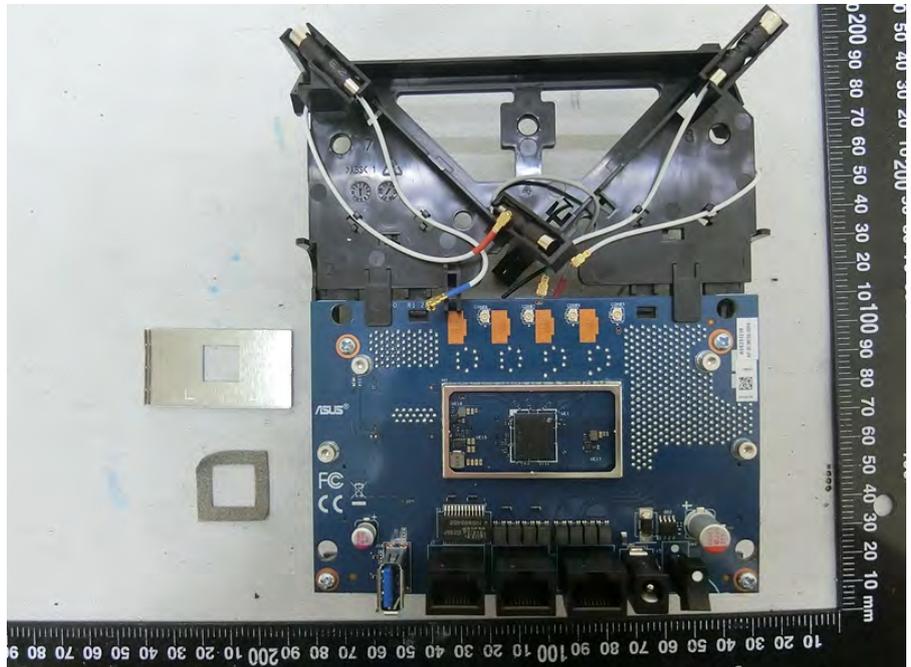
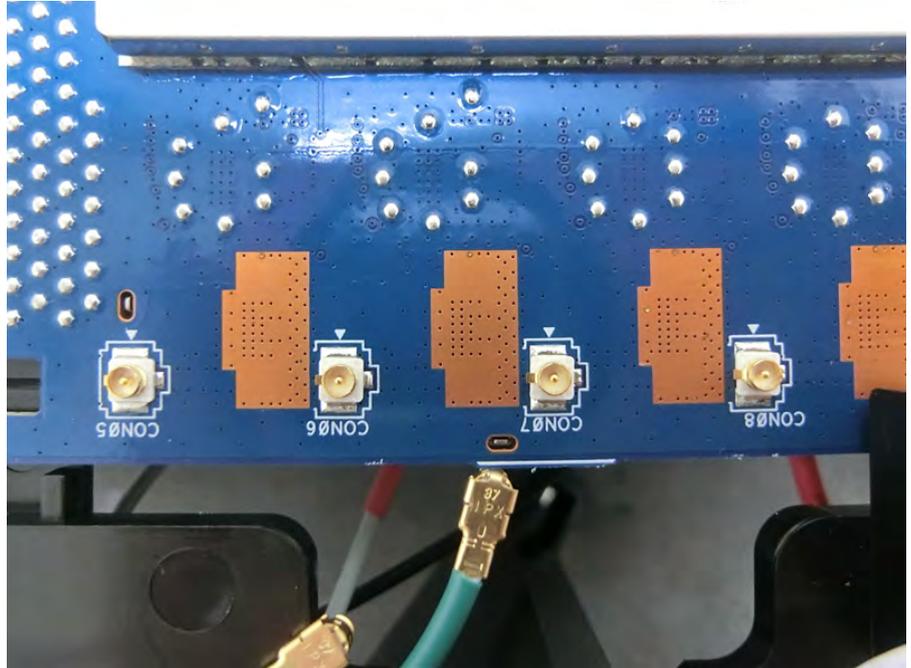


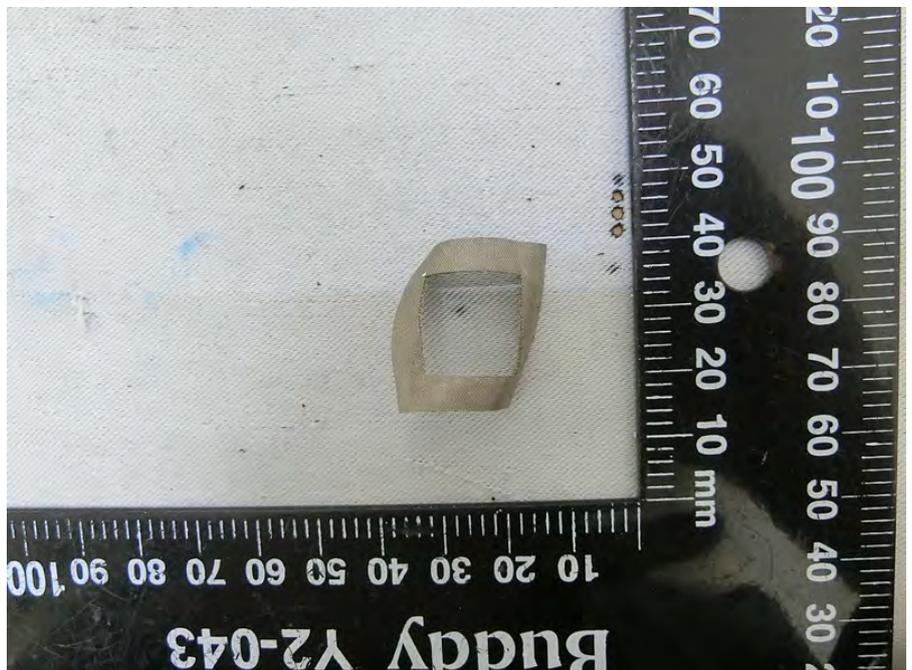
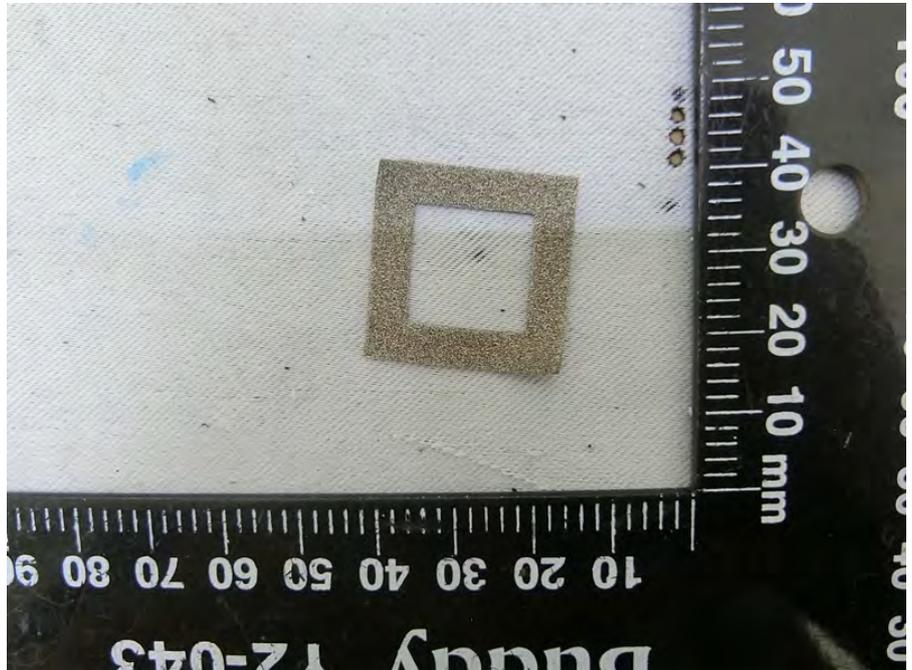
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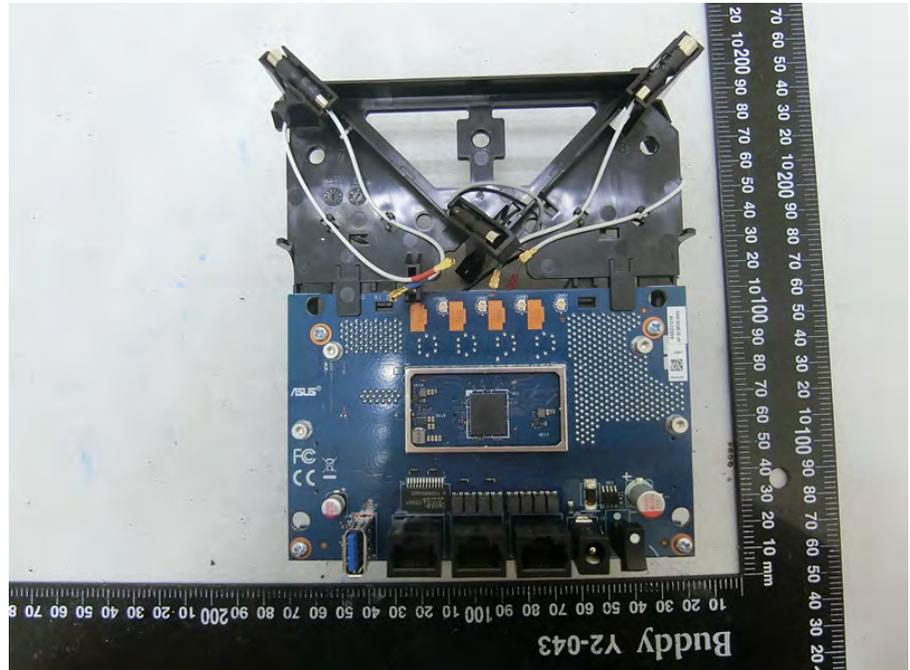


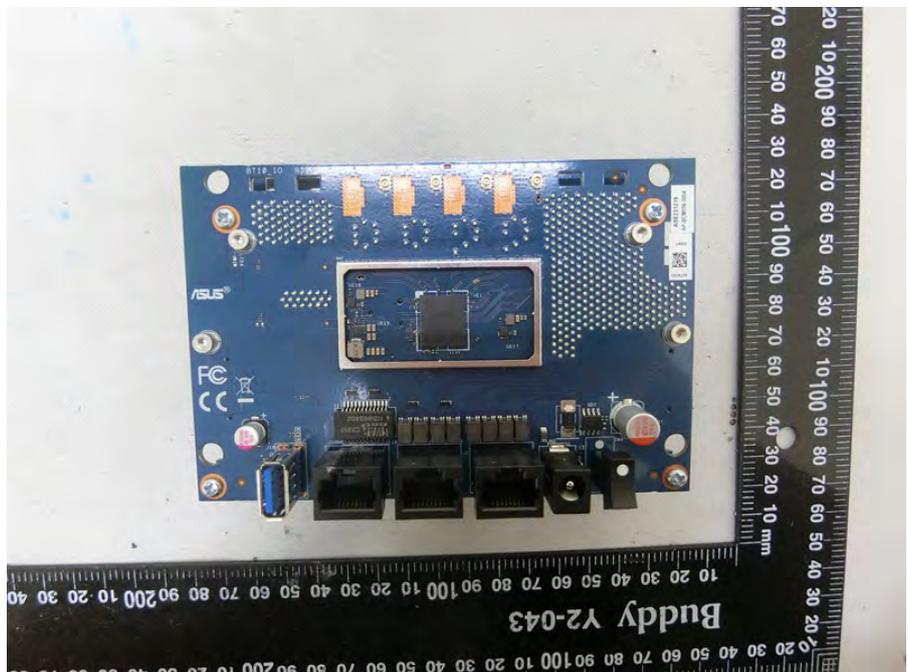
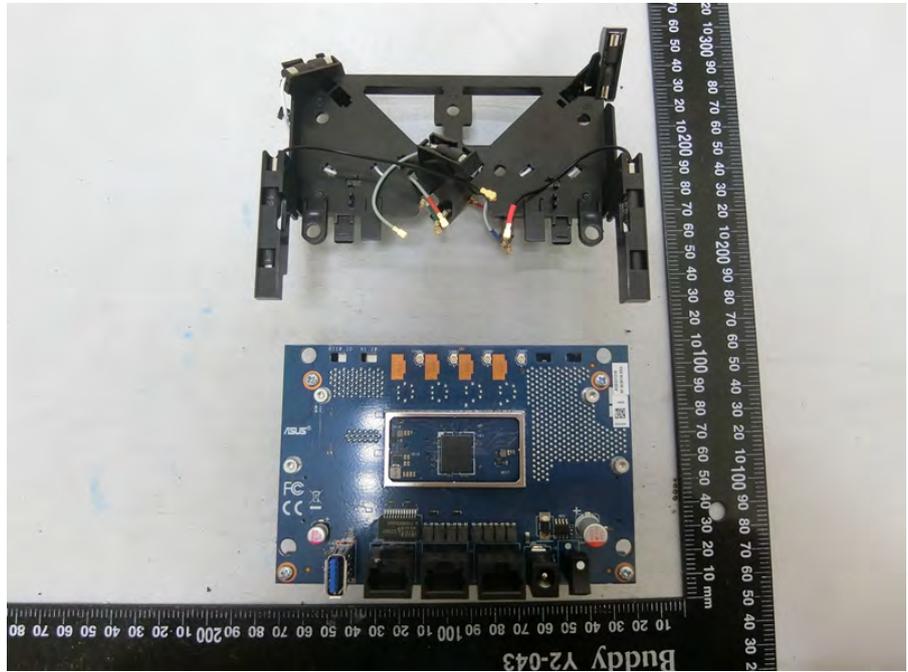


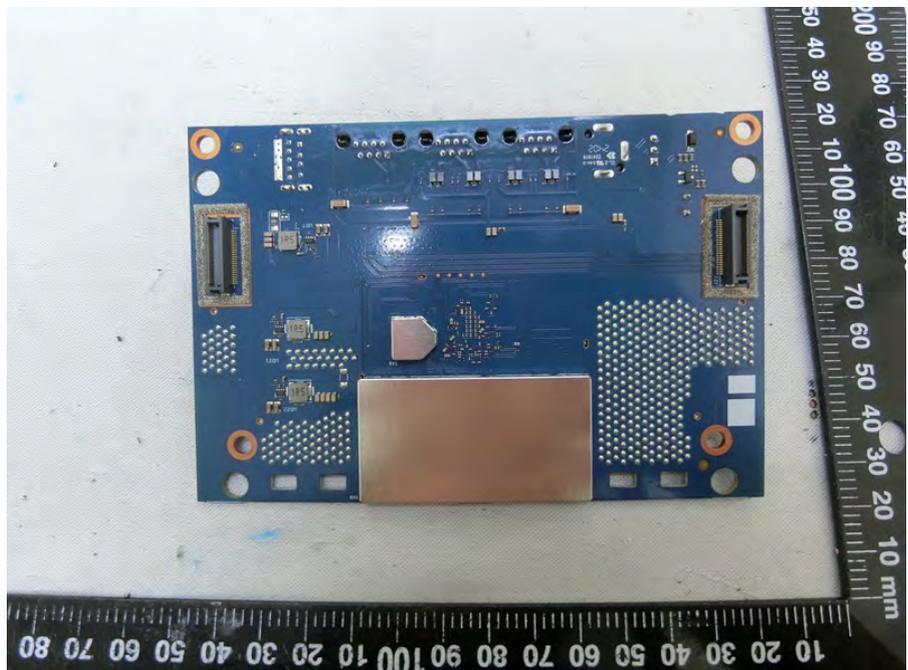
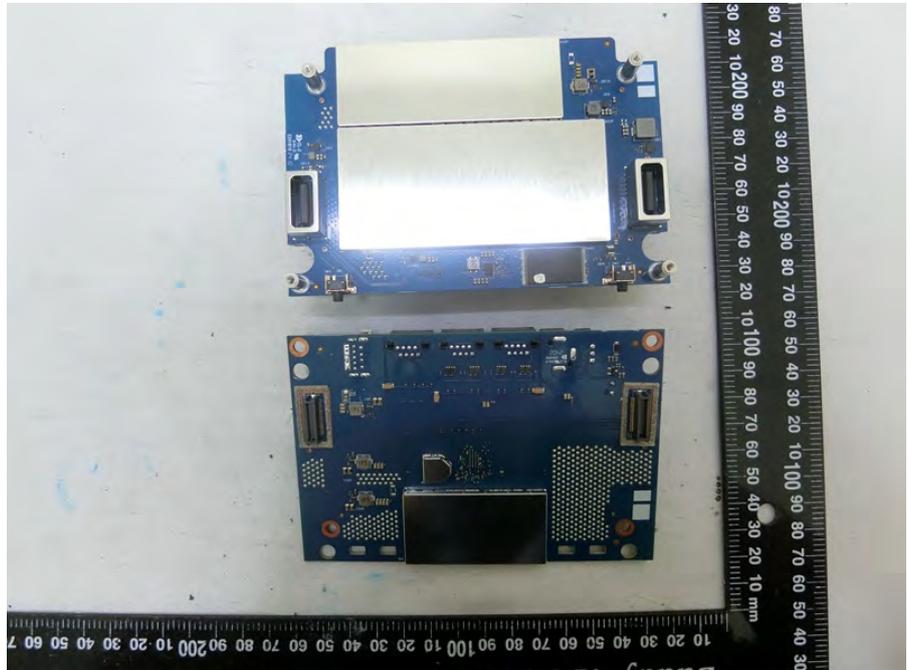


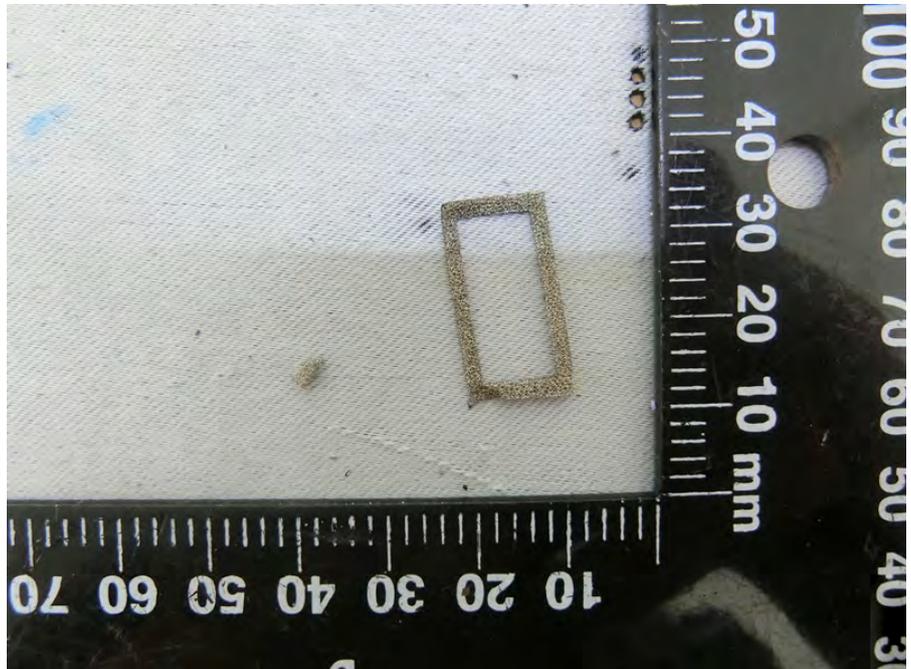
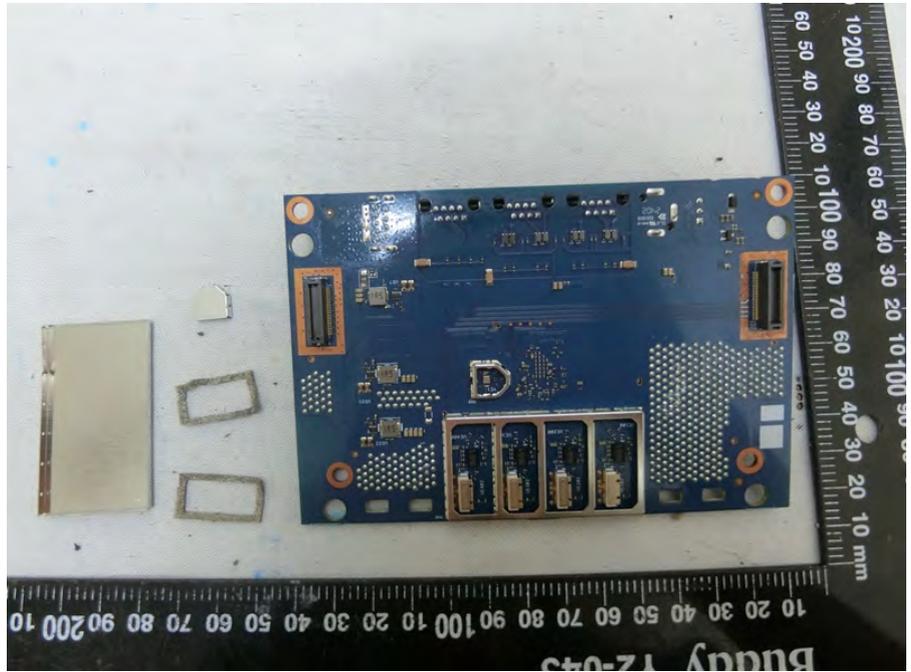


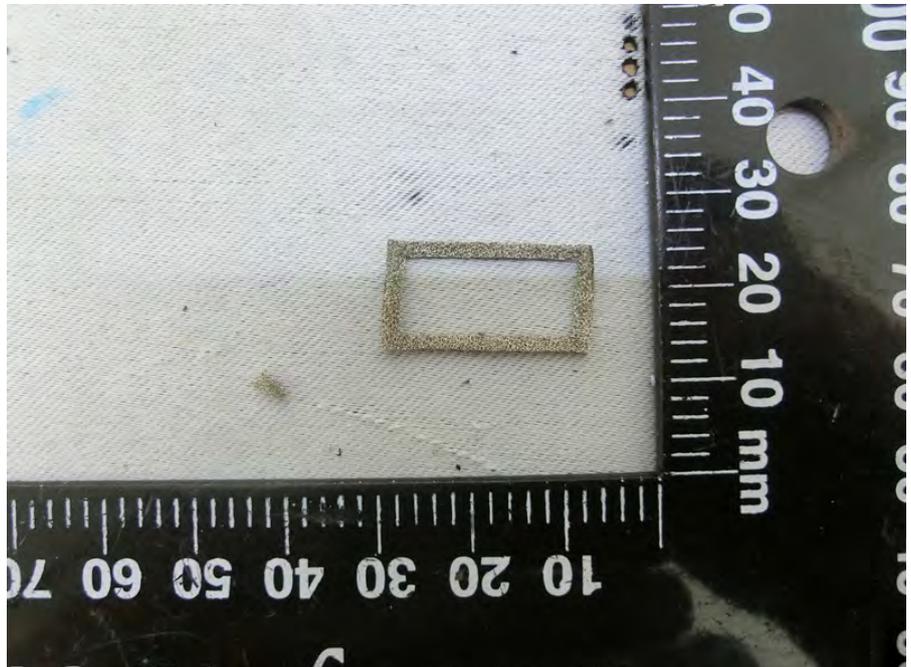
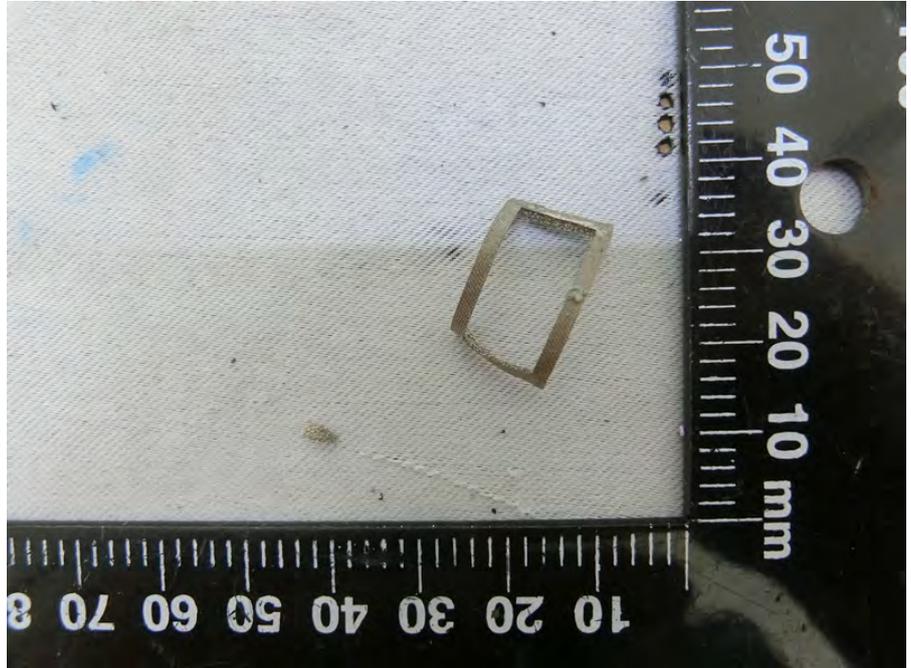


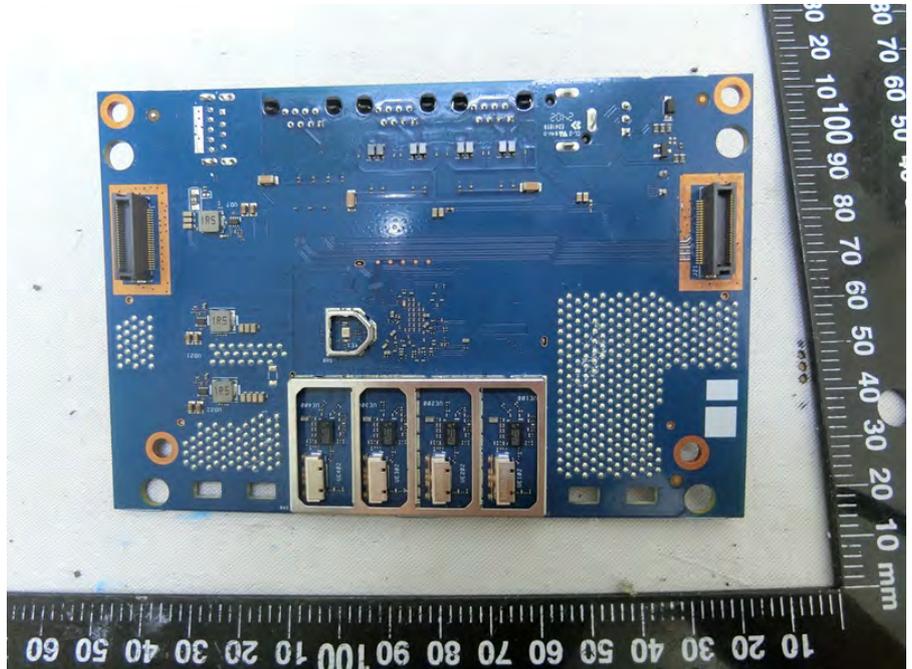
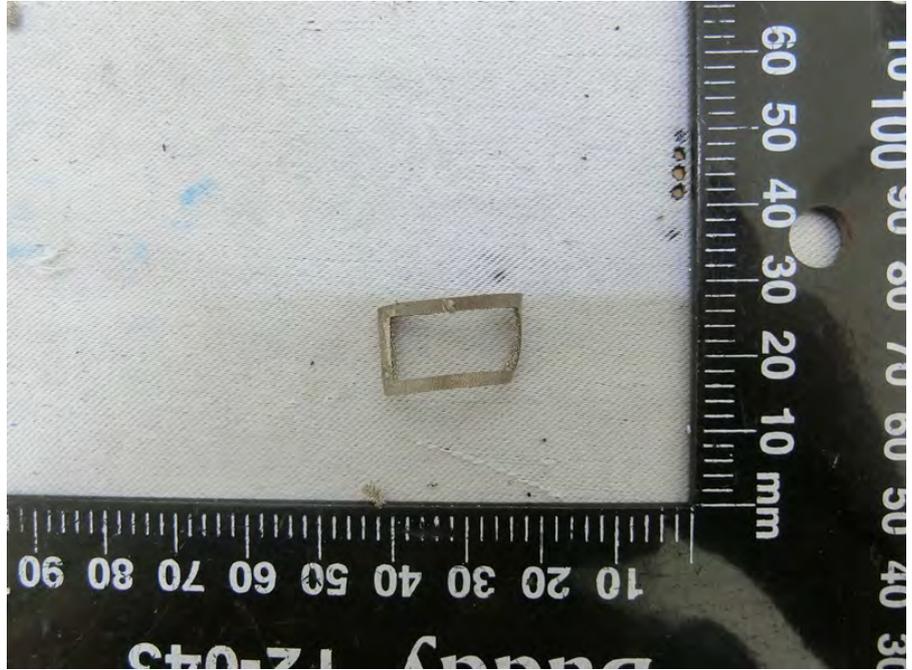


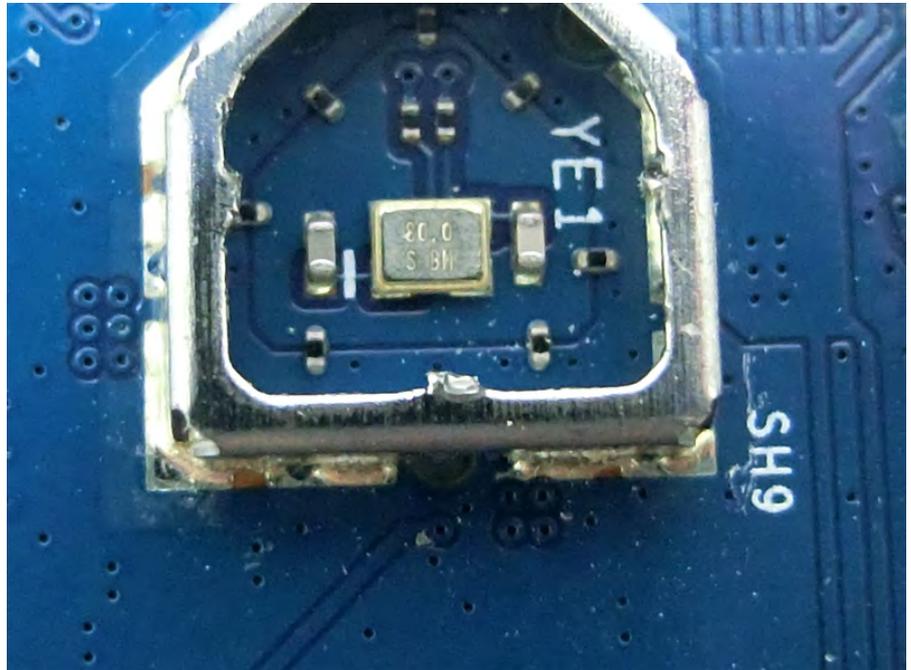




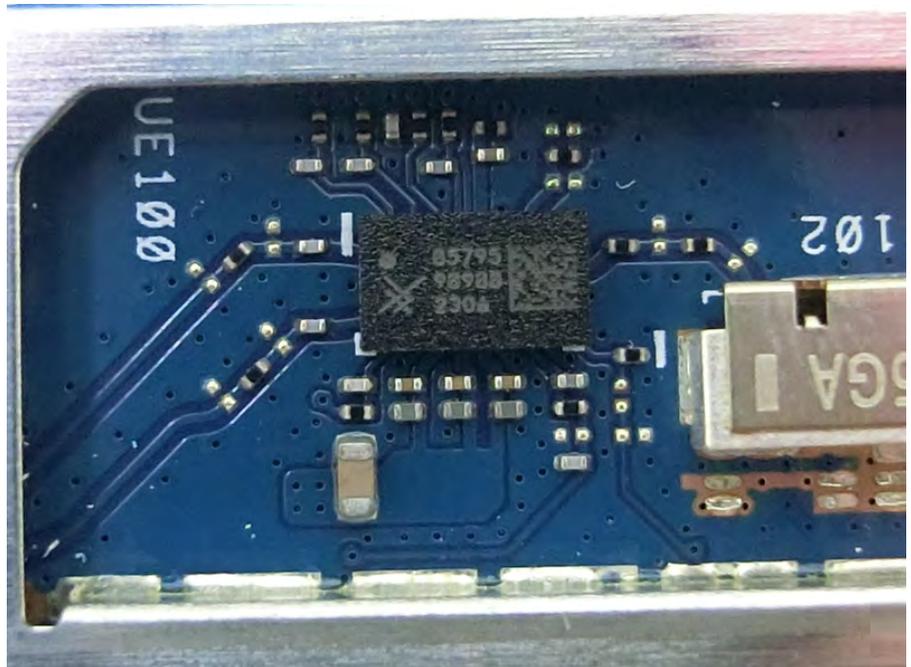


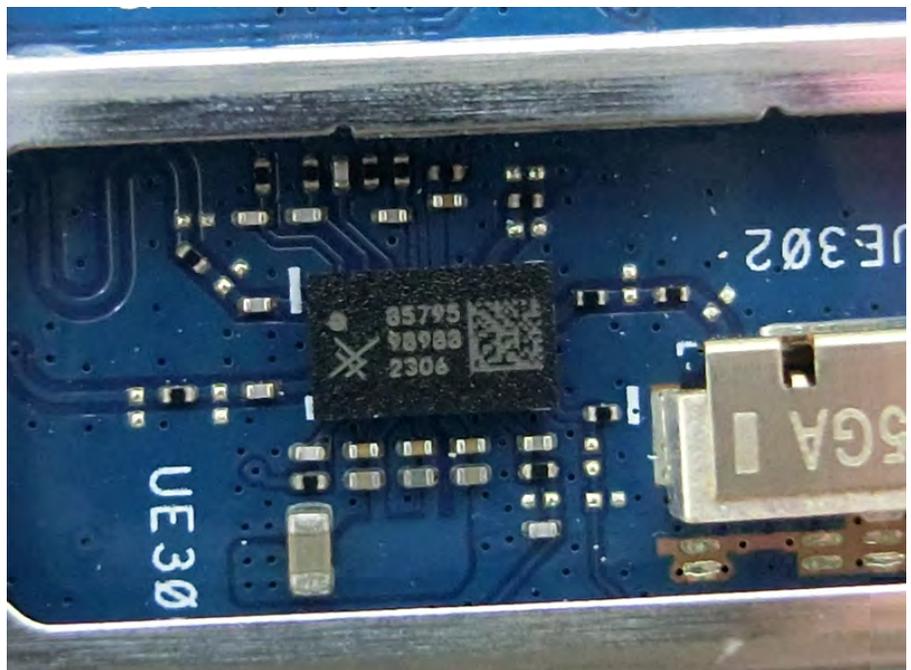
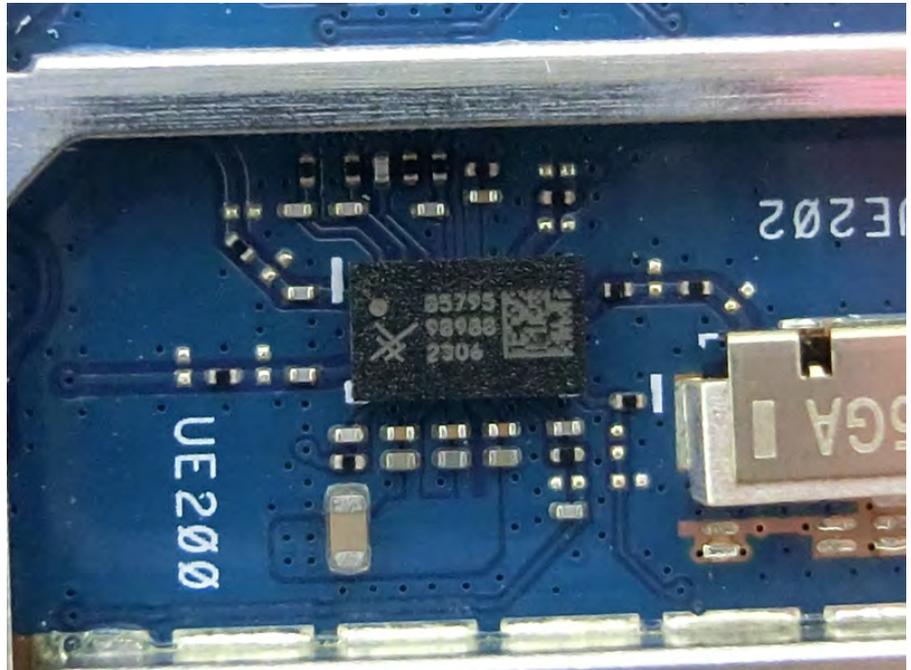


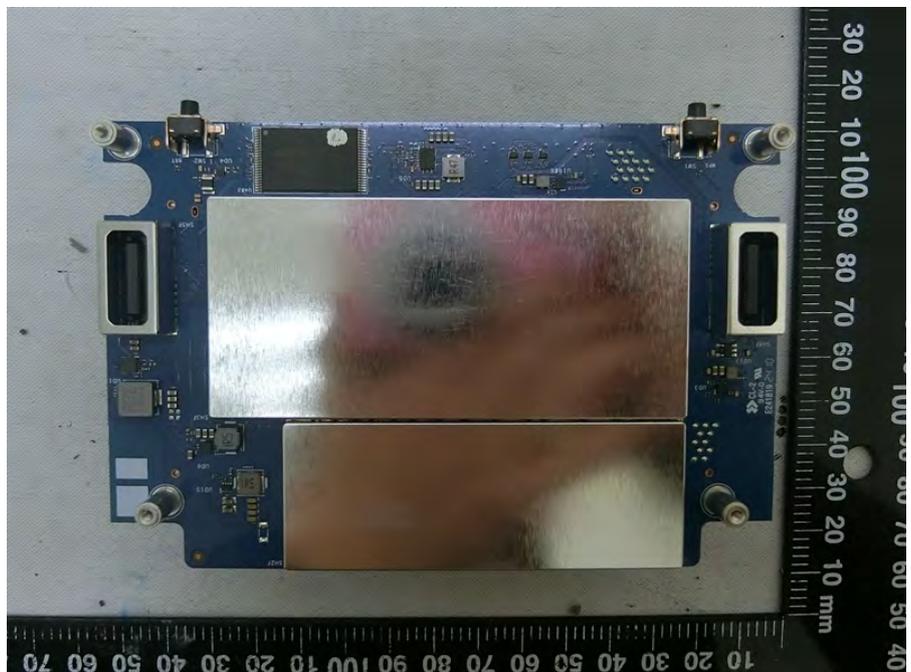
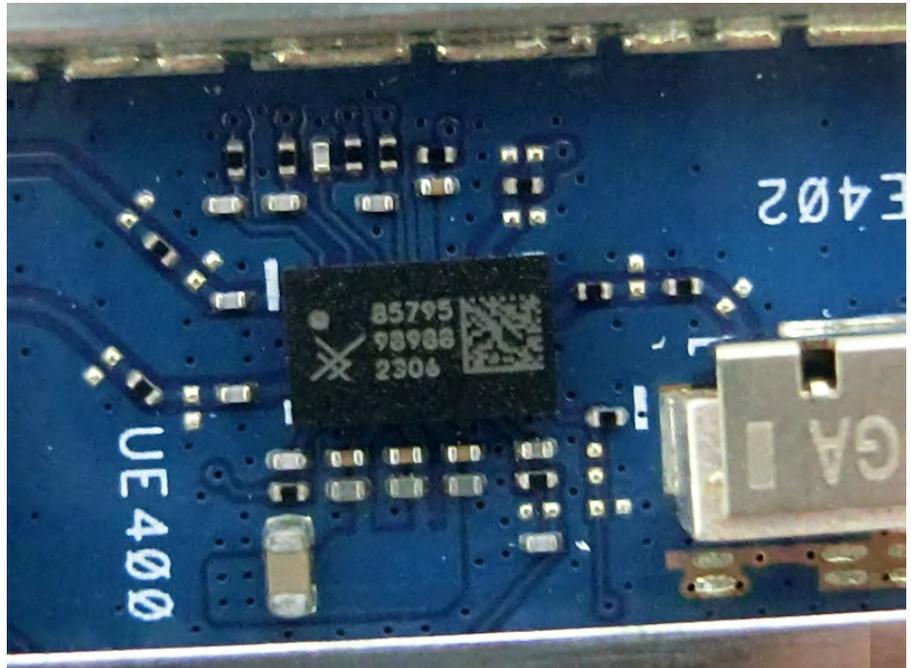


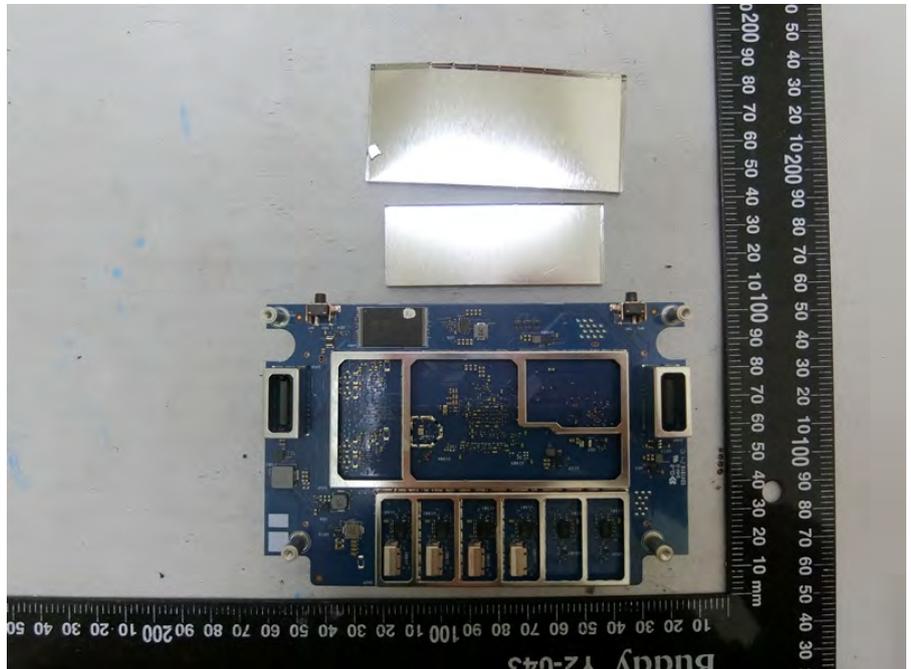


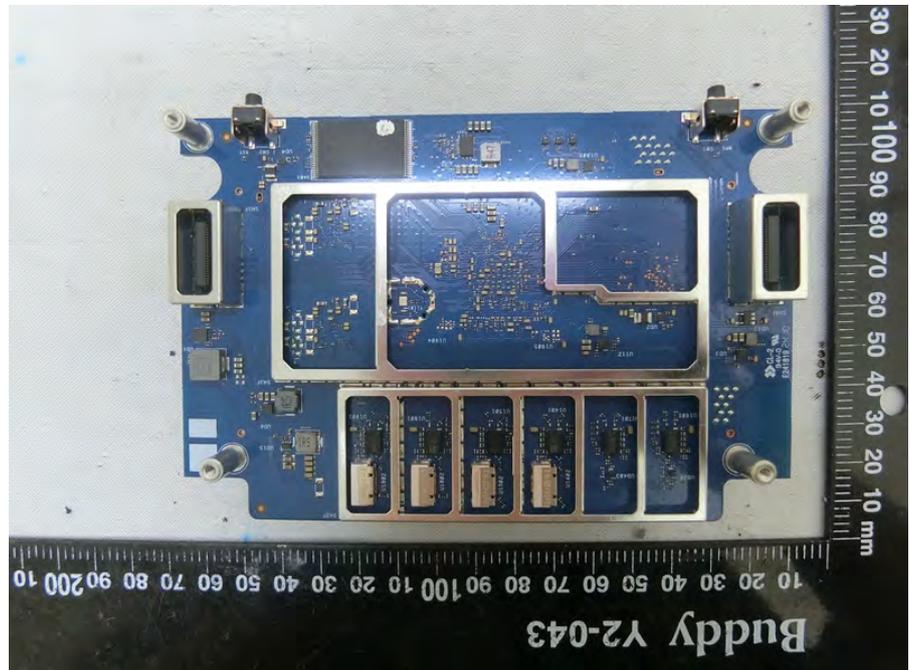


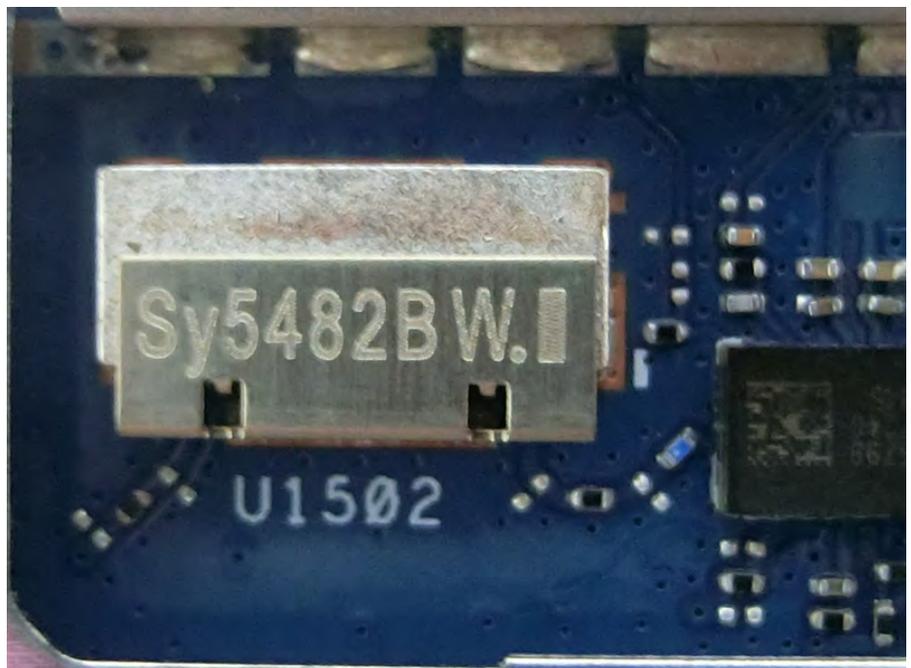


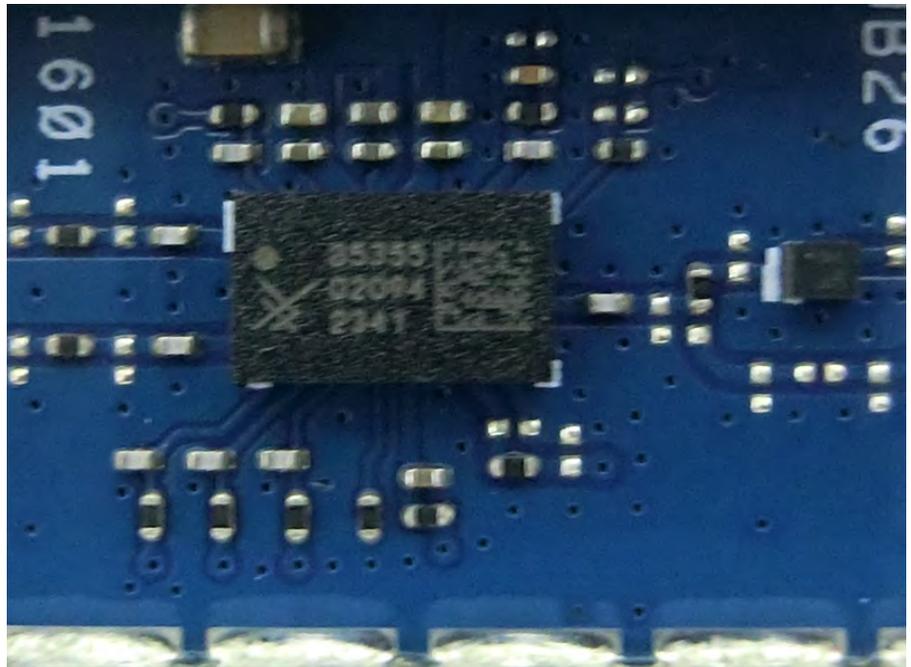
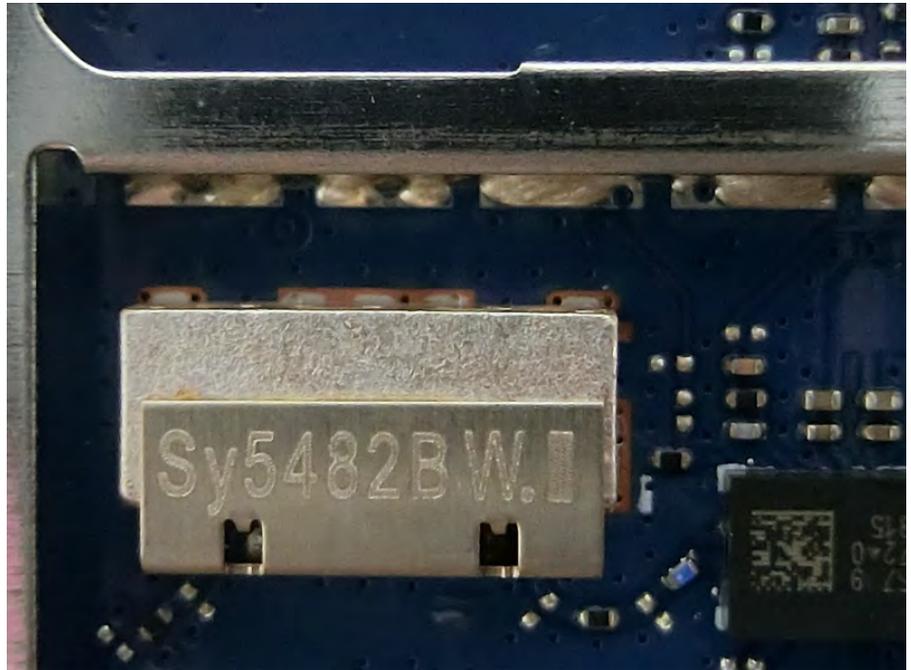


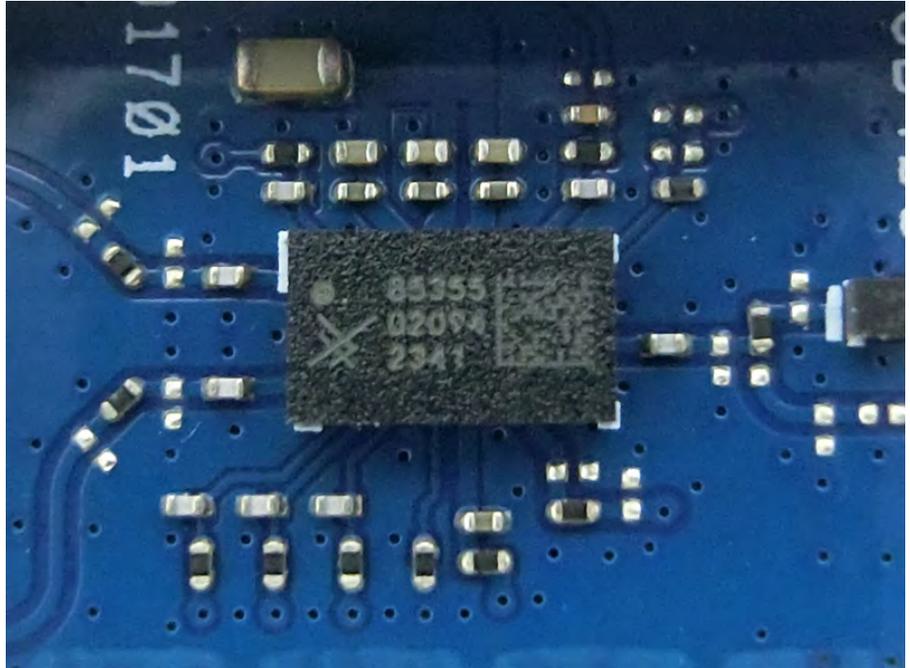


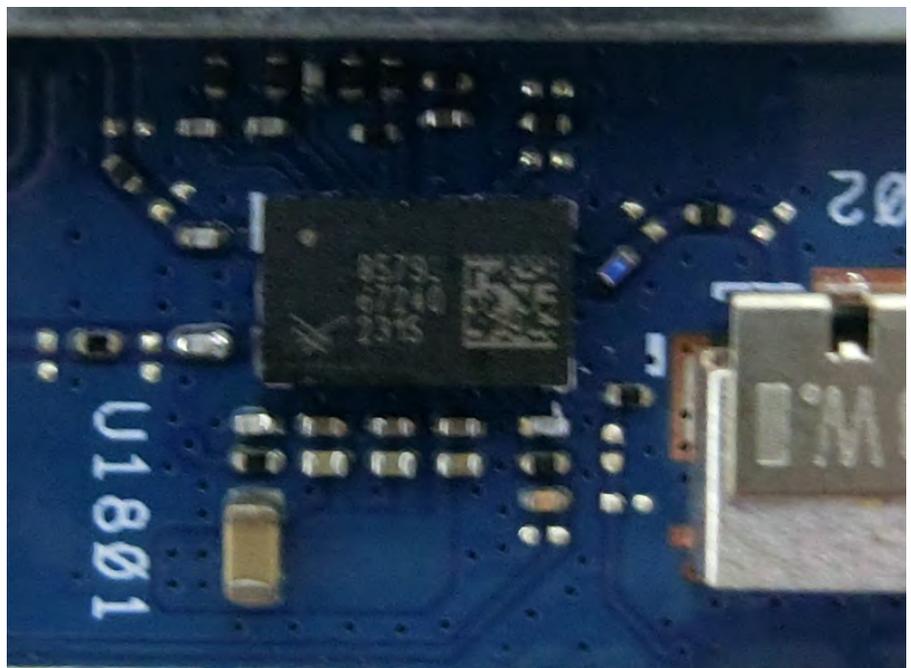
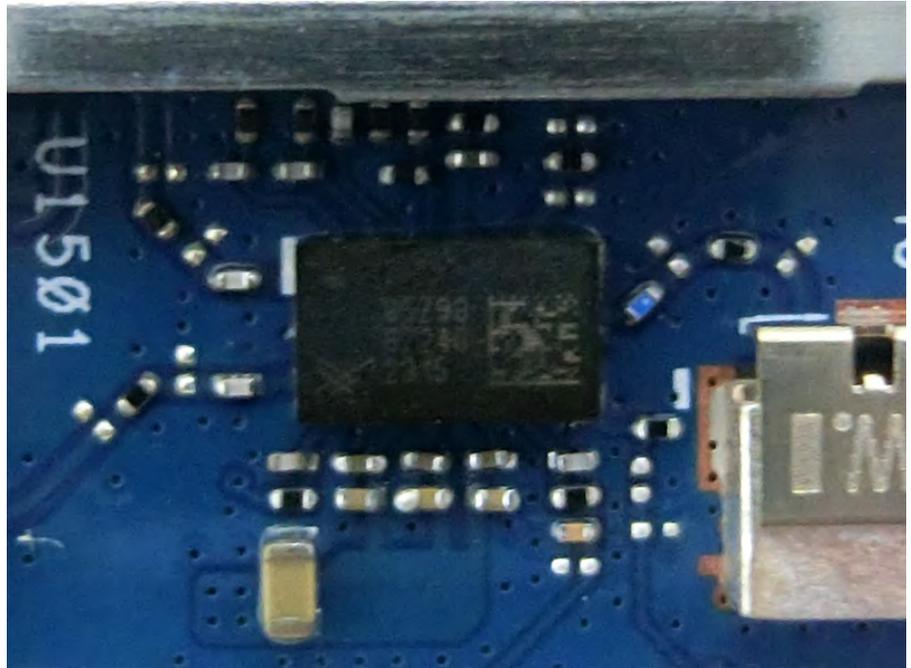


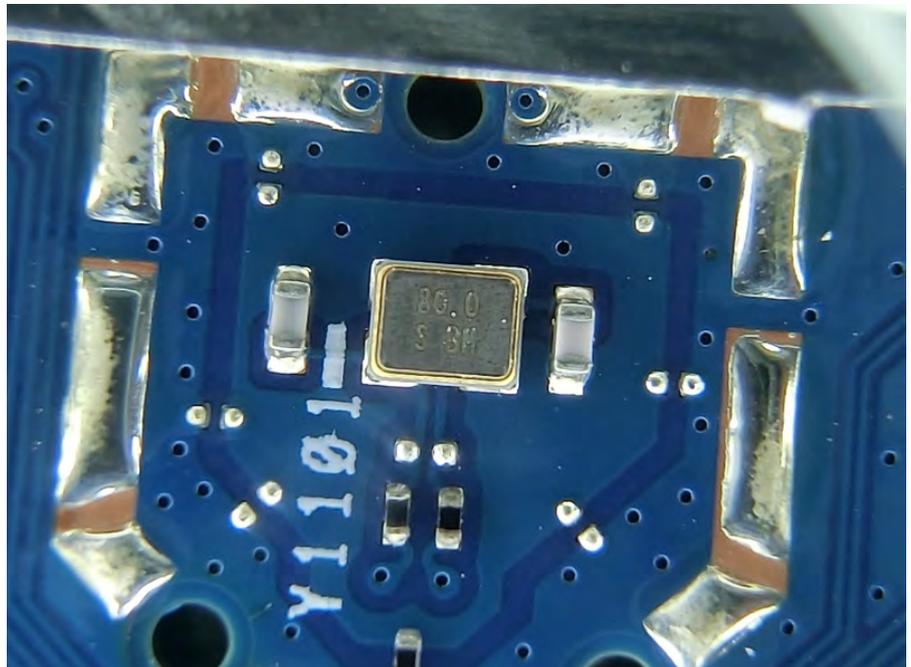
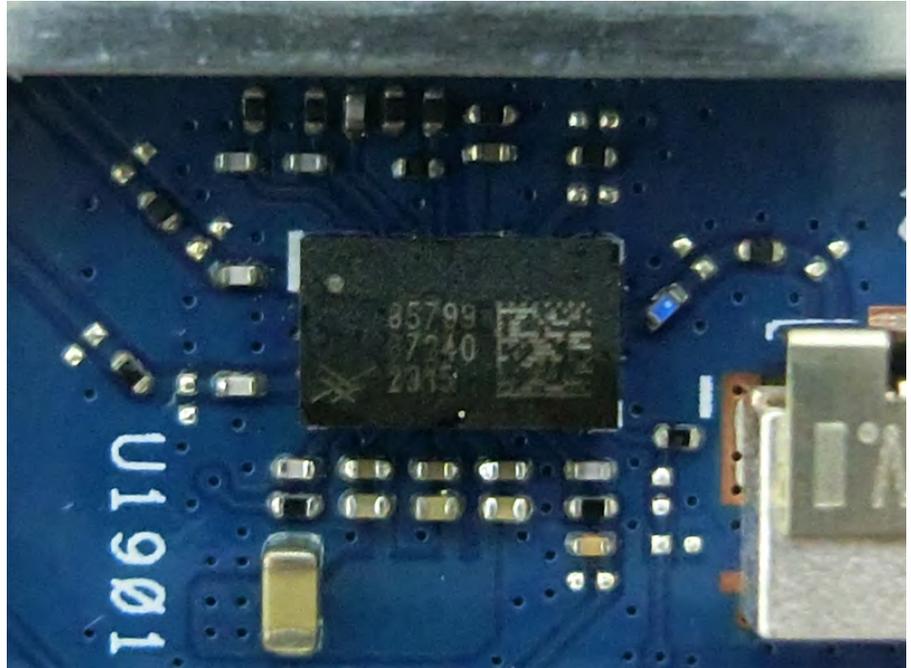


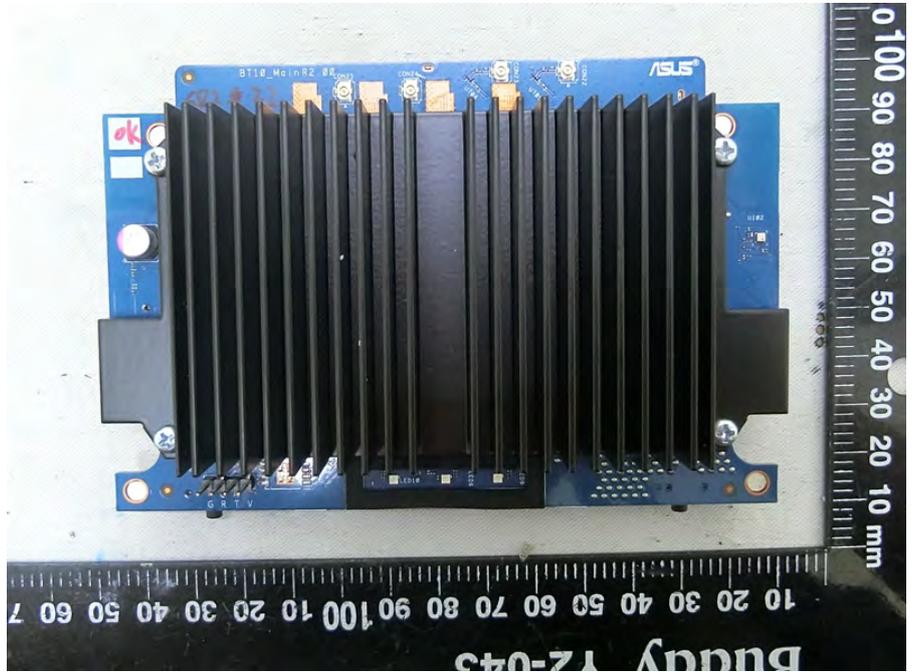


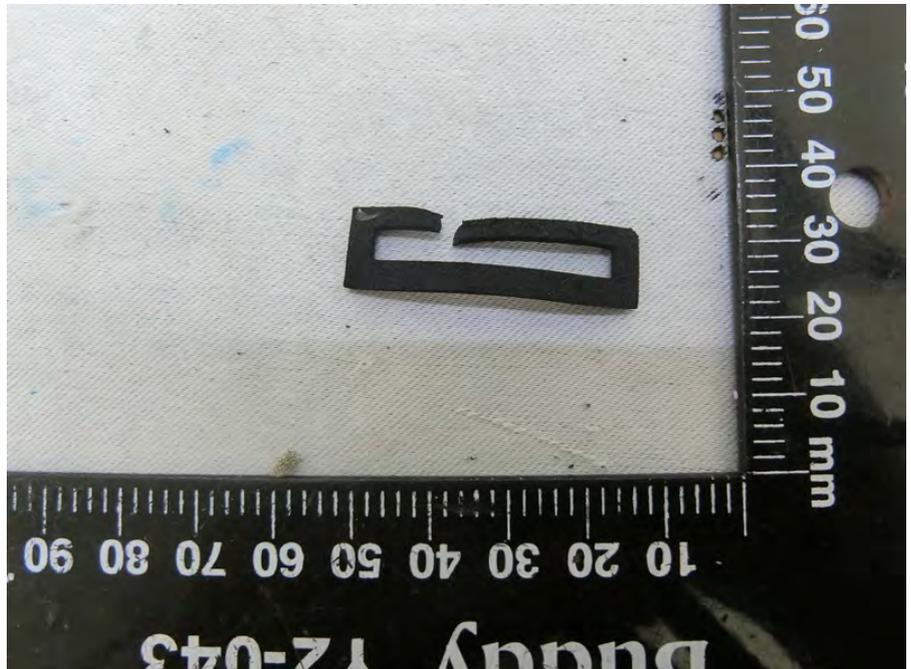
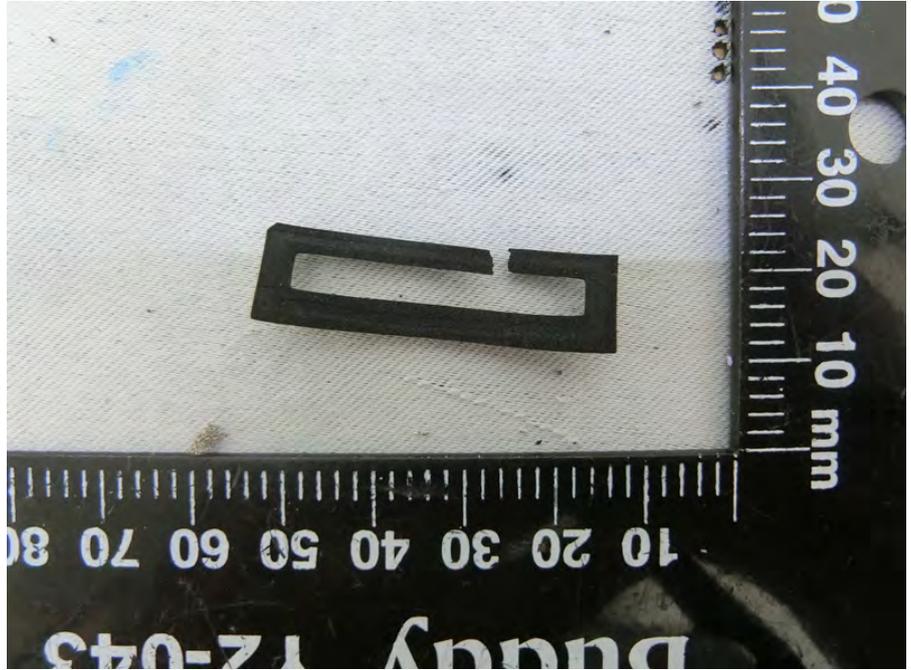


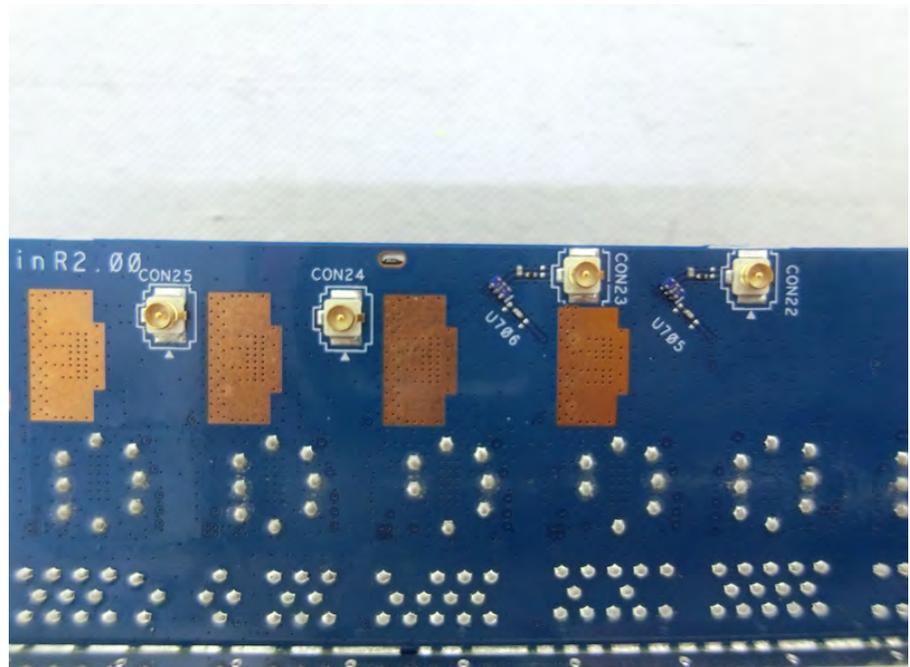
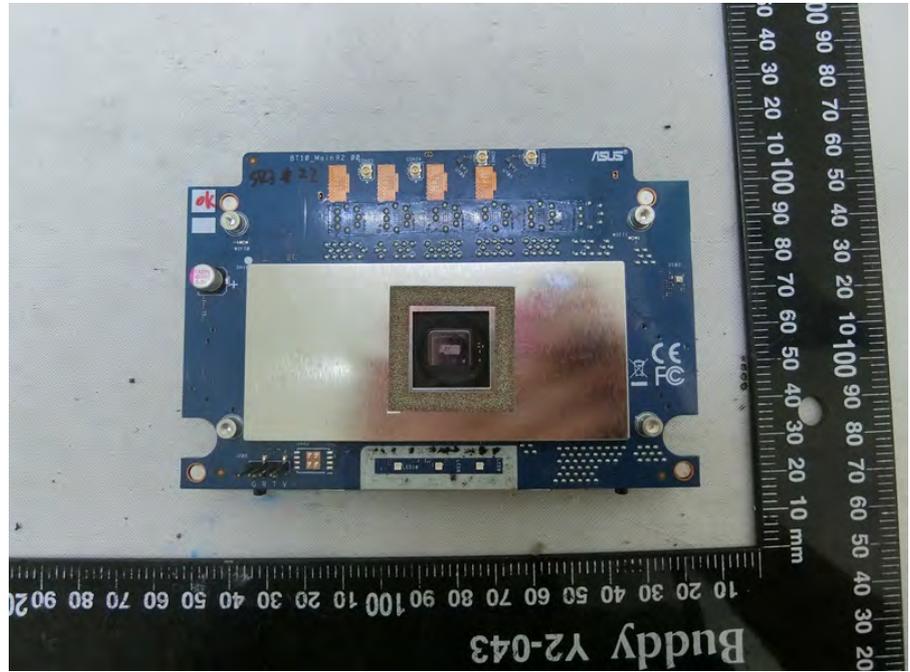


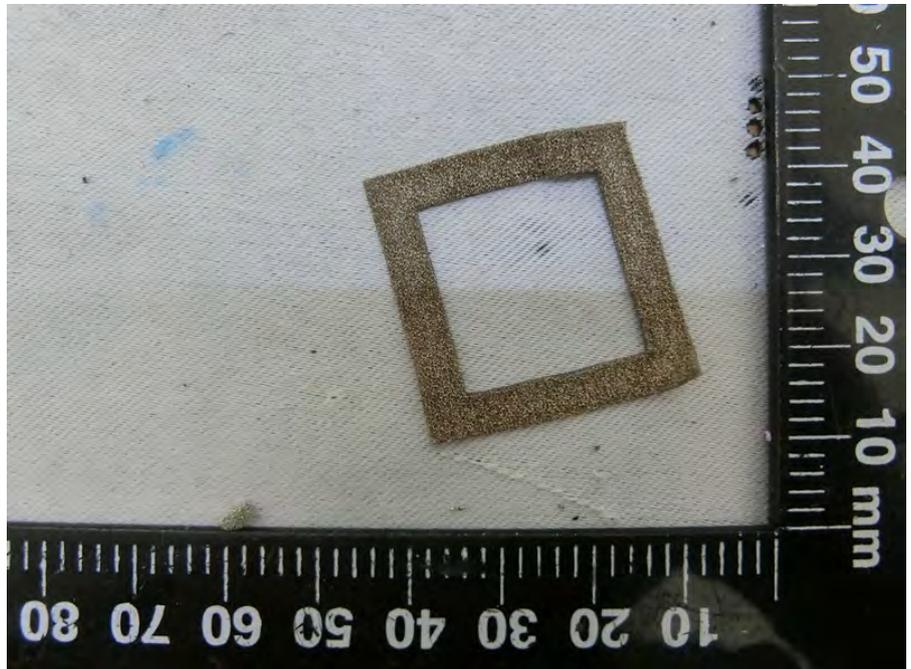
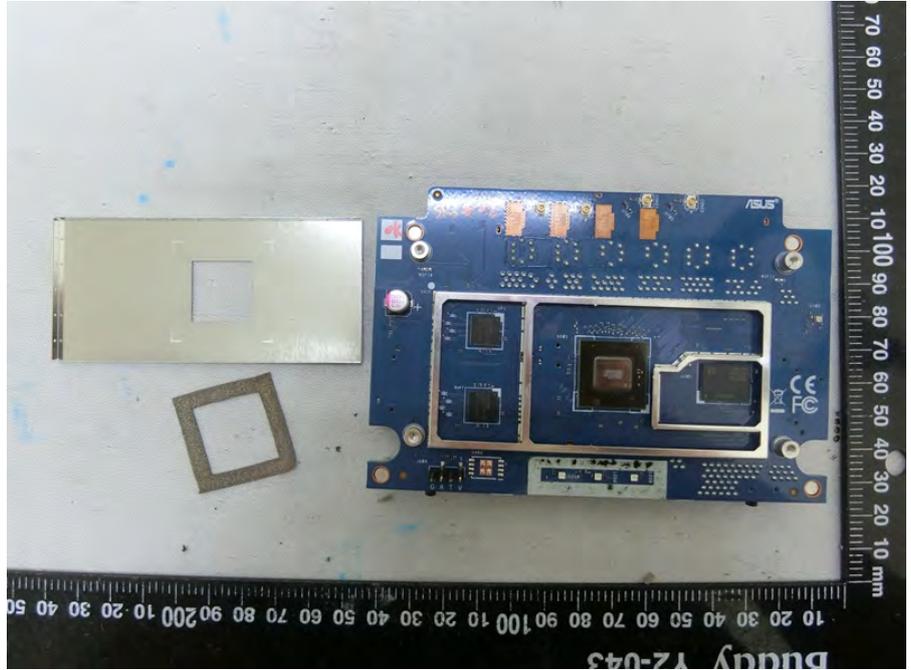














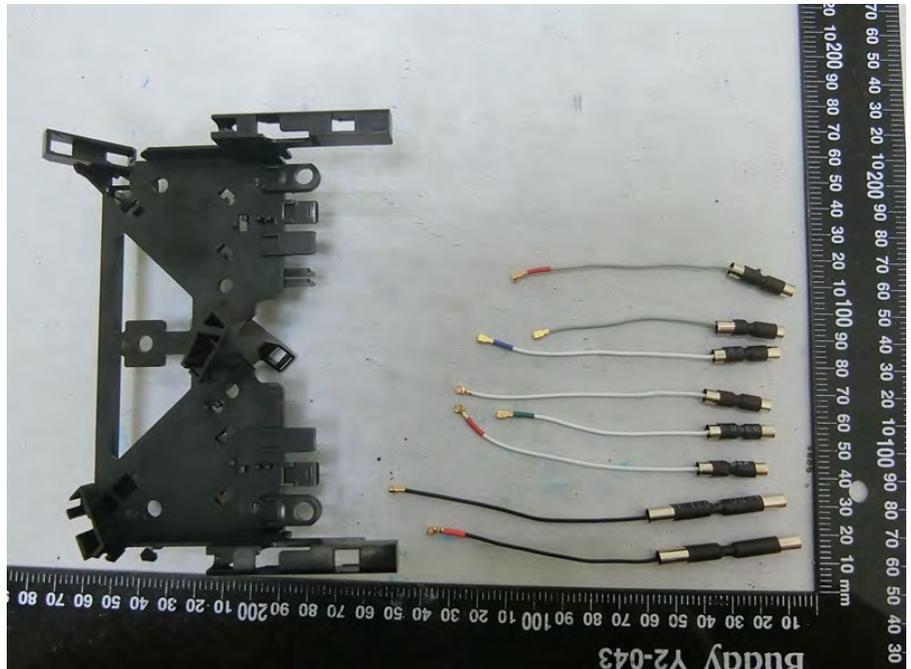
EUT 1

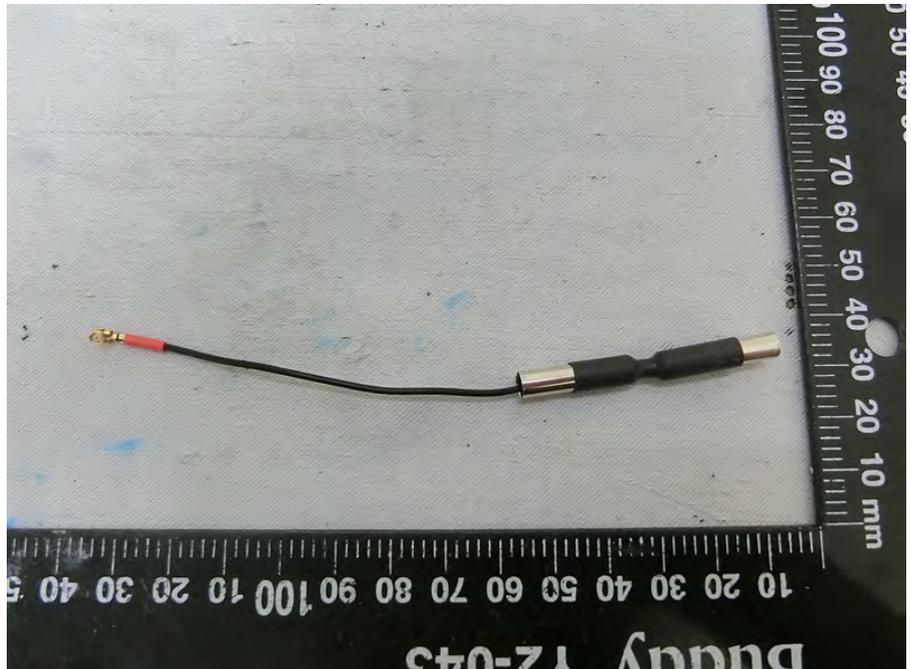
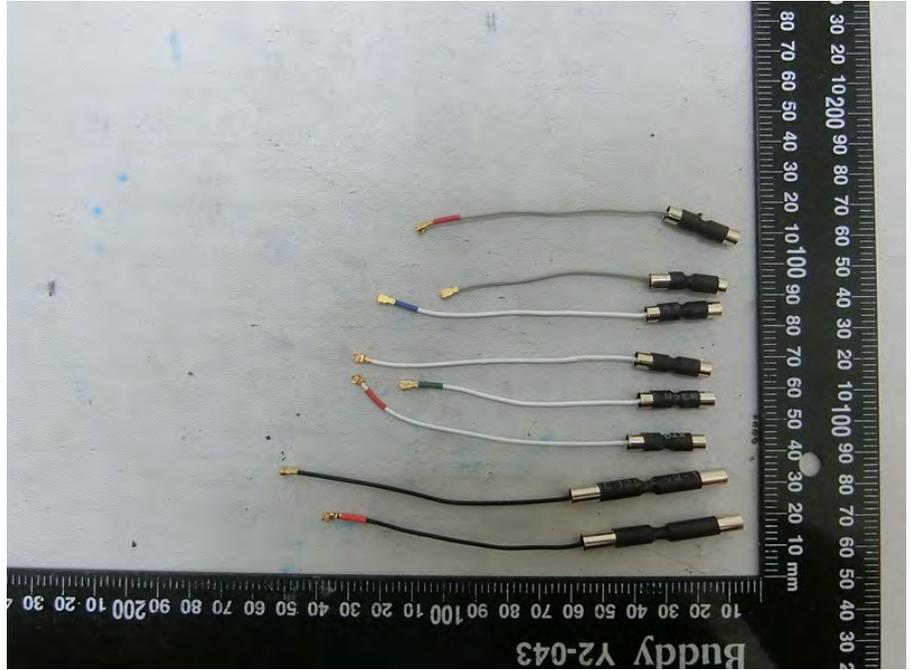
Integrated circuit  
packaging  
FCFBGA Package  
(Location: UP1)

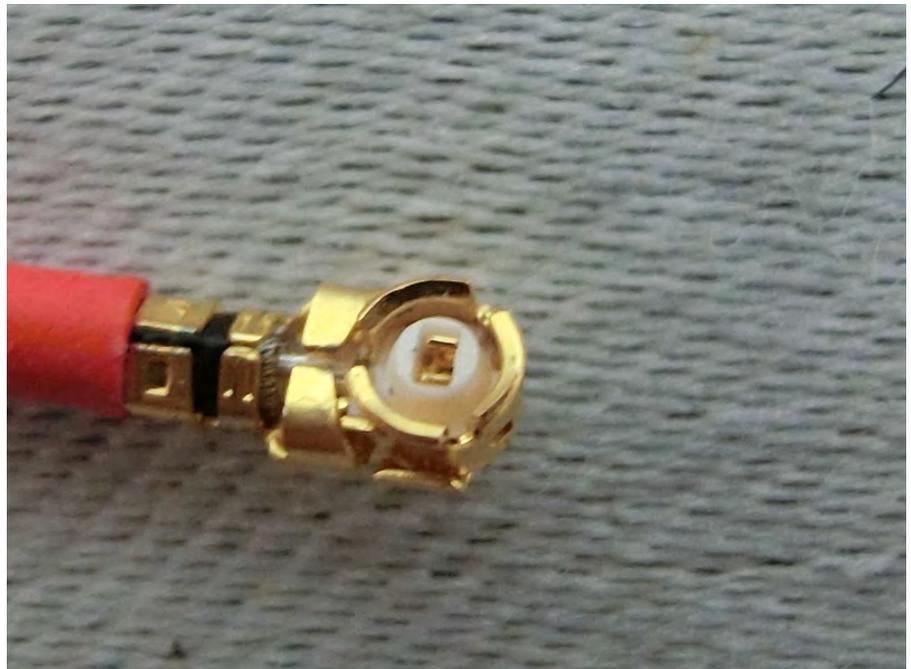
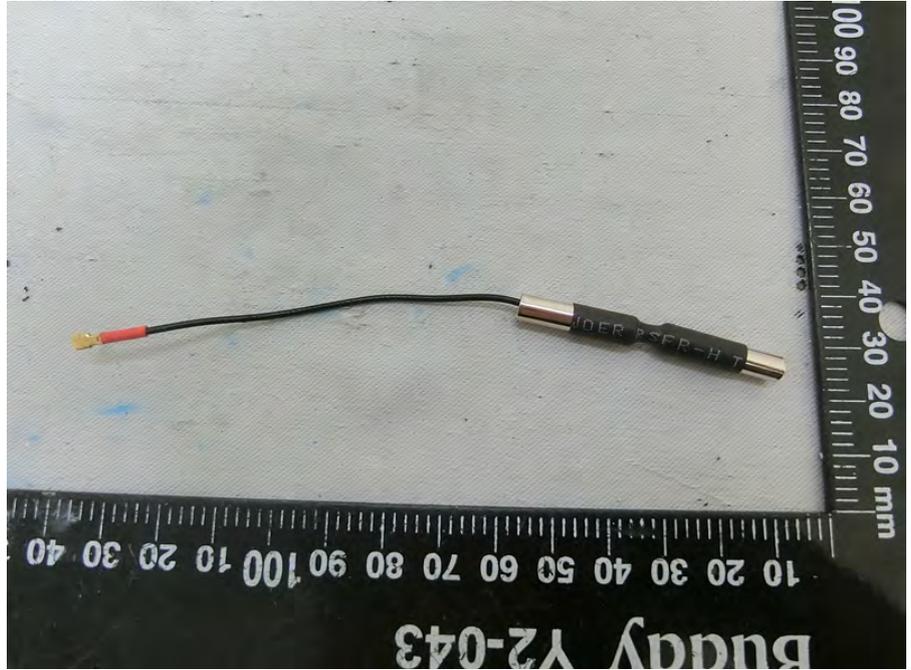


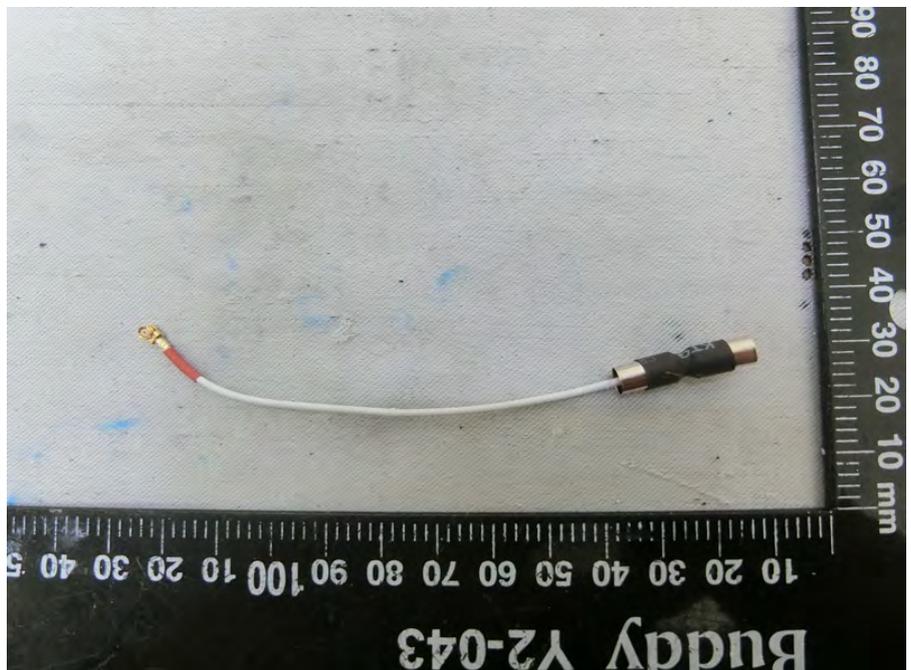
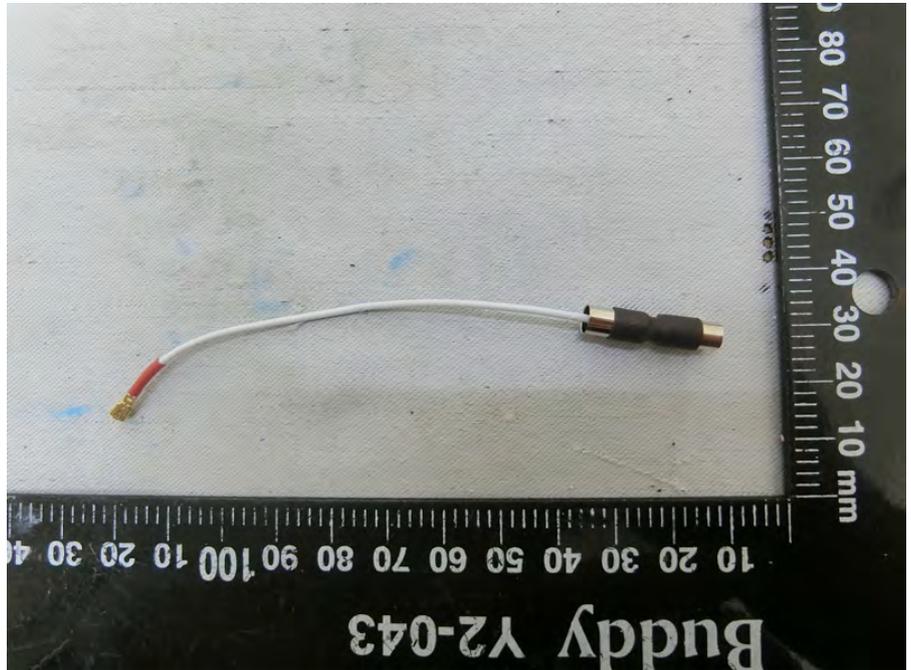
Integrated circuit  
packaging  
FCFBGA Package  
(Location: BUP7)

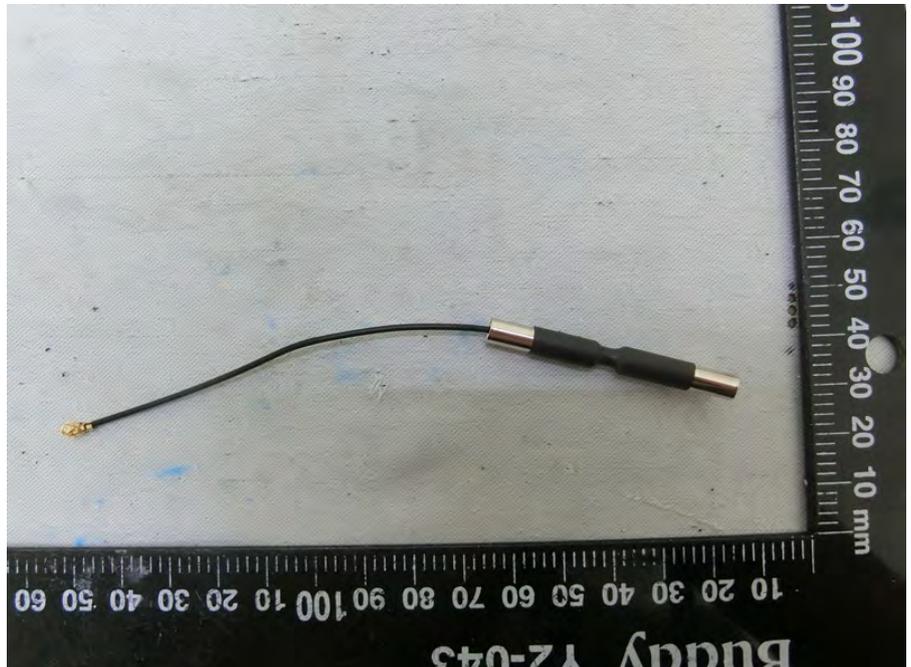
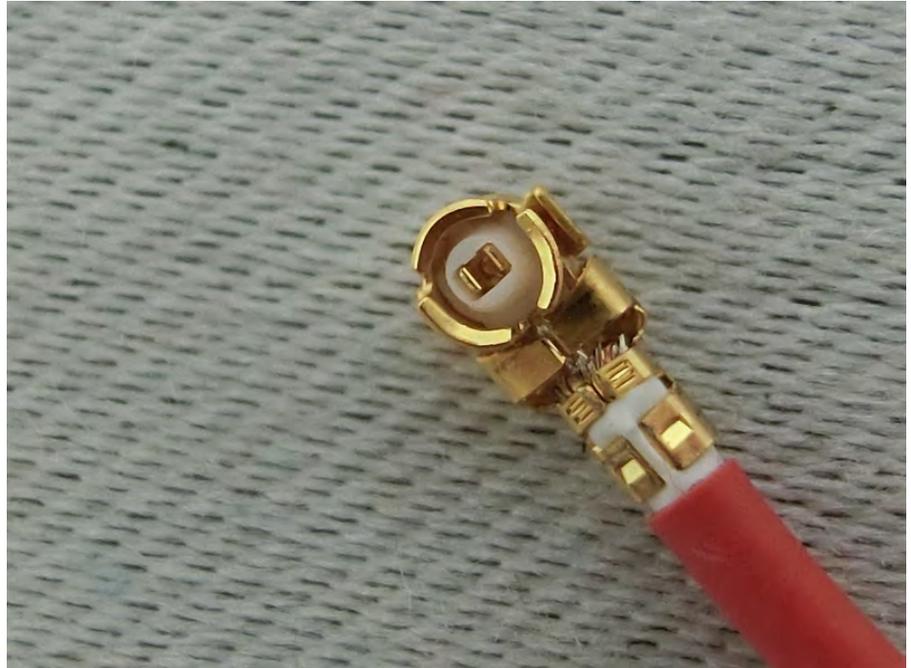




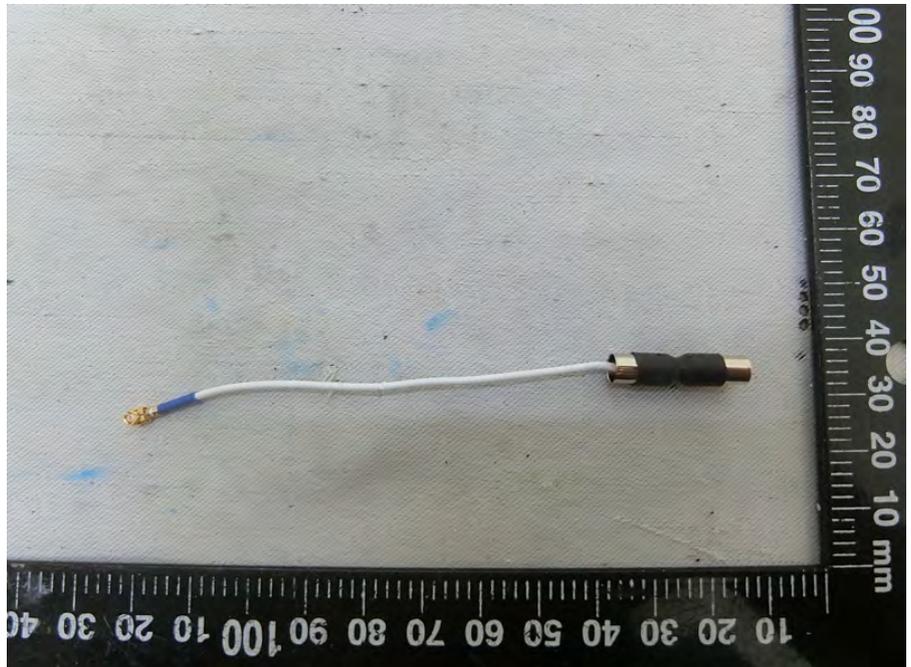
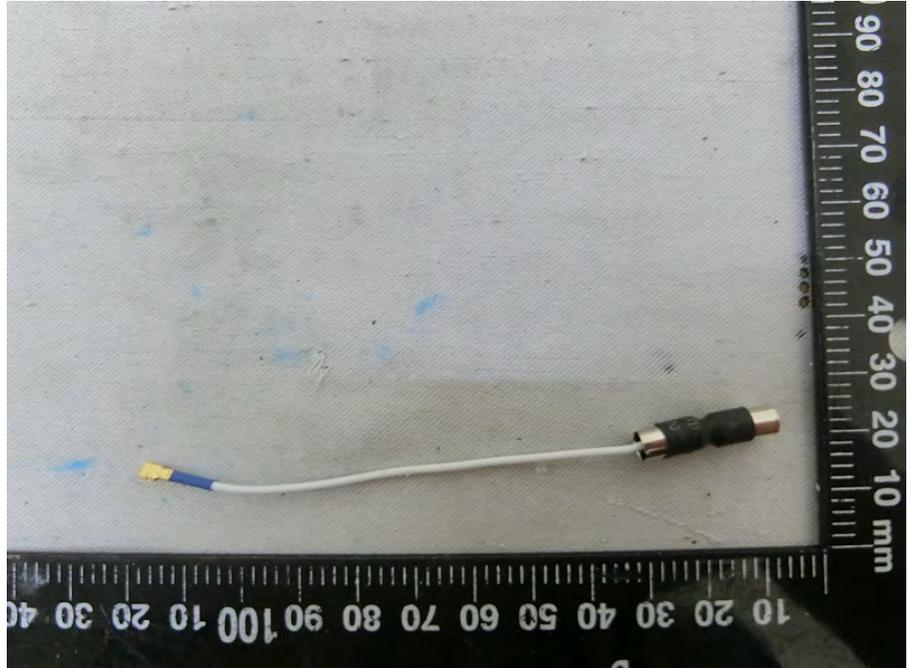


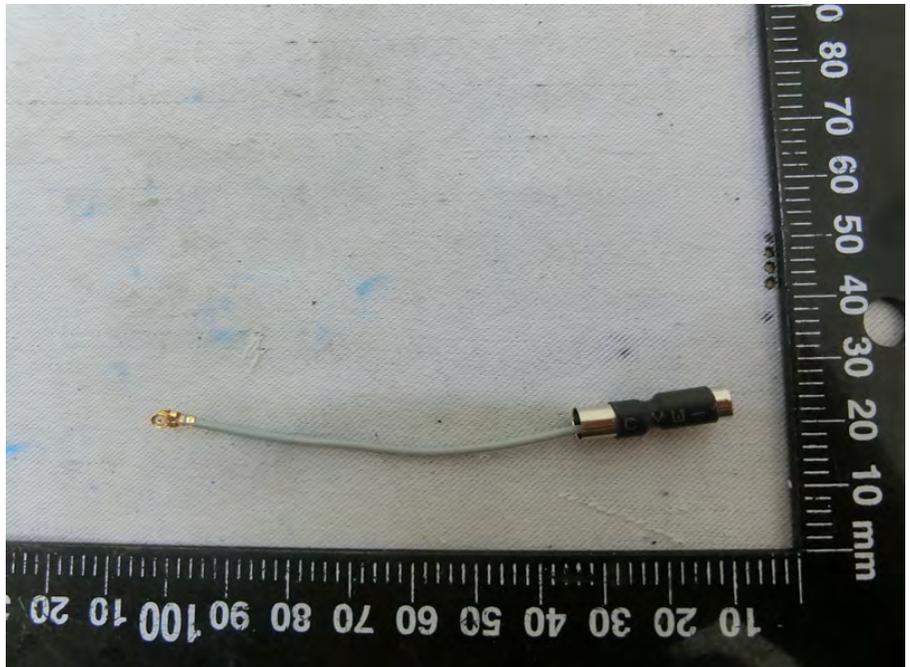
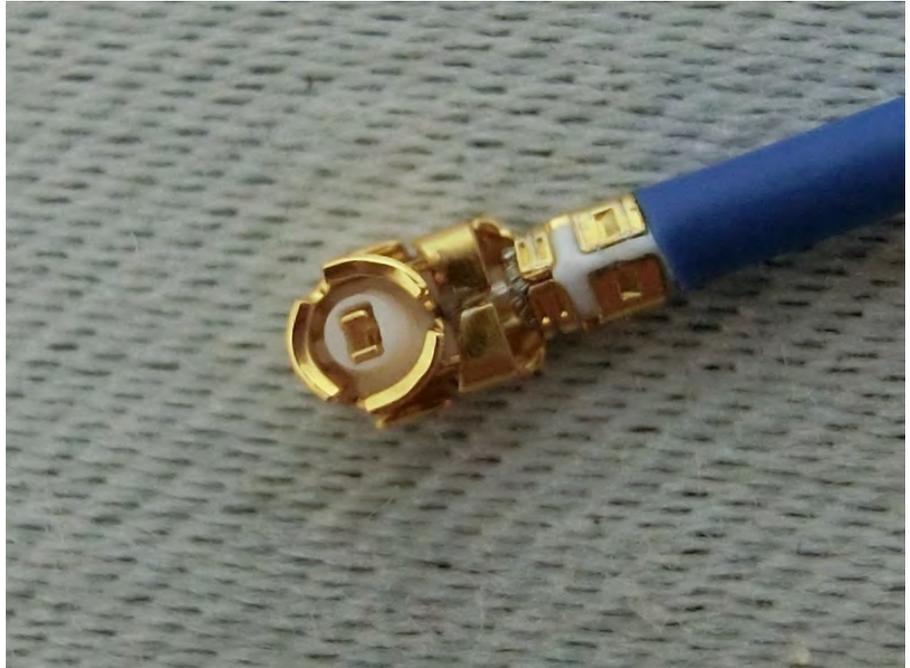


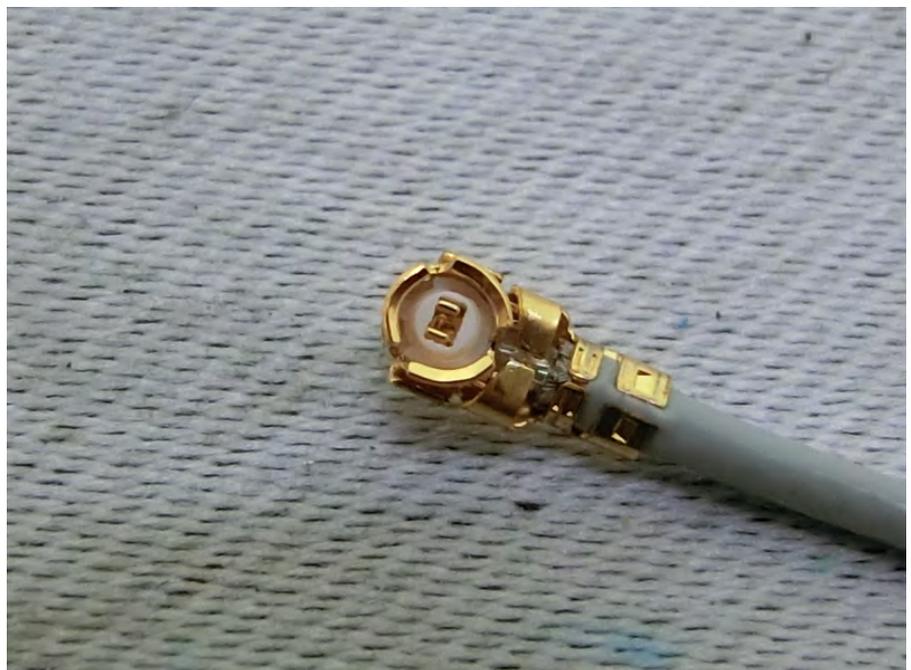
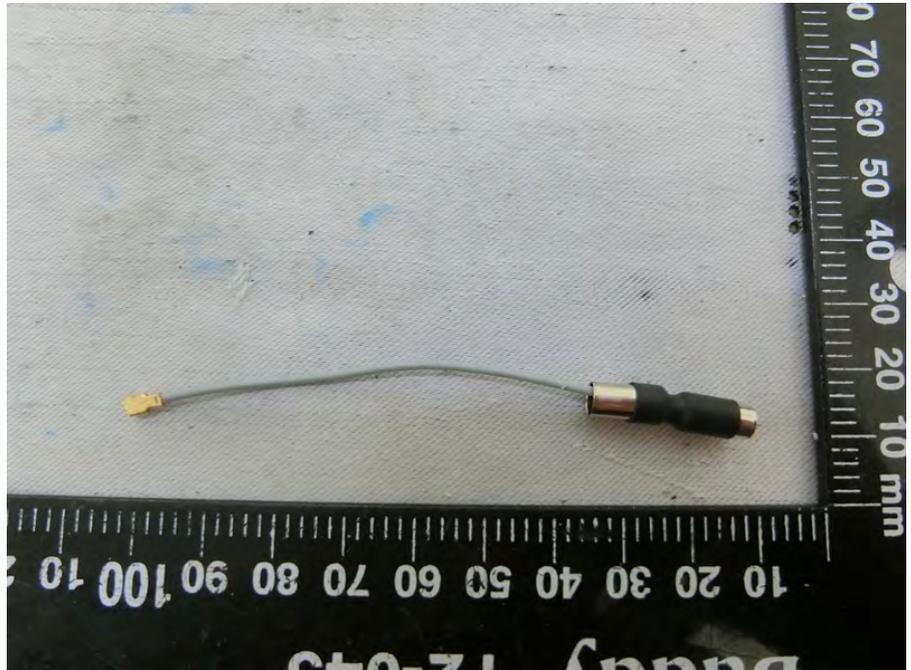


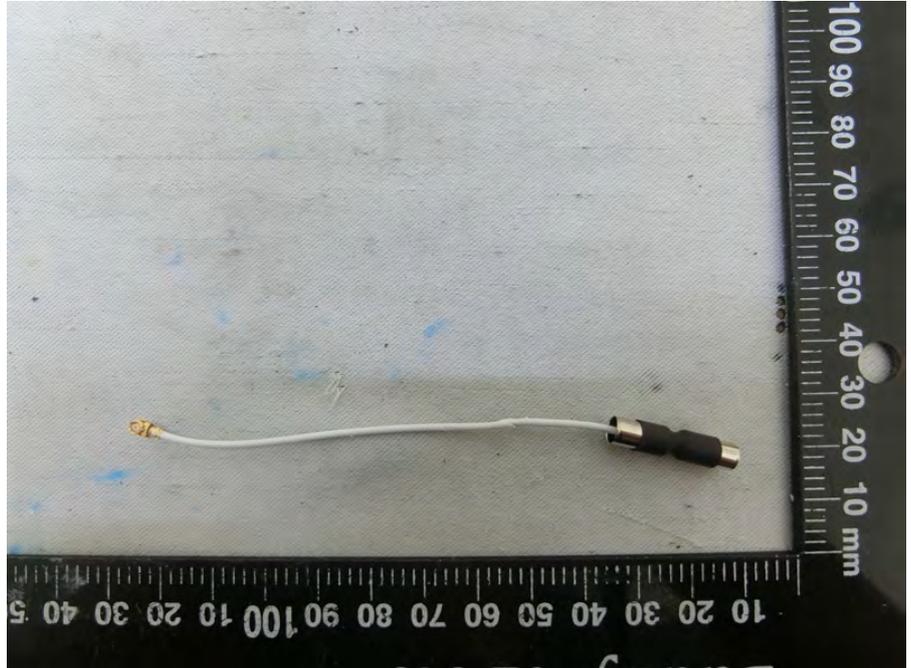


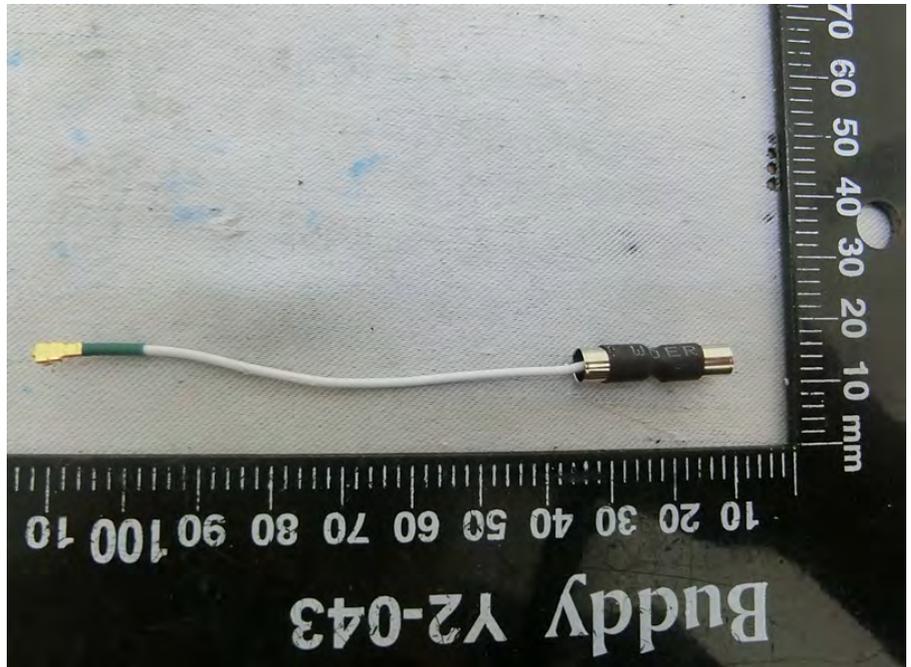
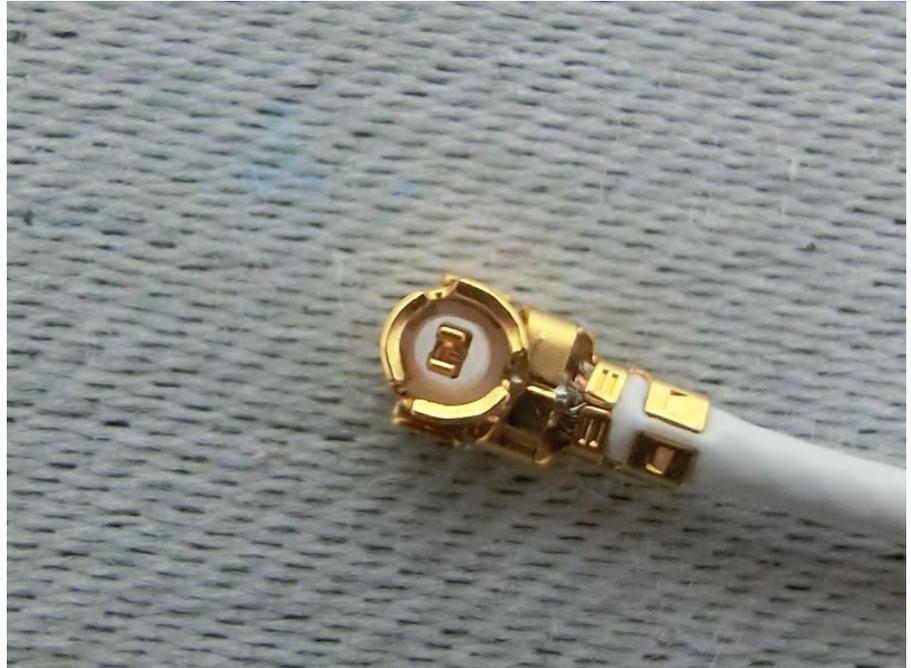


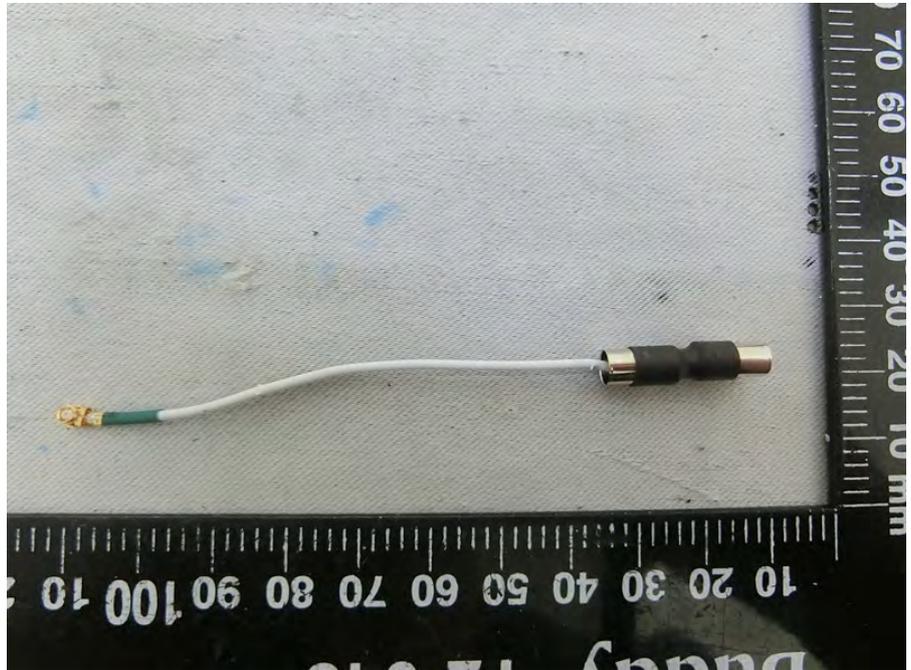


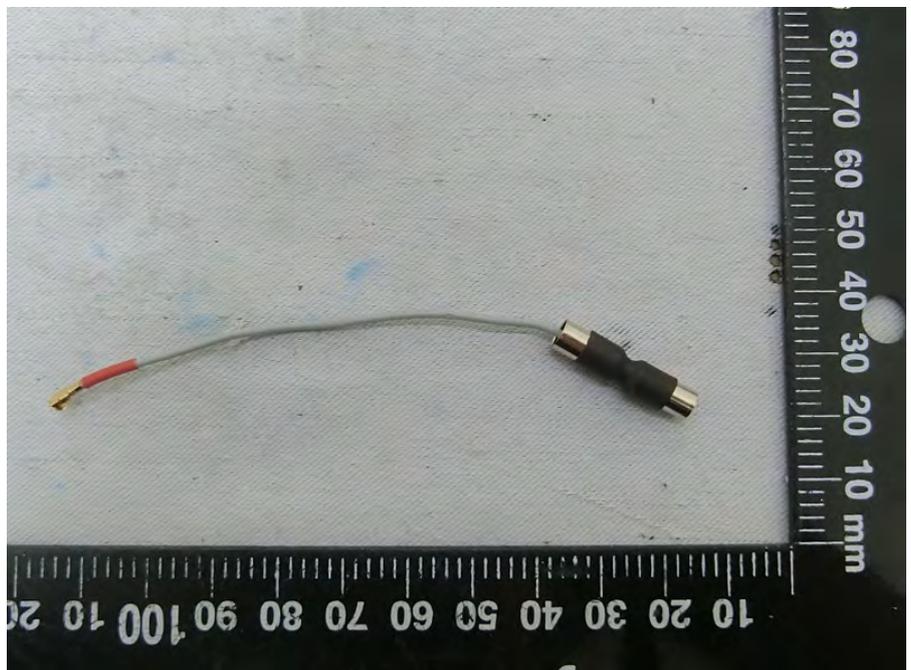
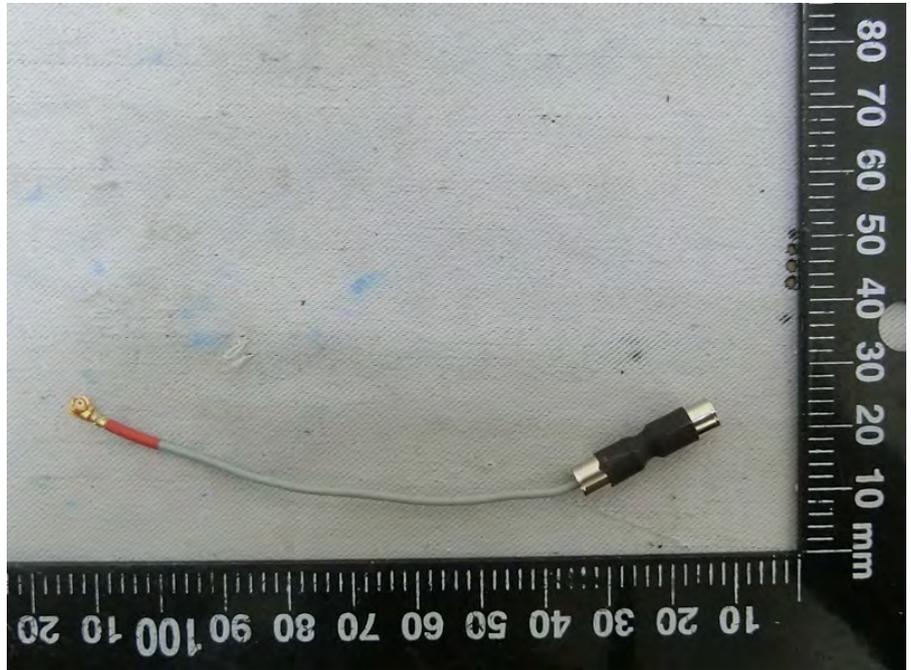


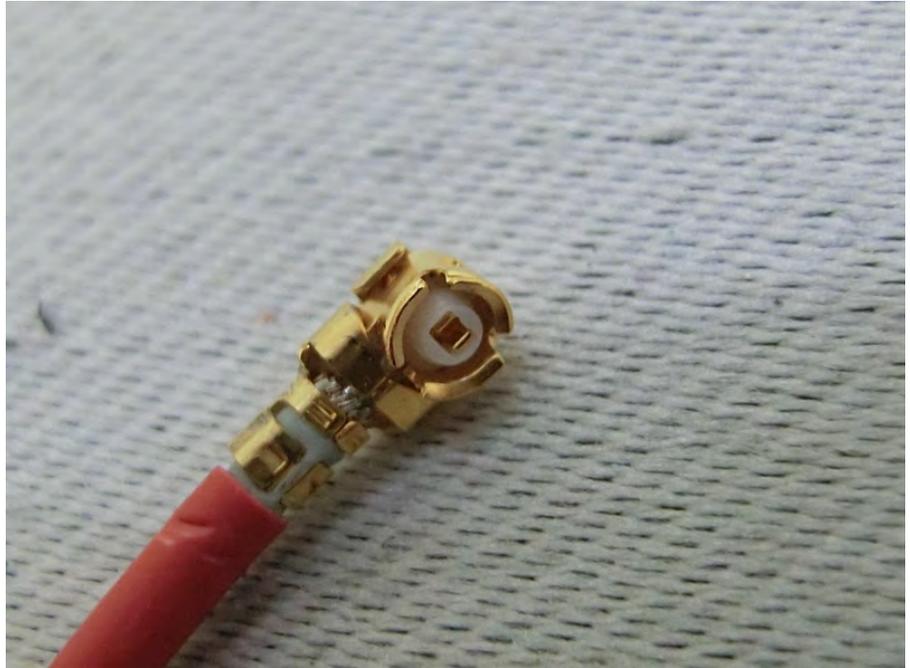






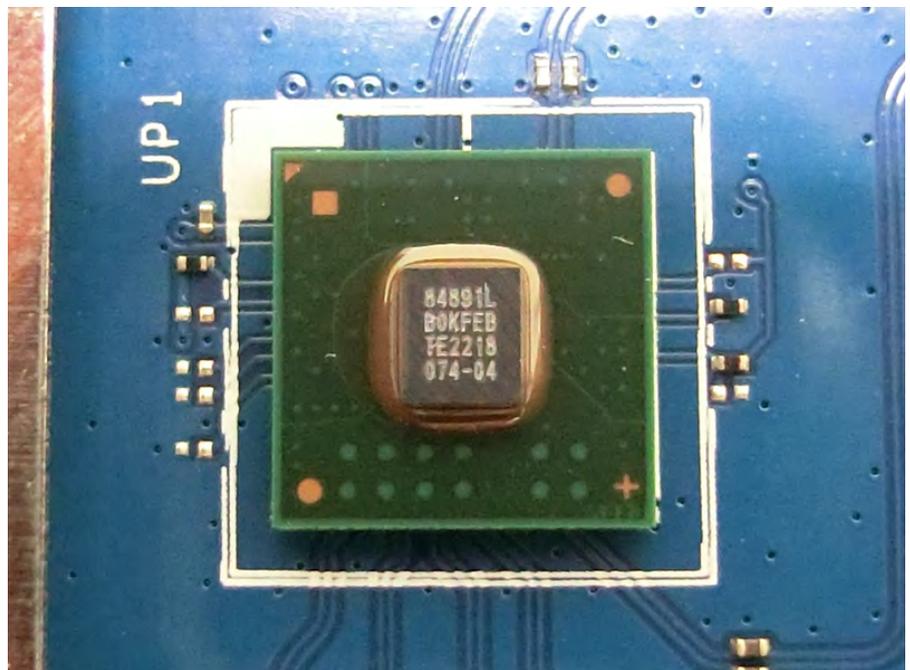






EUT 2

Integrated circuit  
packaging  
FCBGA Package  
(Location: UP1)



Integrated circuit  
packaging  
FCBGA Package  
(Location: BUP7)

