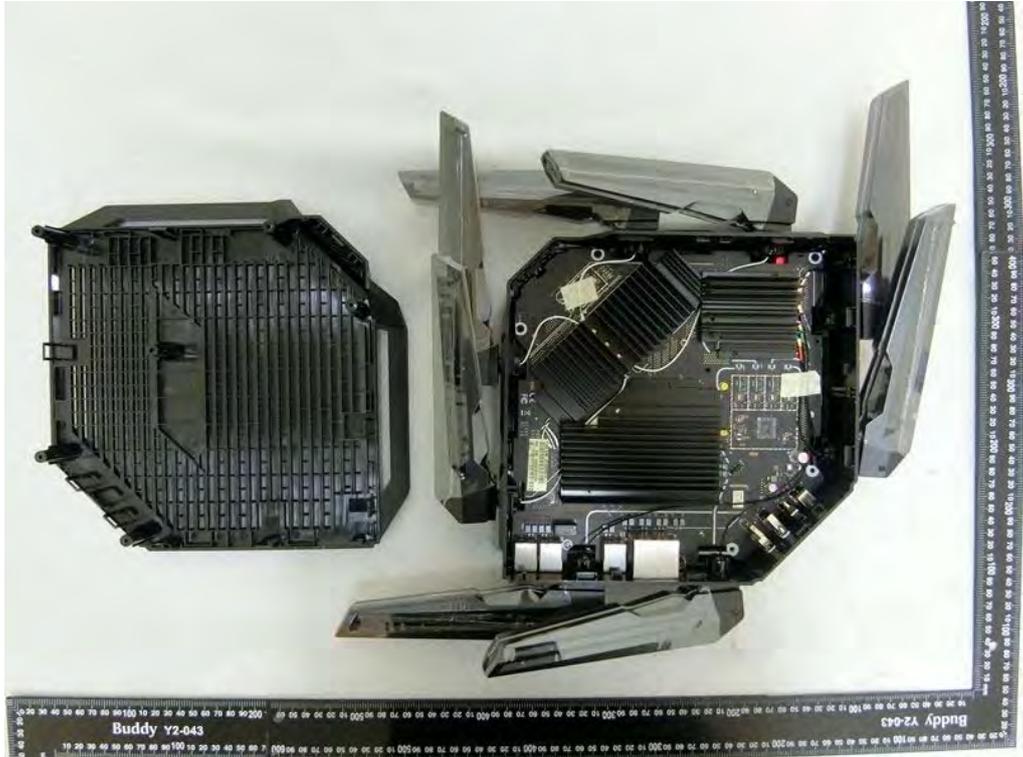
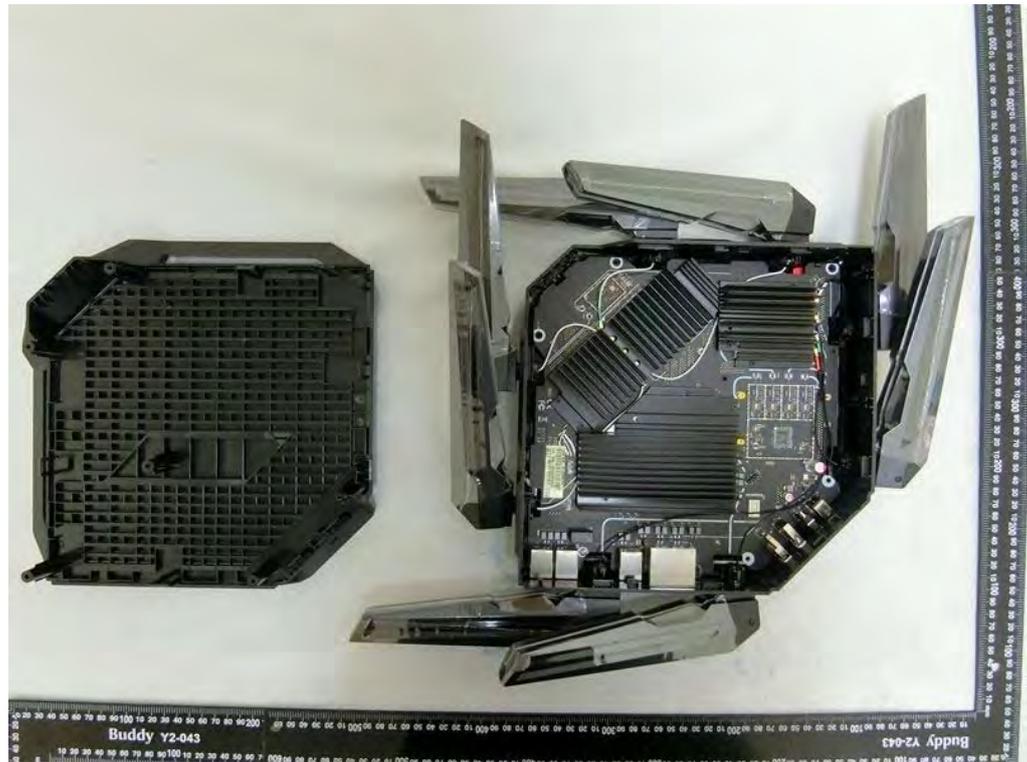
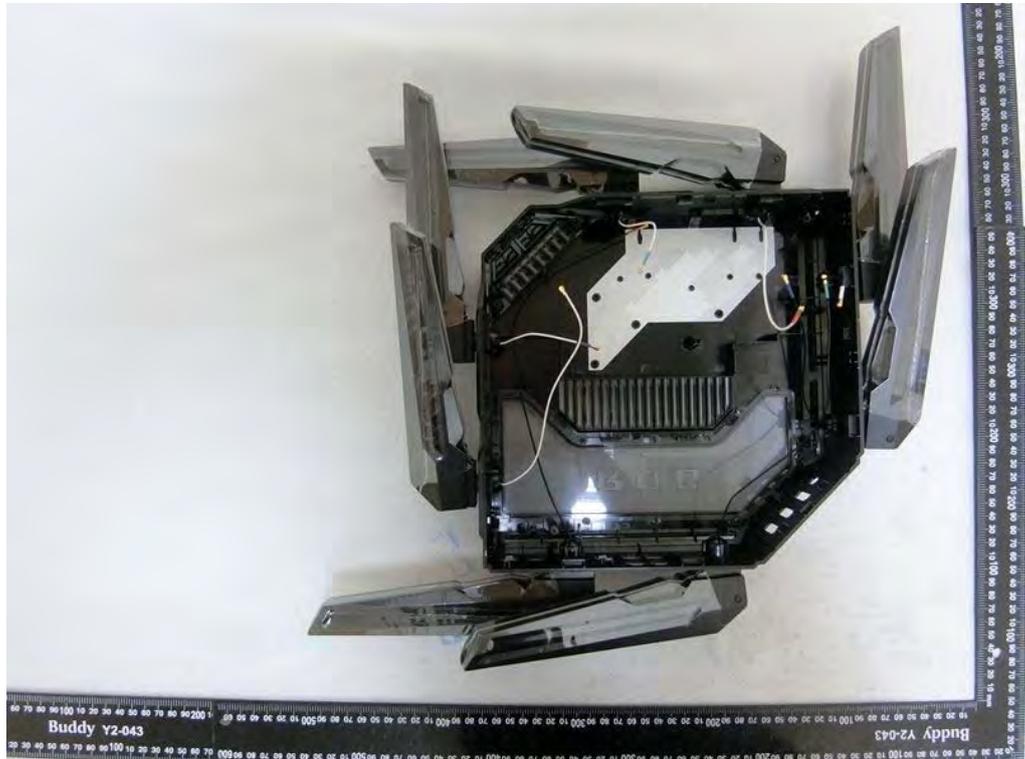


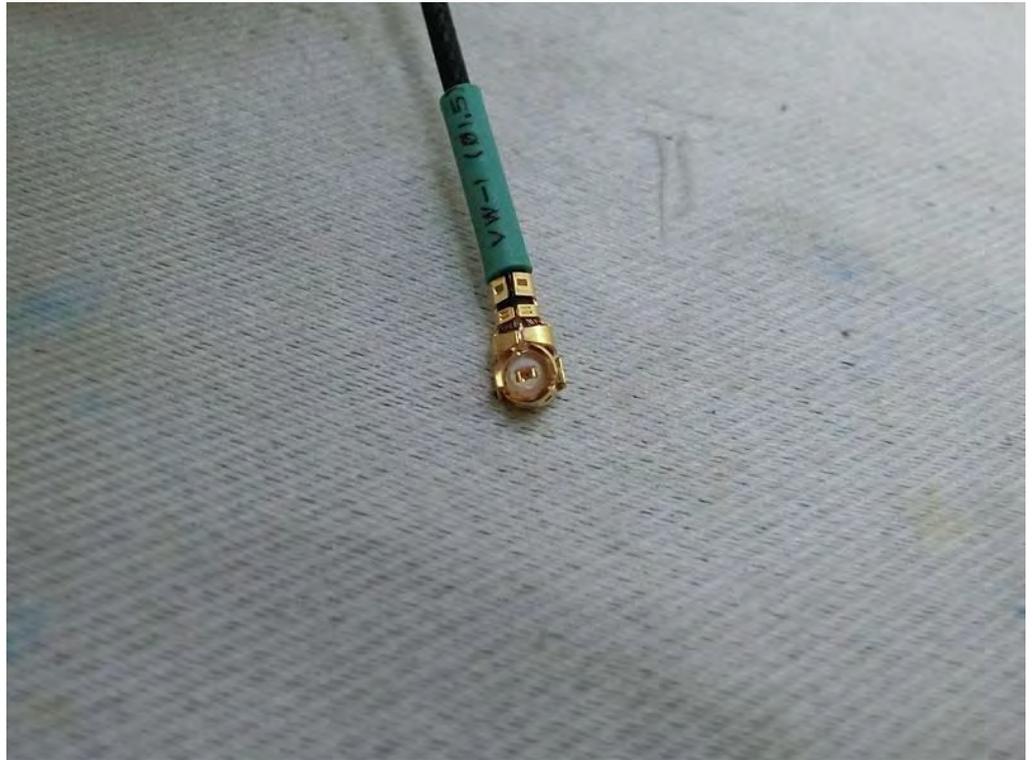
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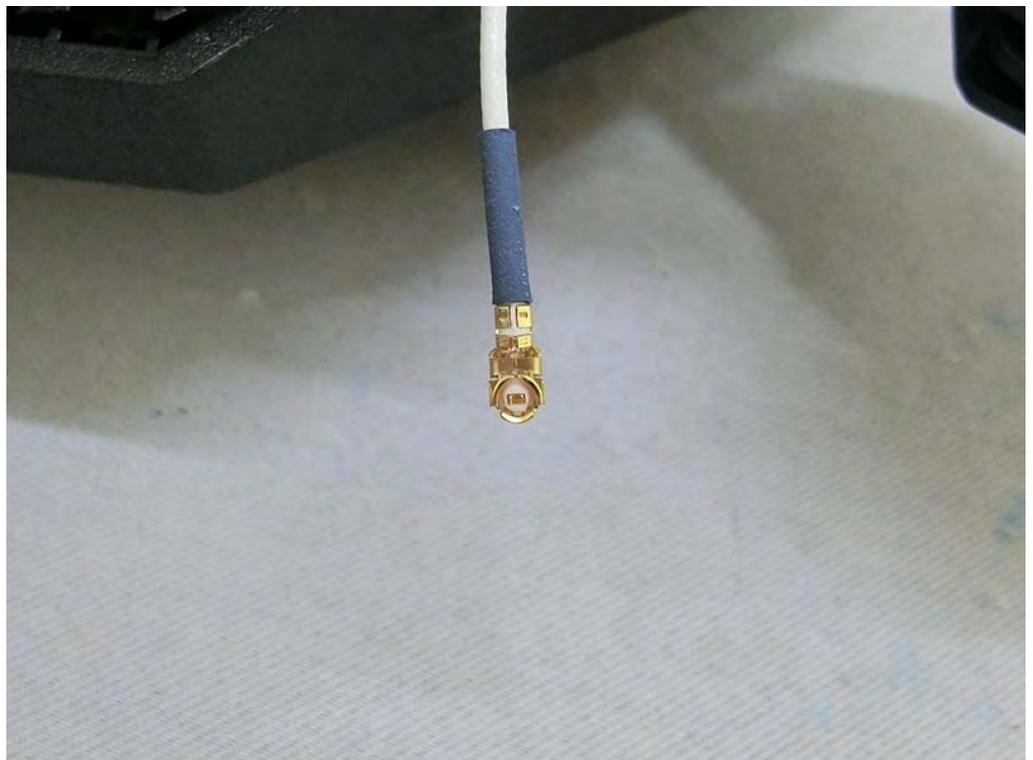
EUT 1, EUT 3

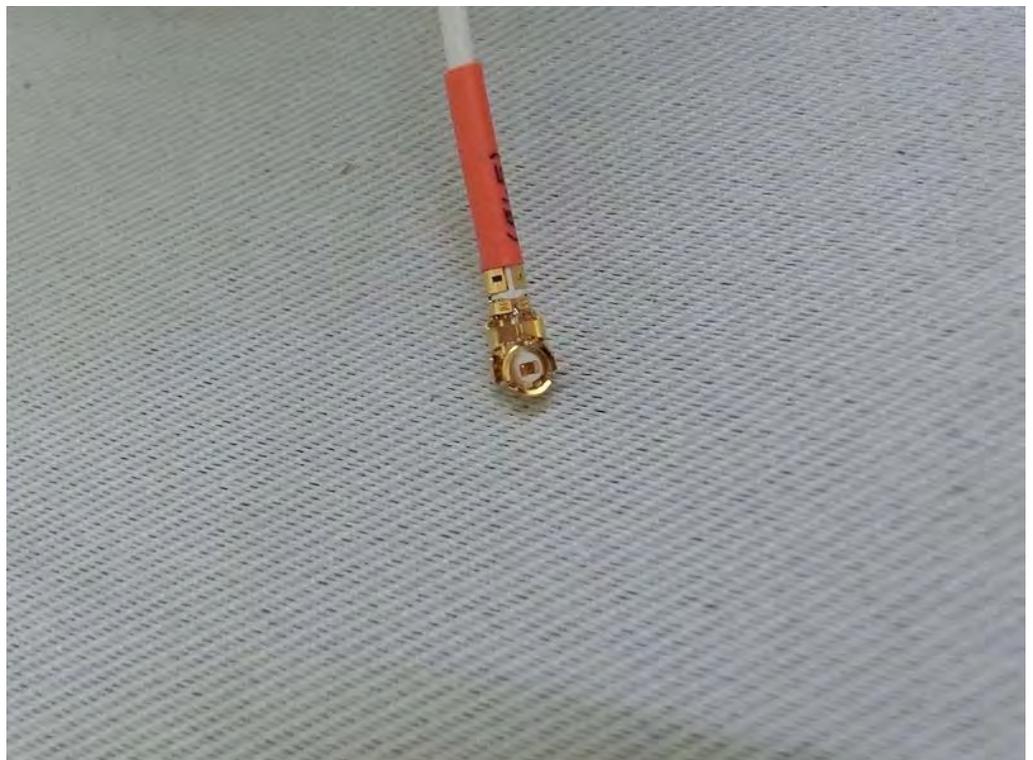




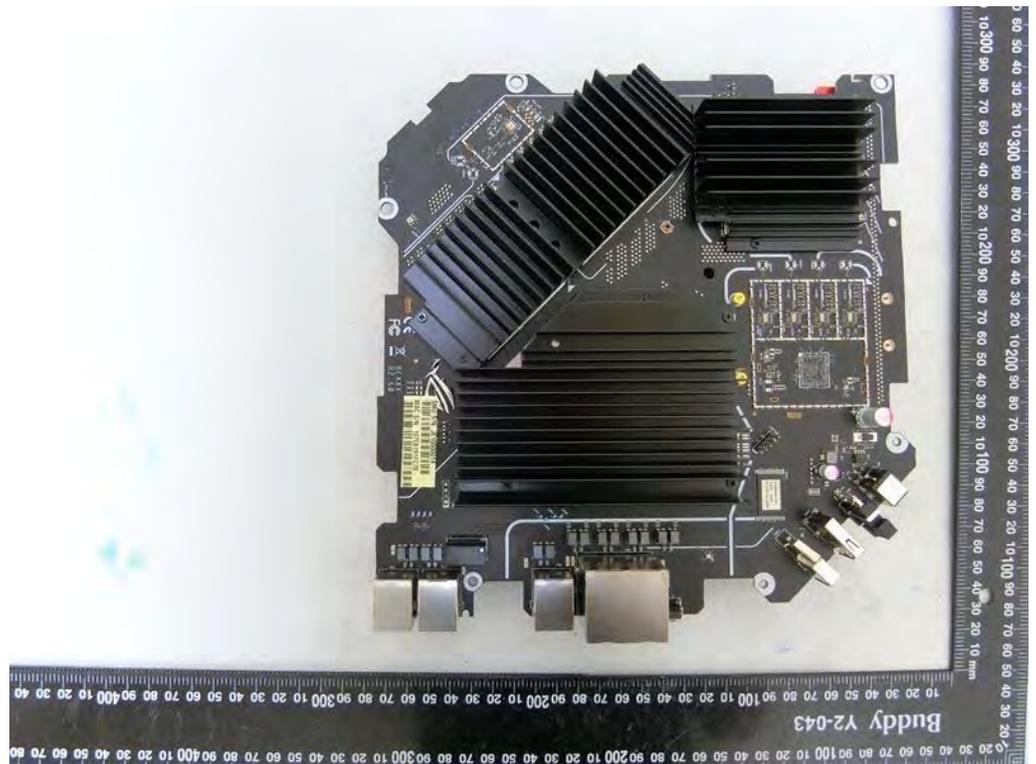




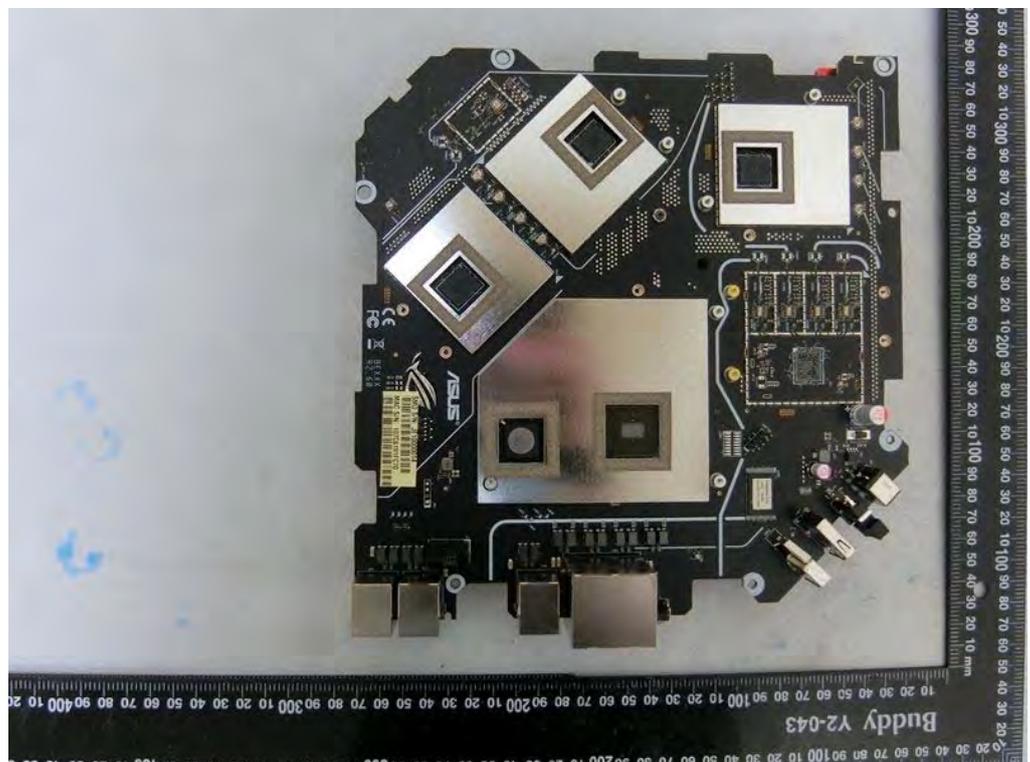
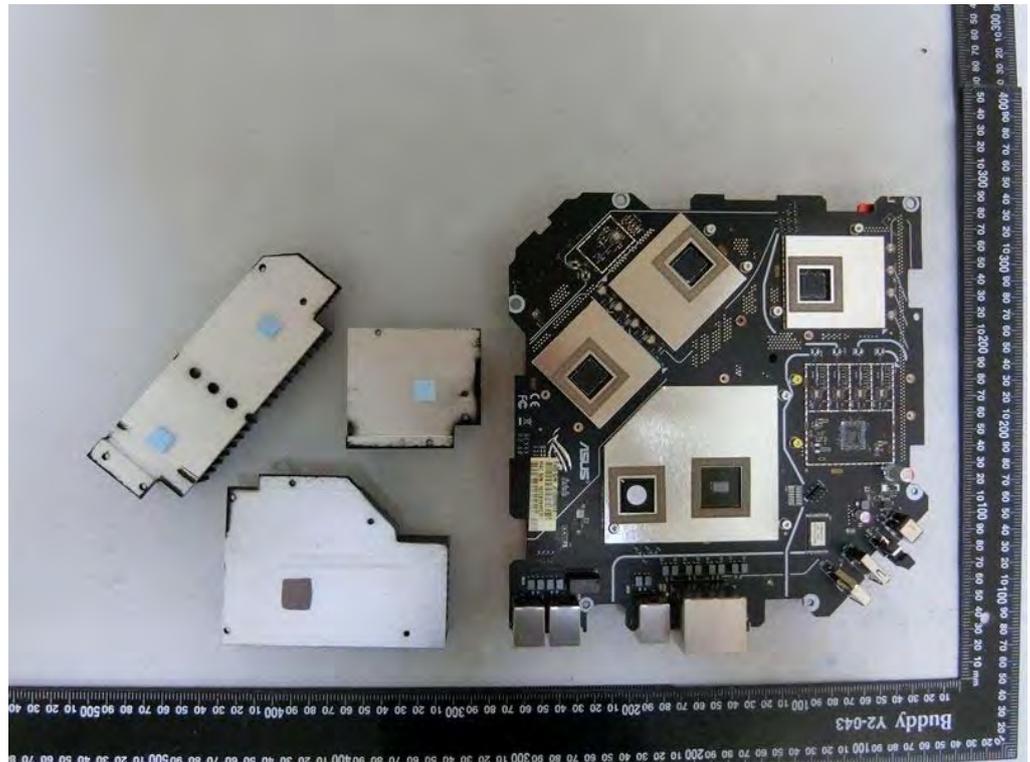


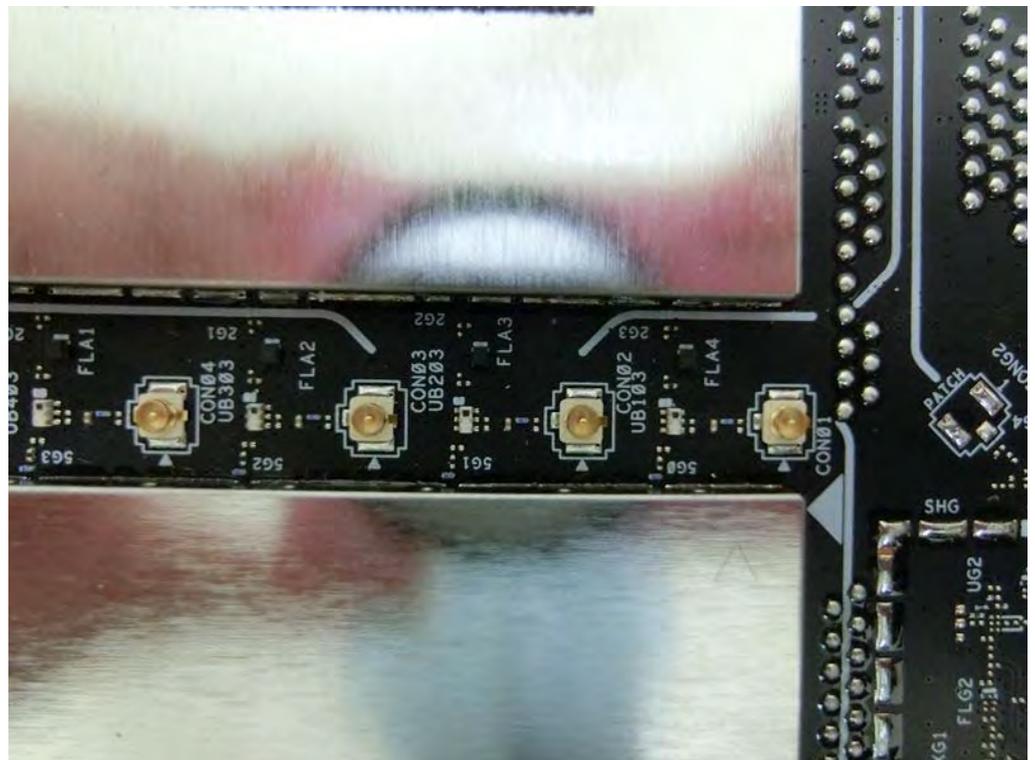
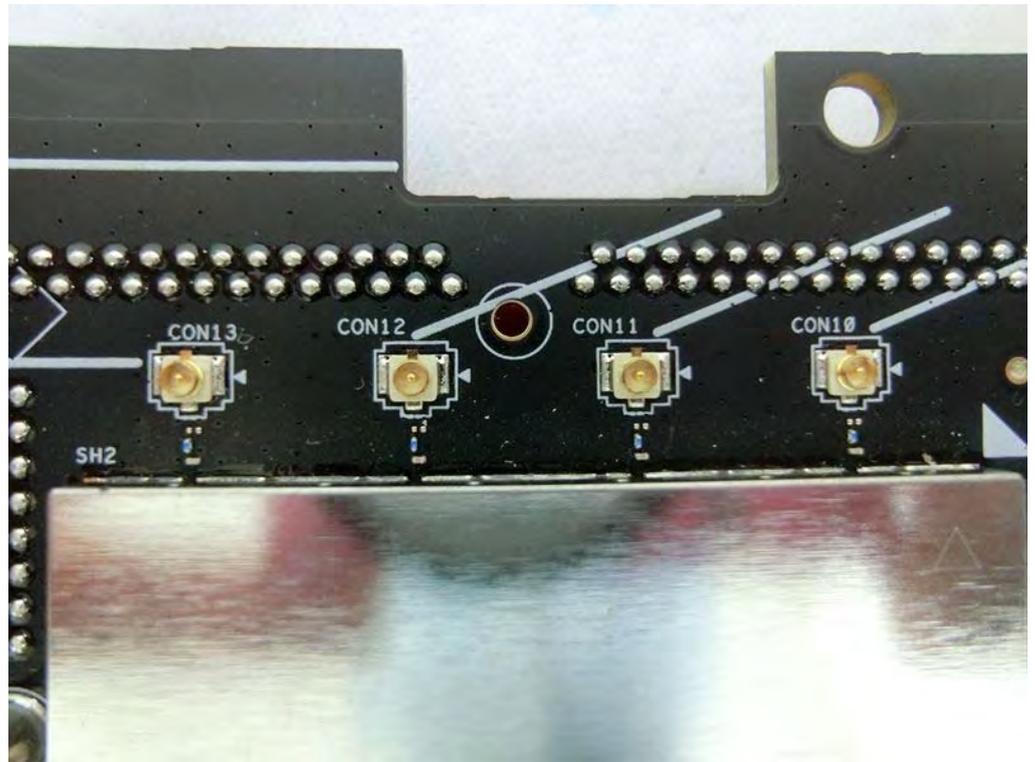


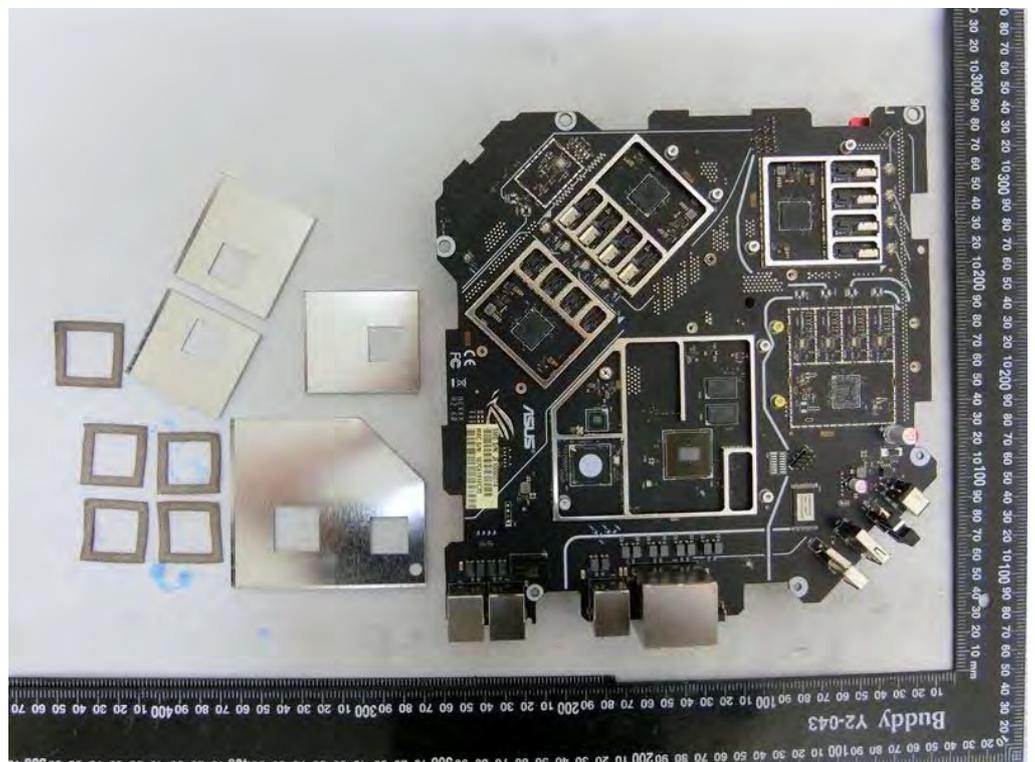


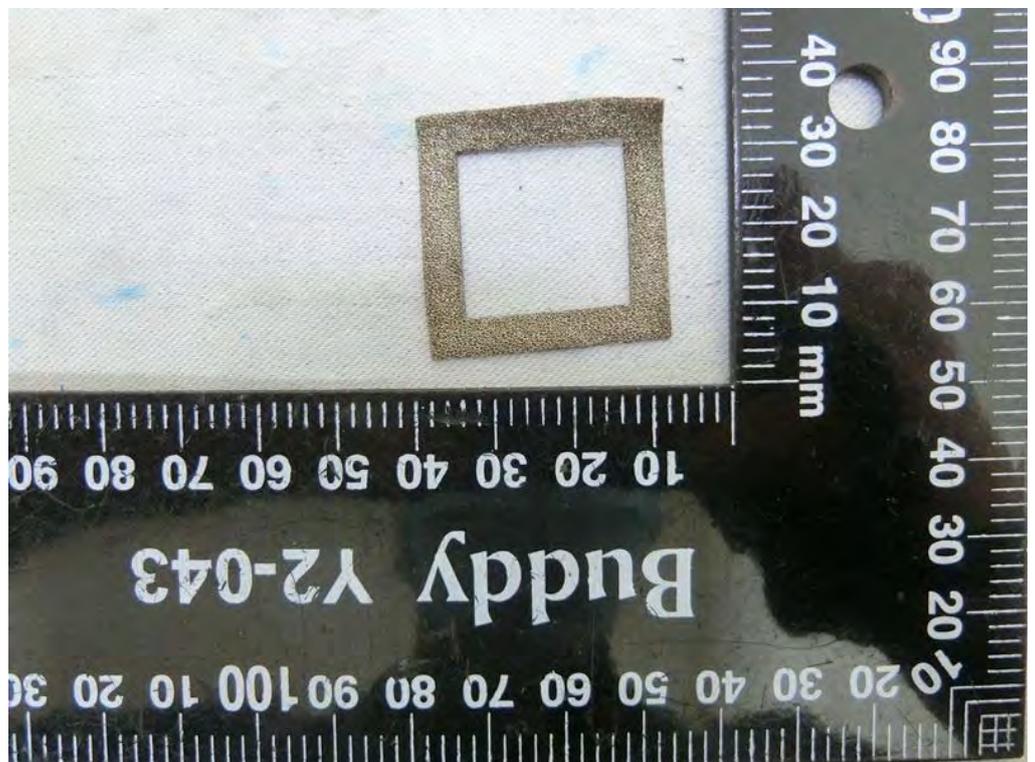
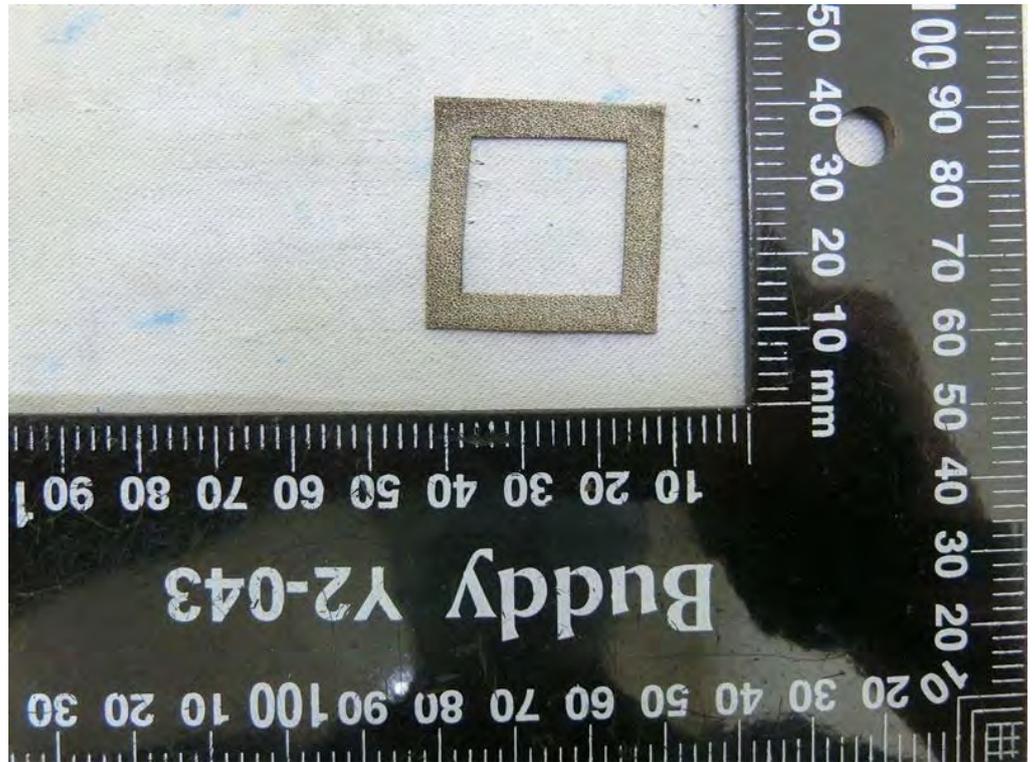


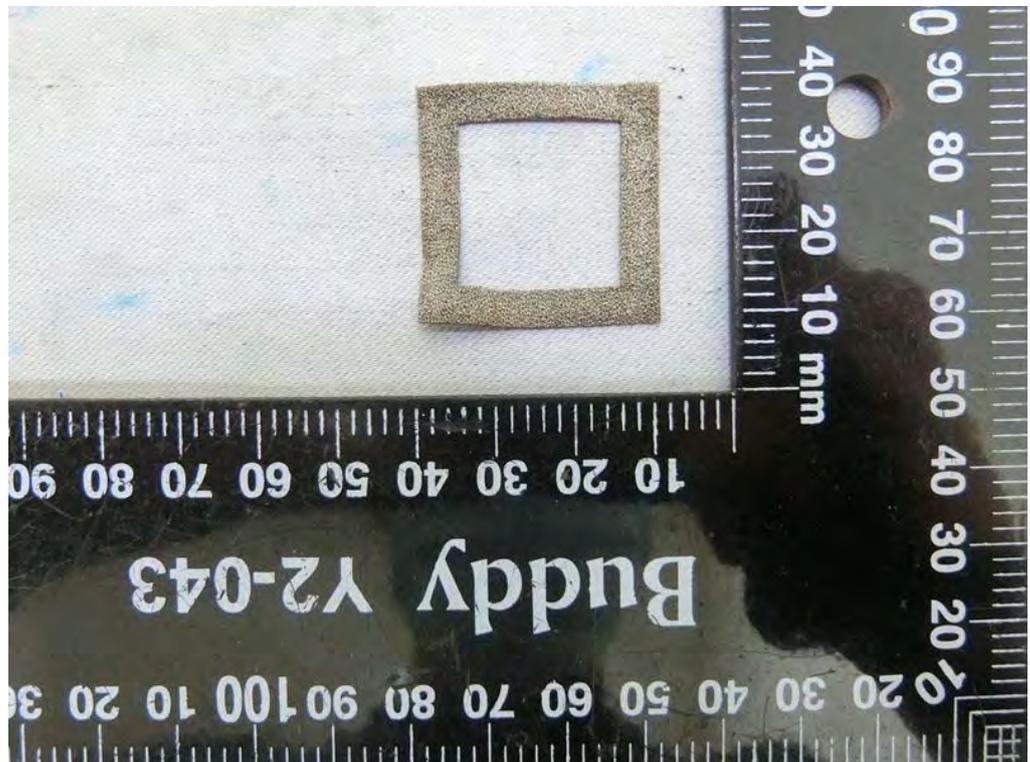
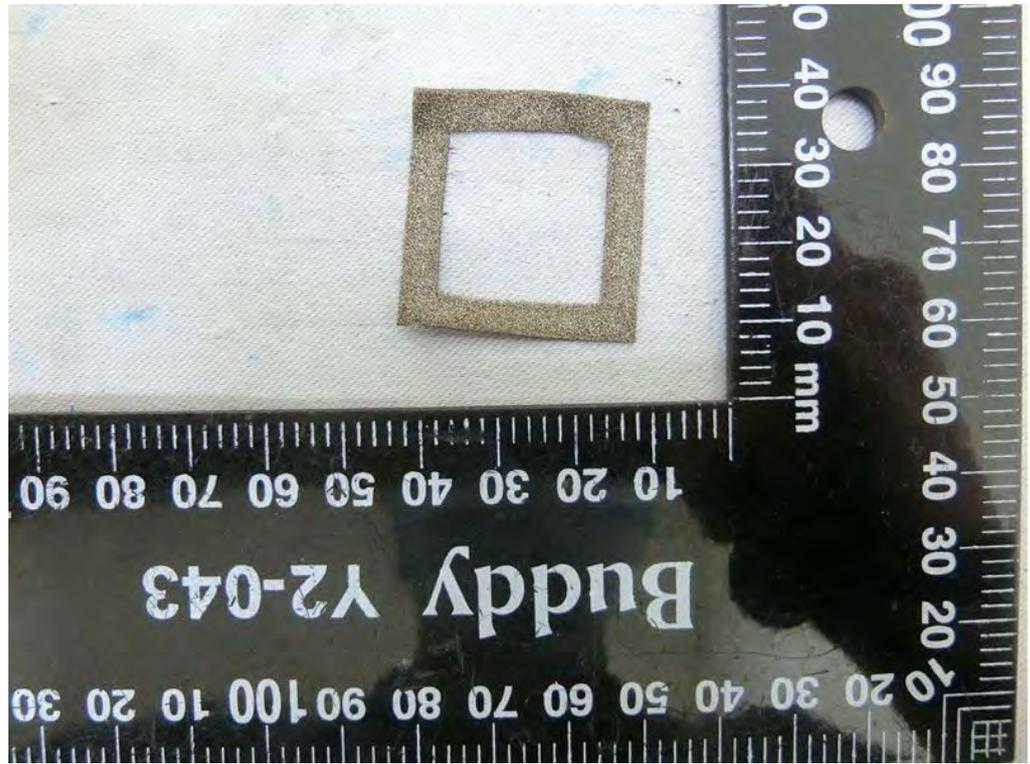


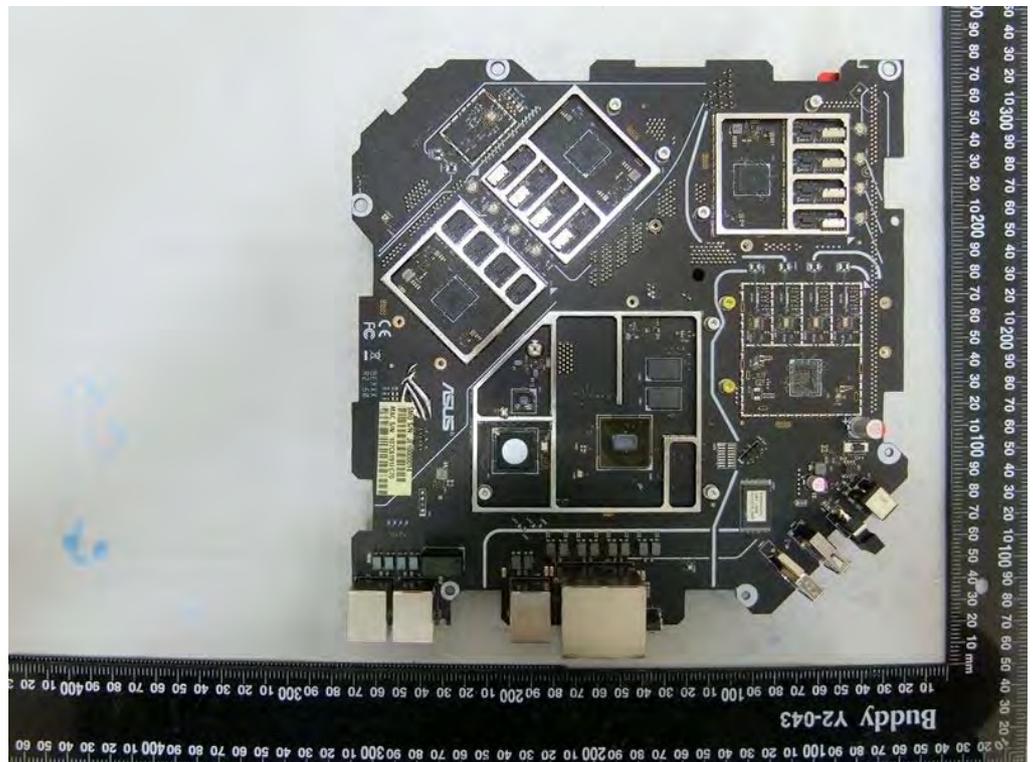
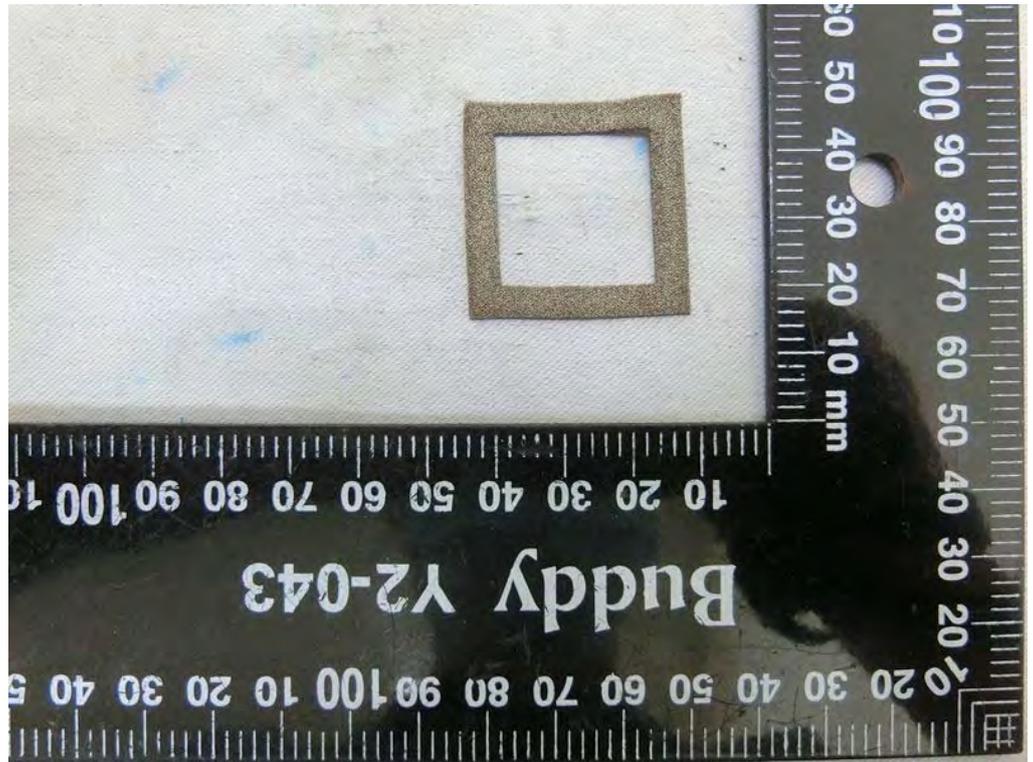


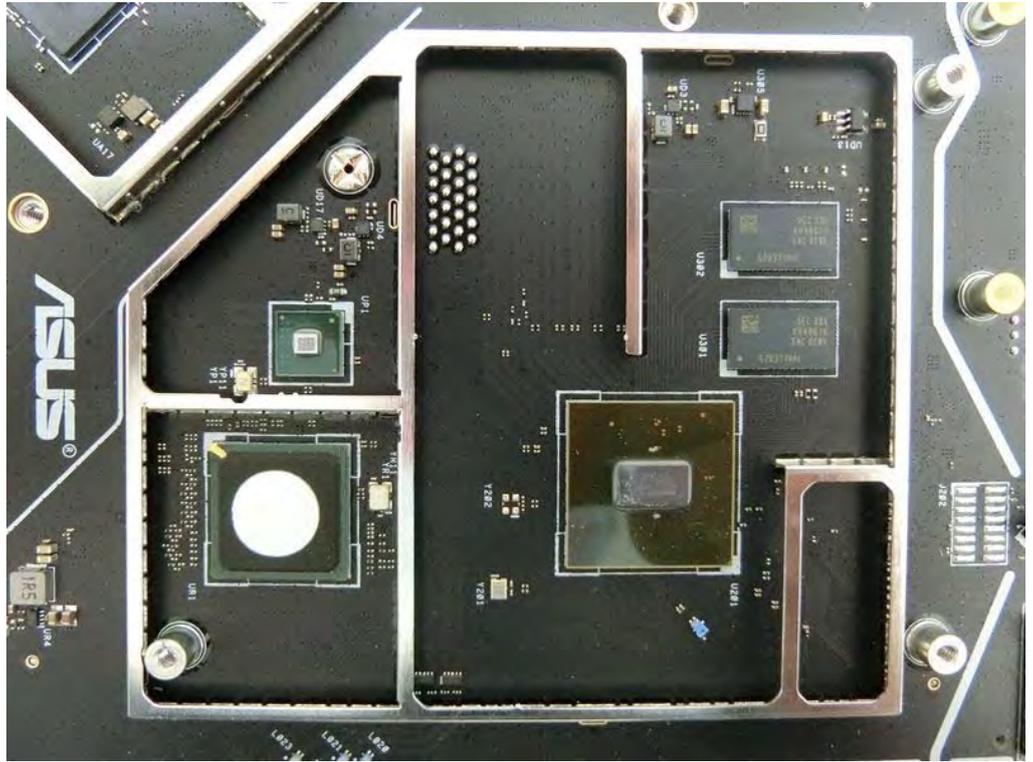


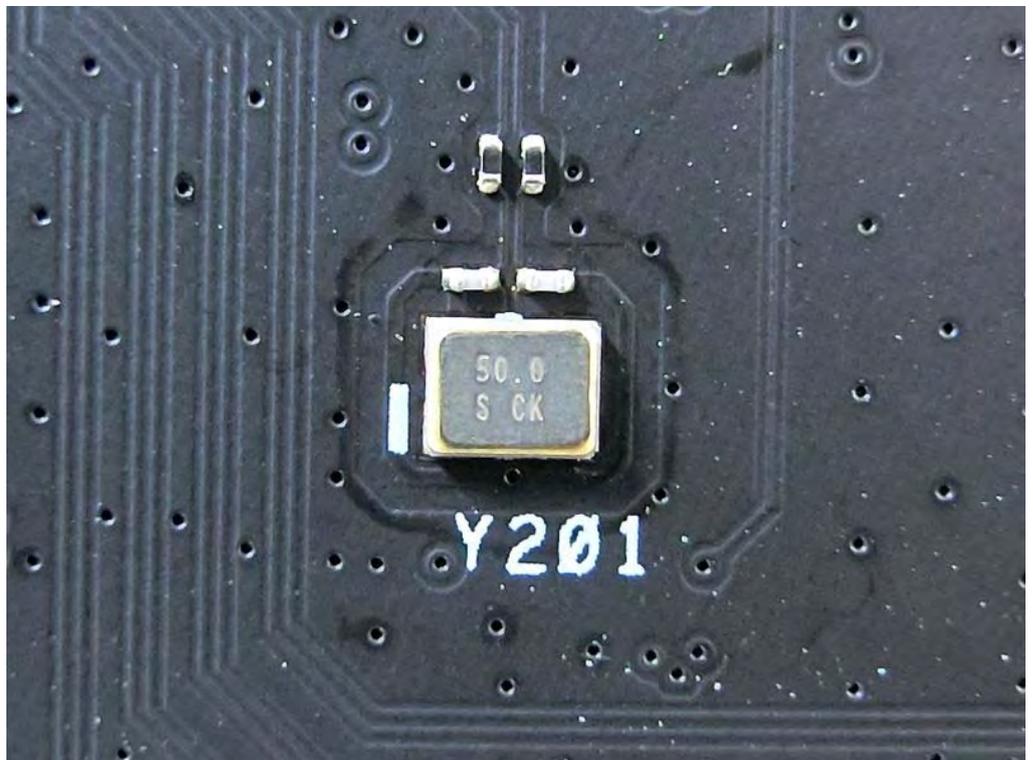


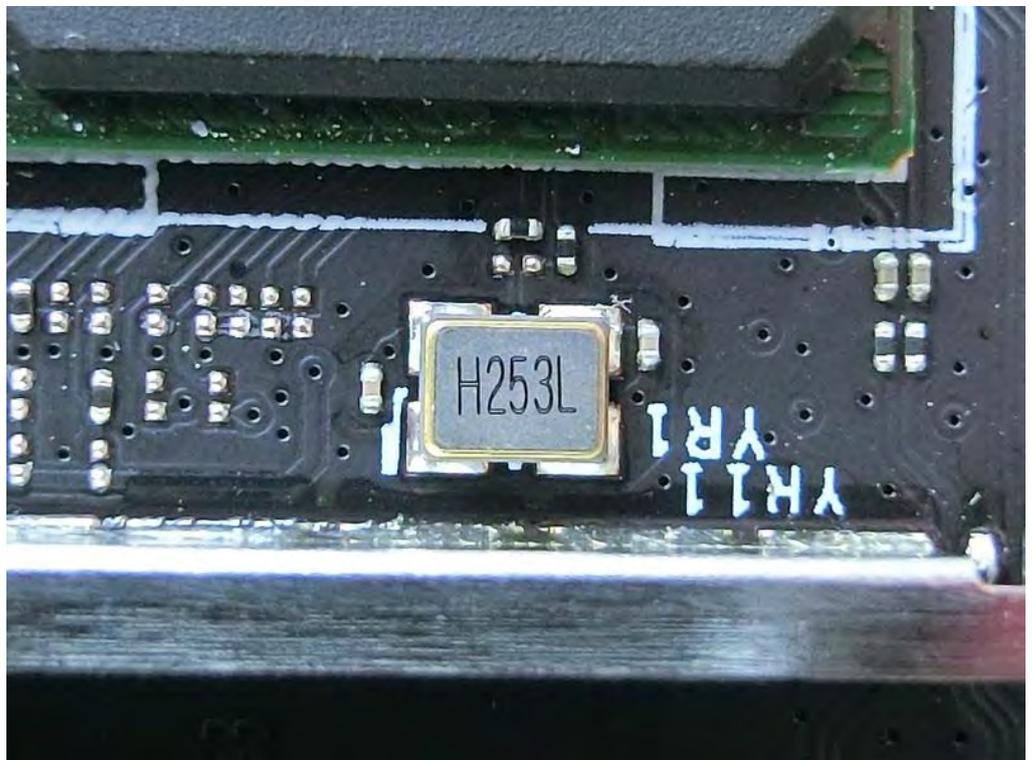






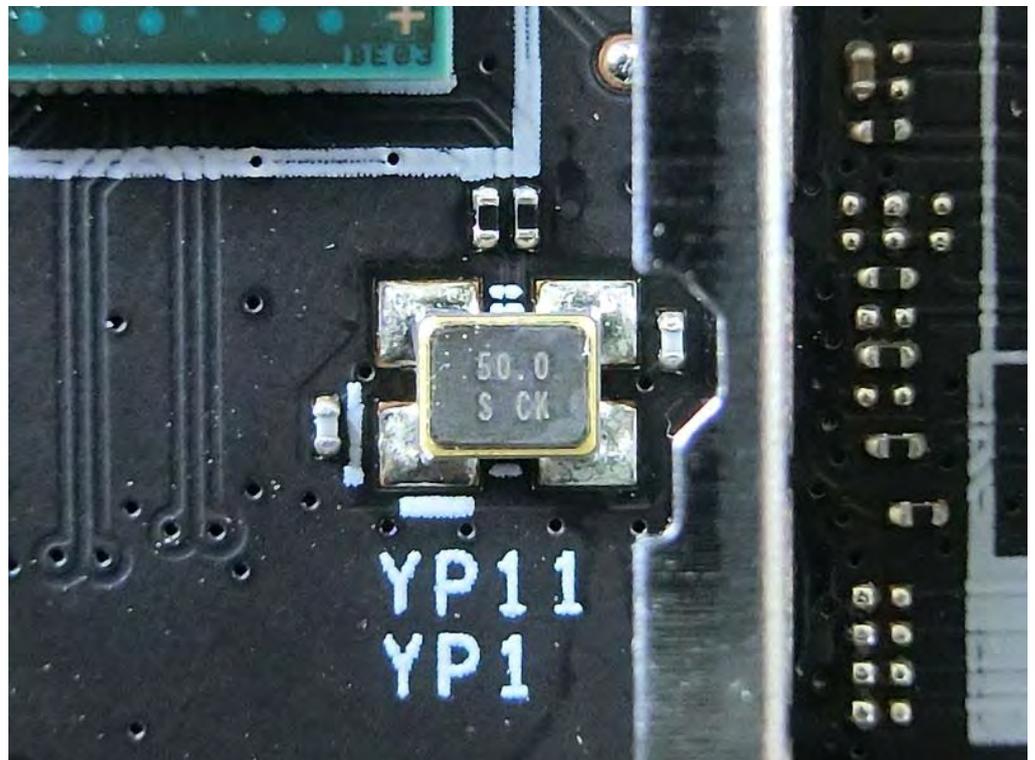


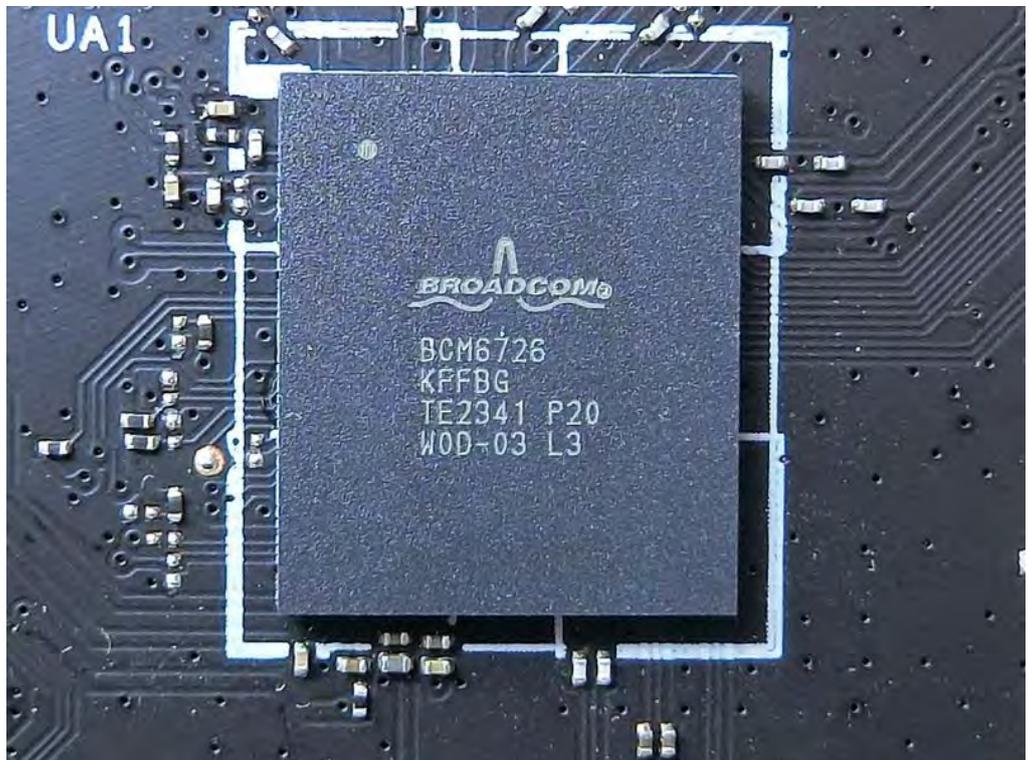
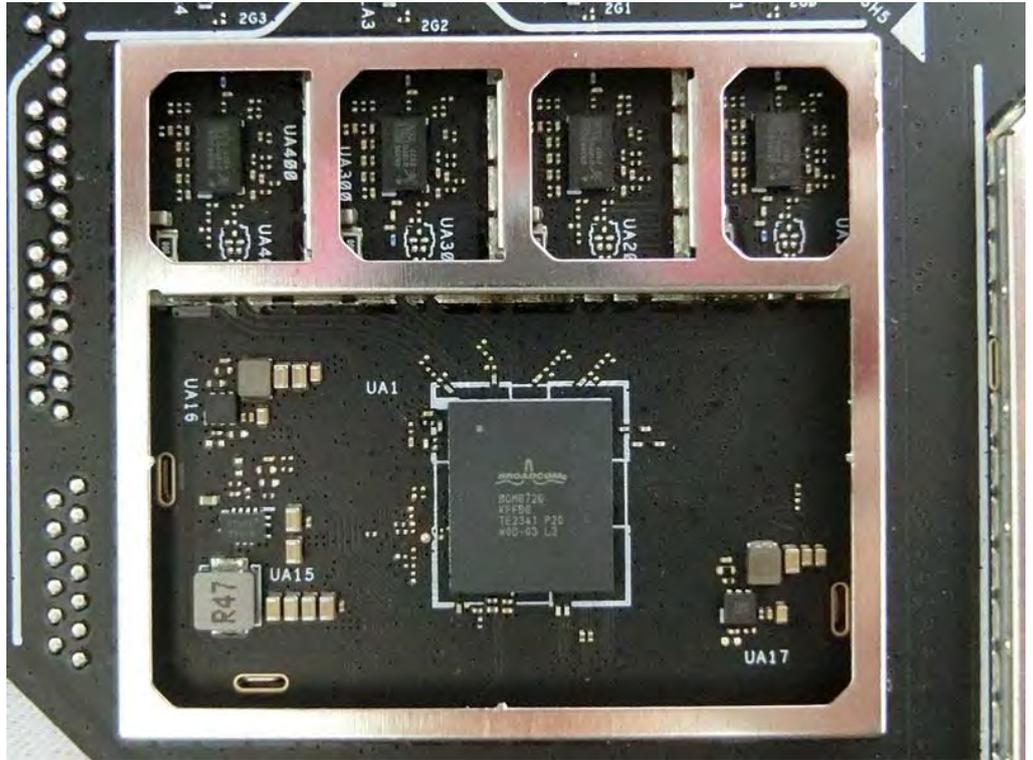


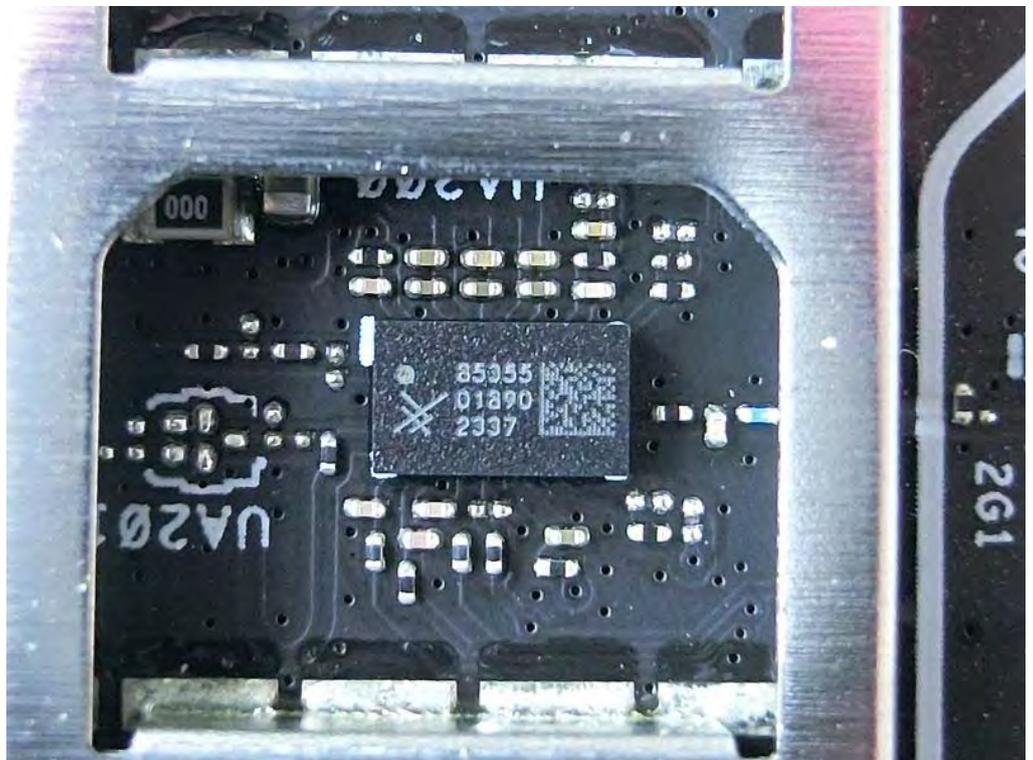
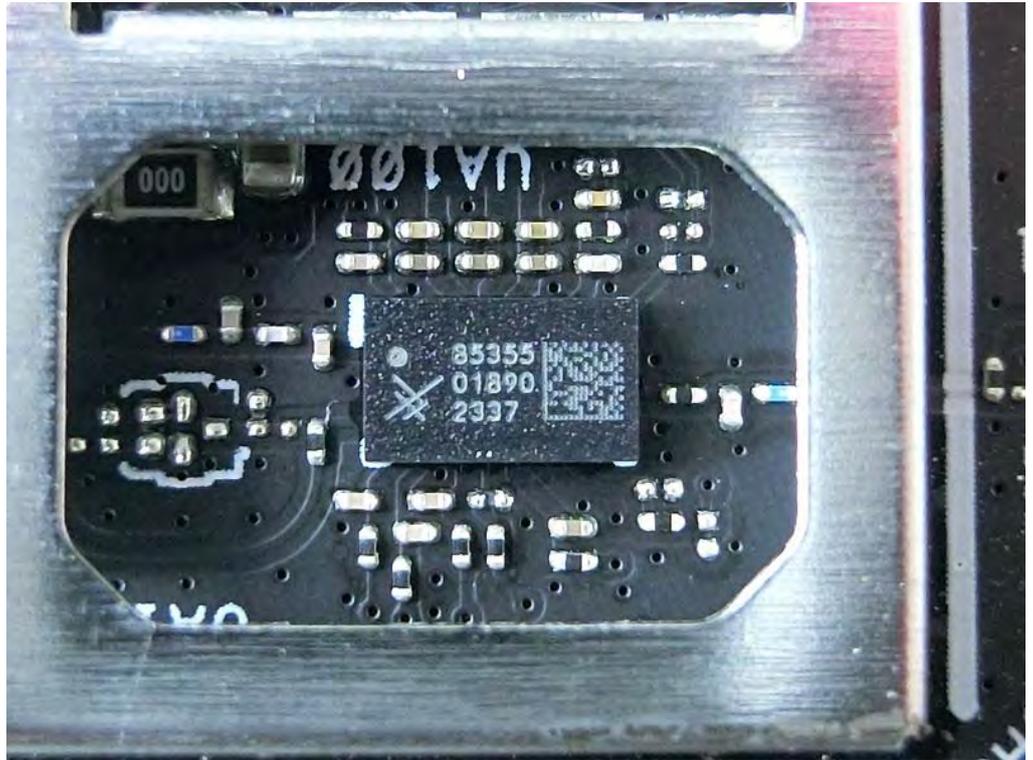


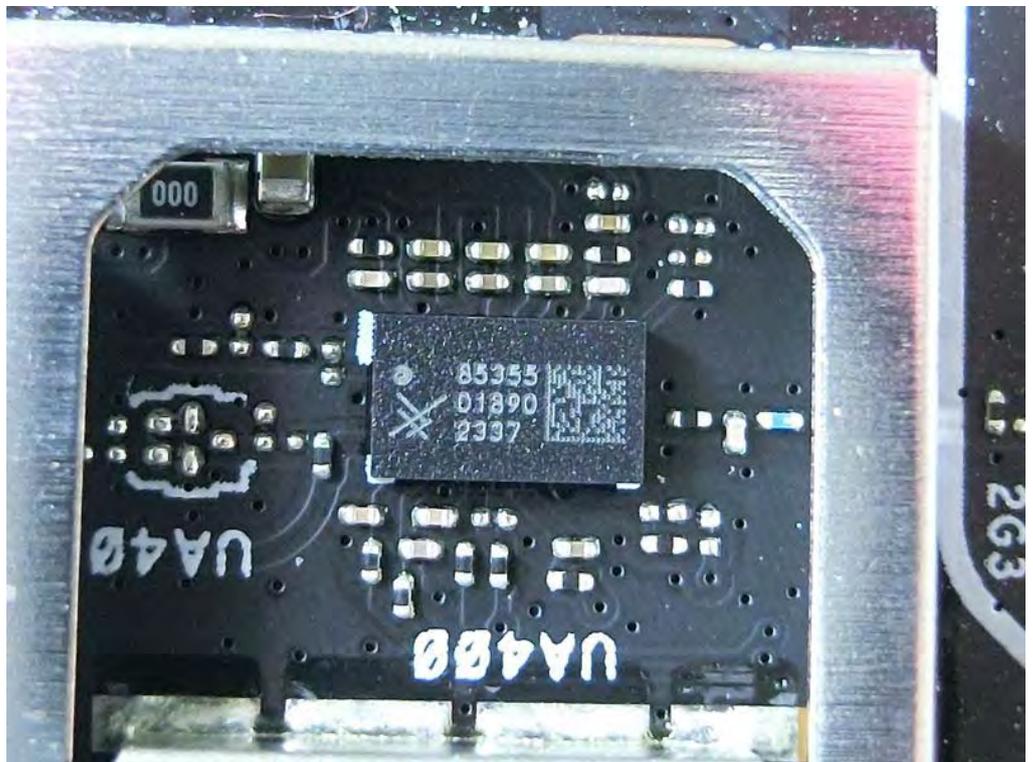
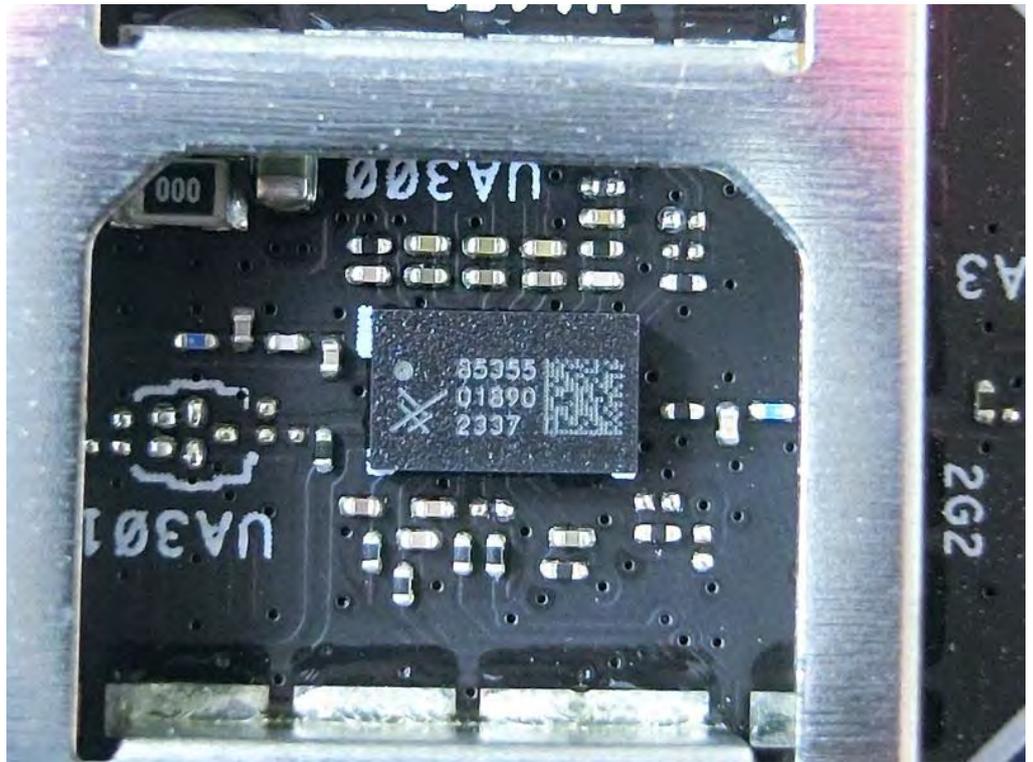
Integrated circuit packaging  
(Location: UP1)

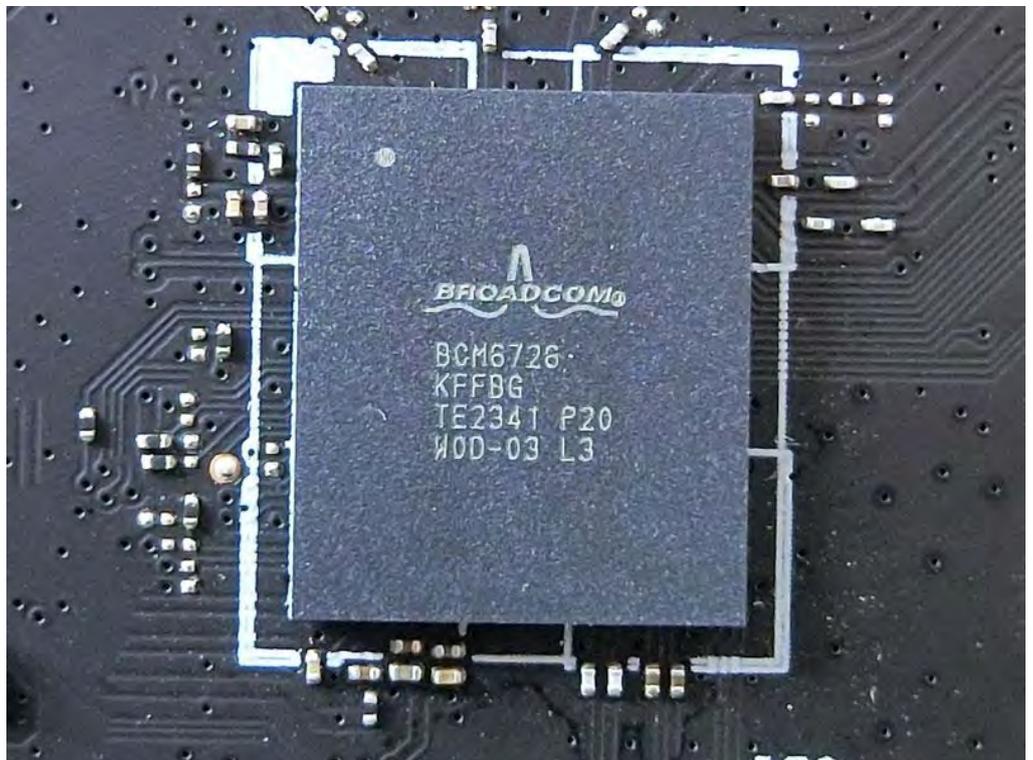
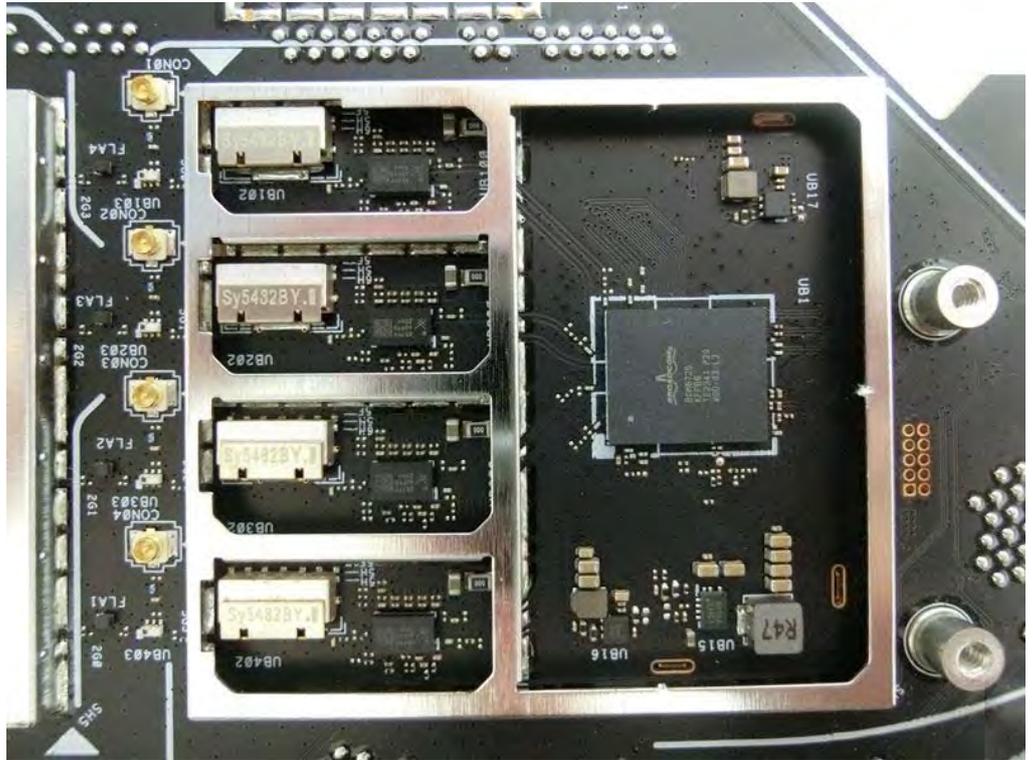
FCBGA Package  
(Used for EUT 1~2)

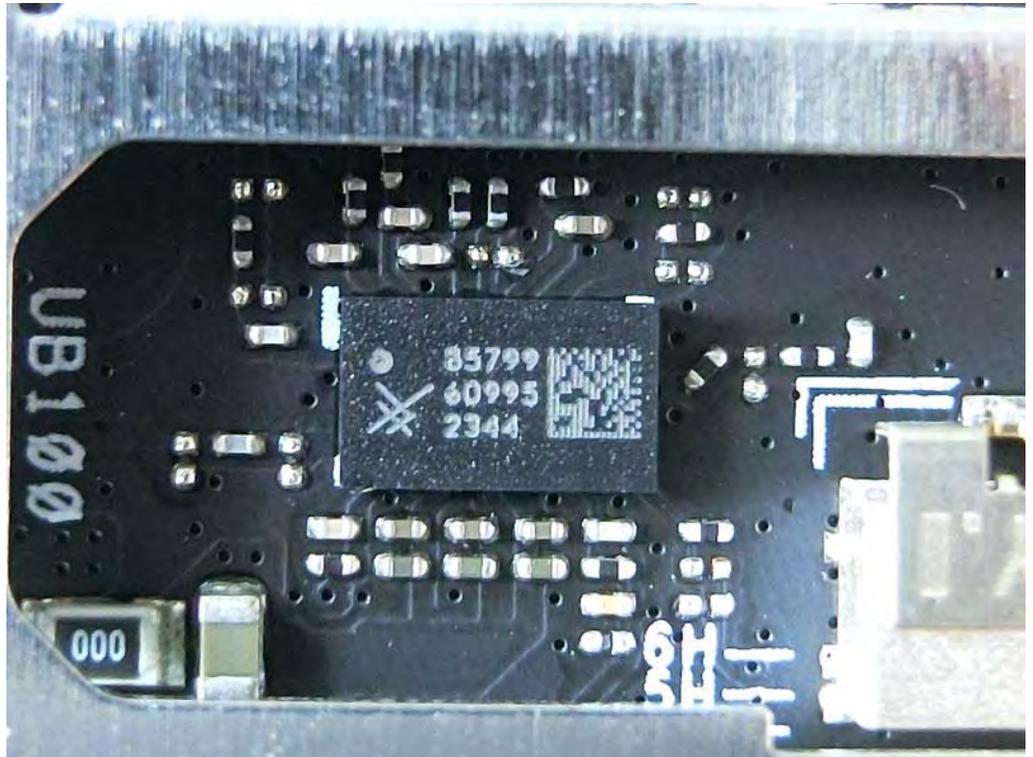
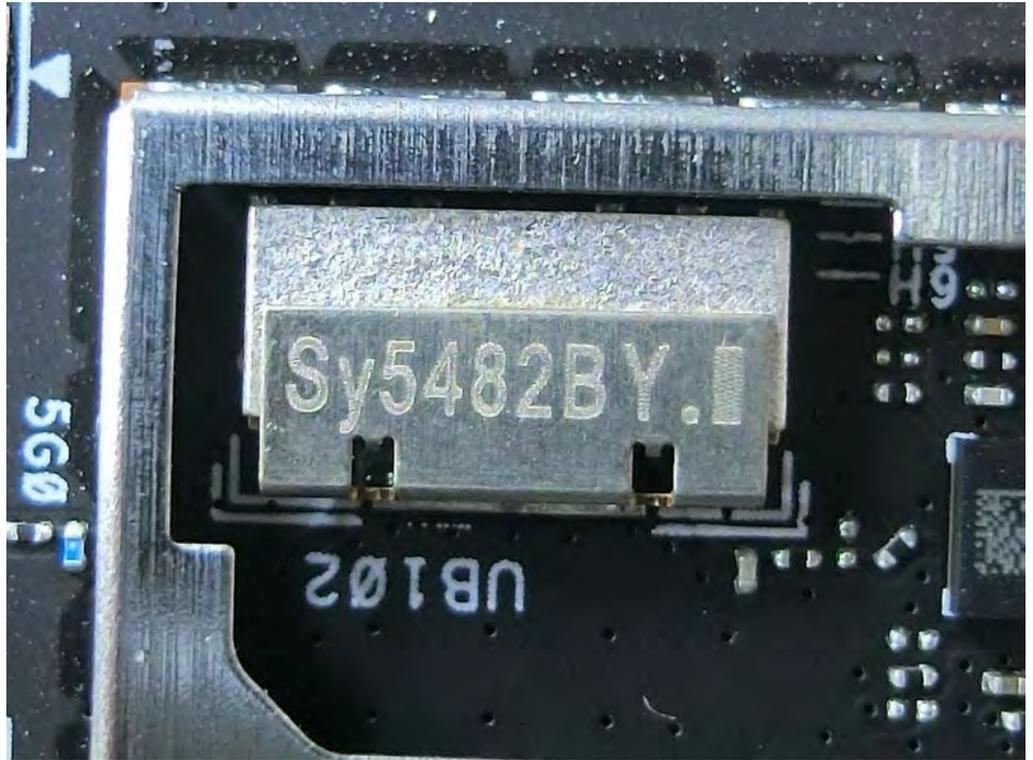


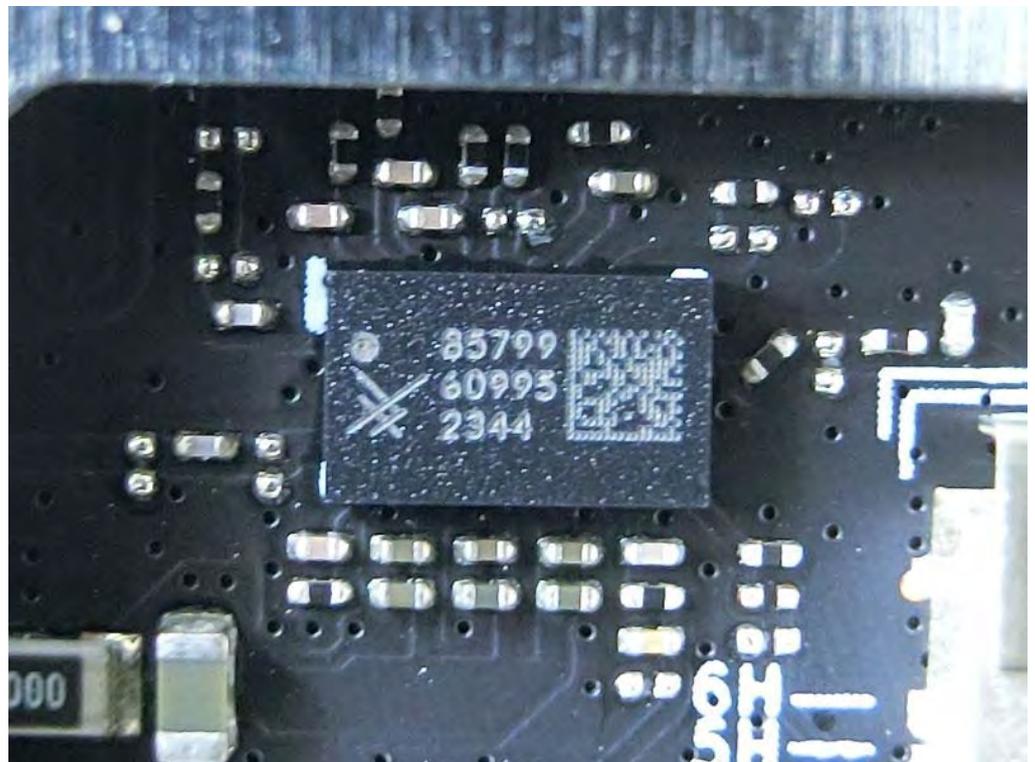


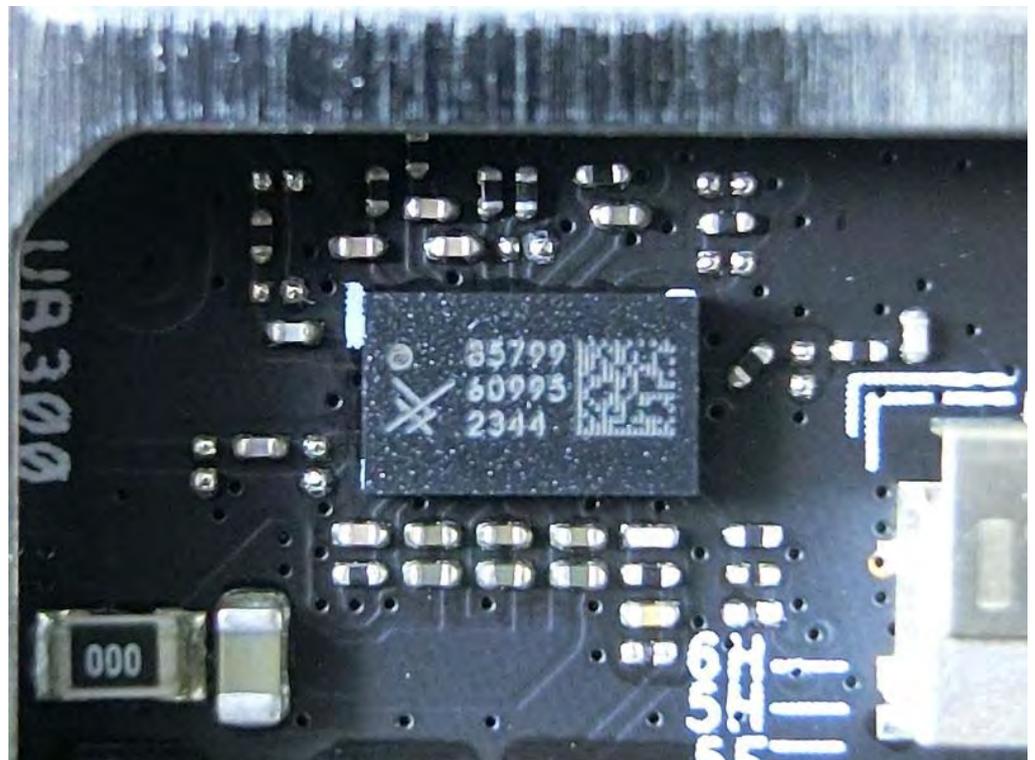
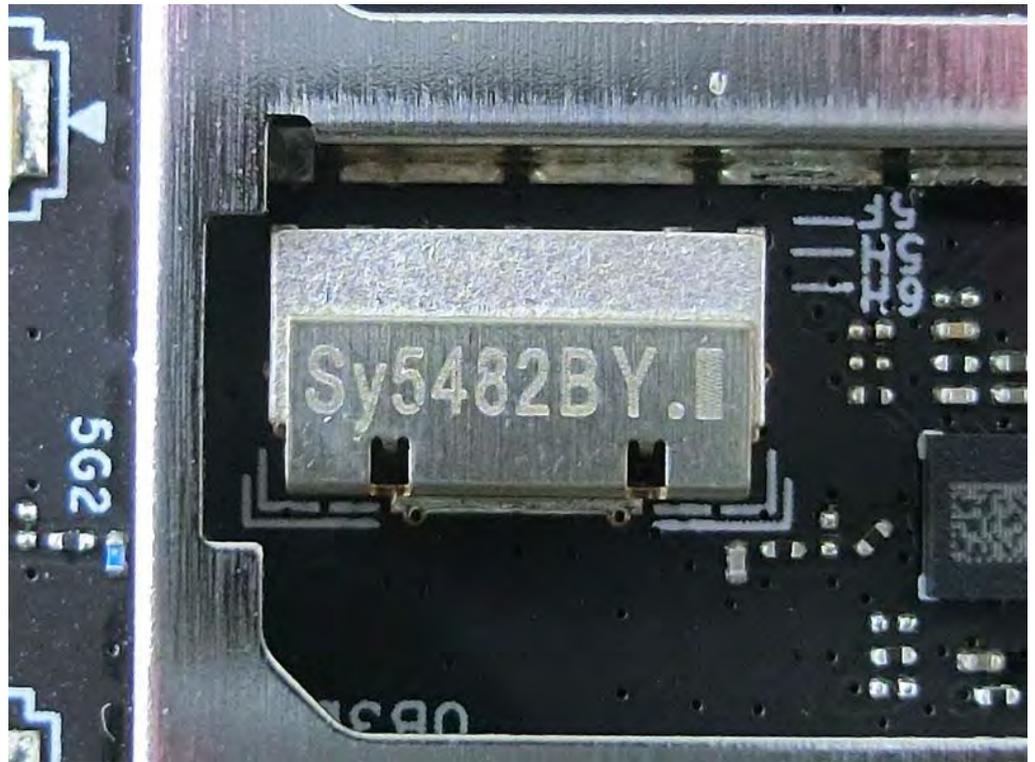


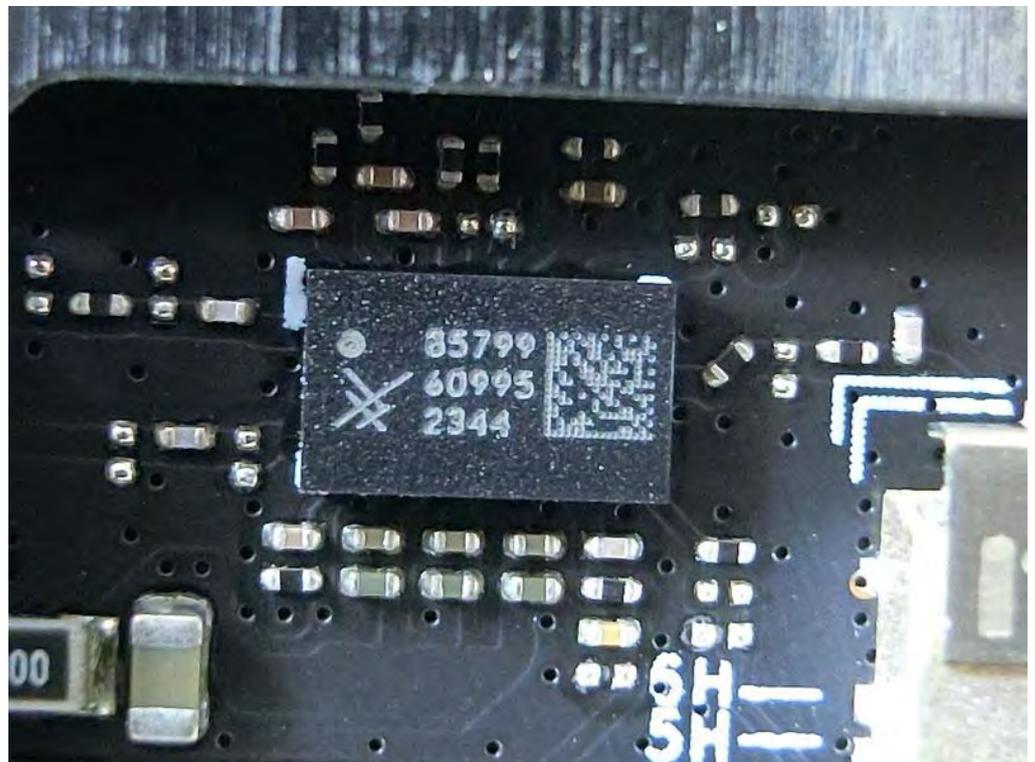
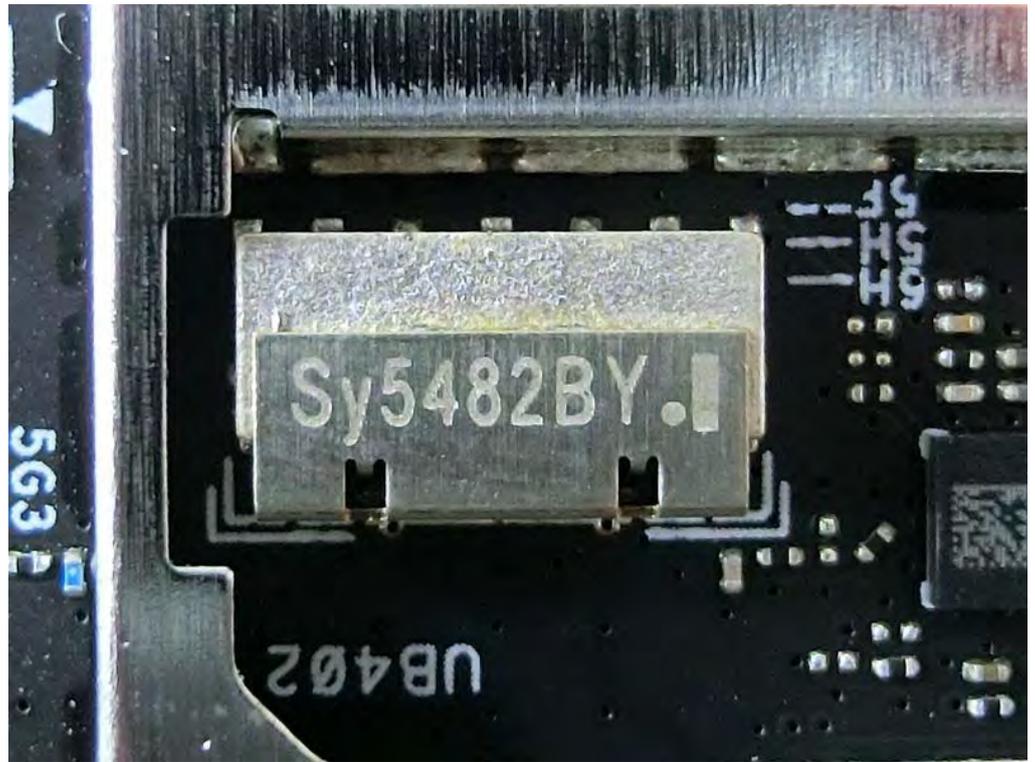


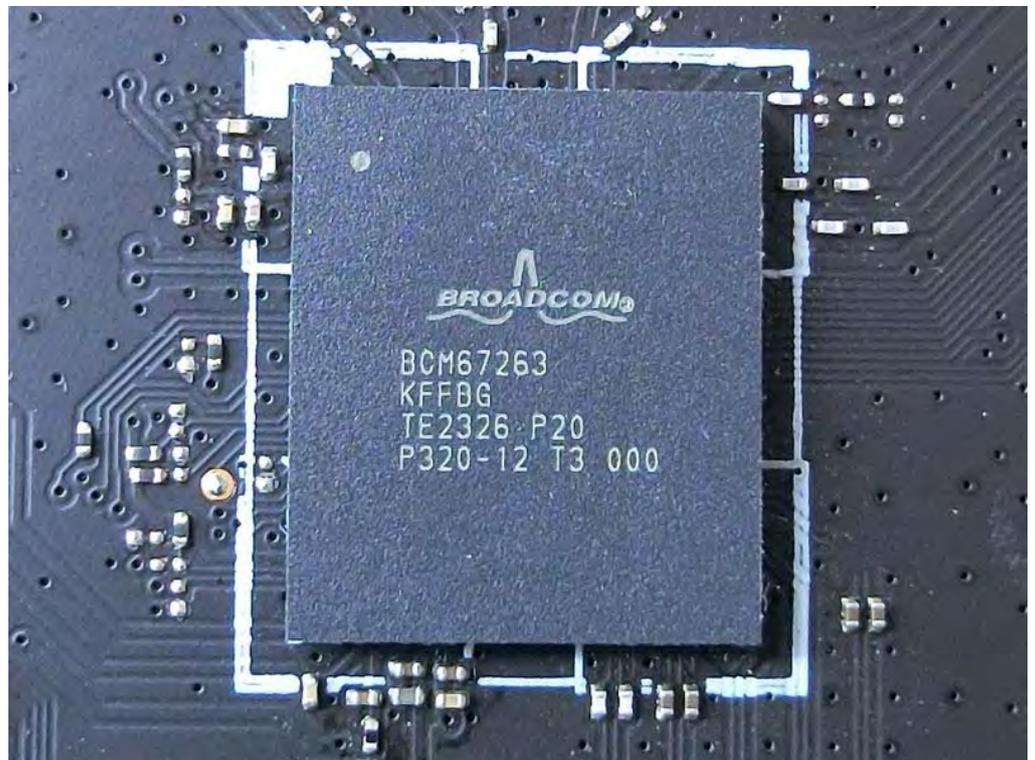
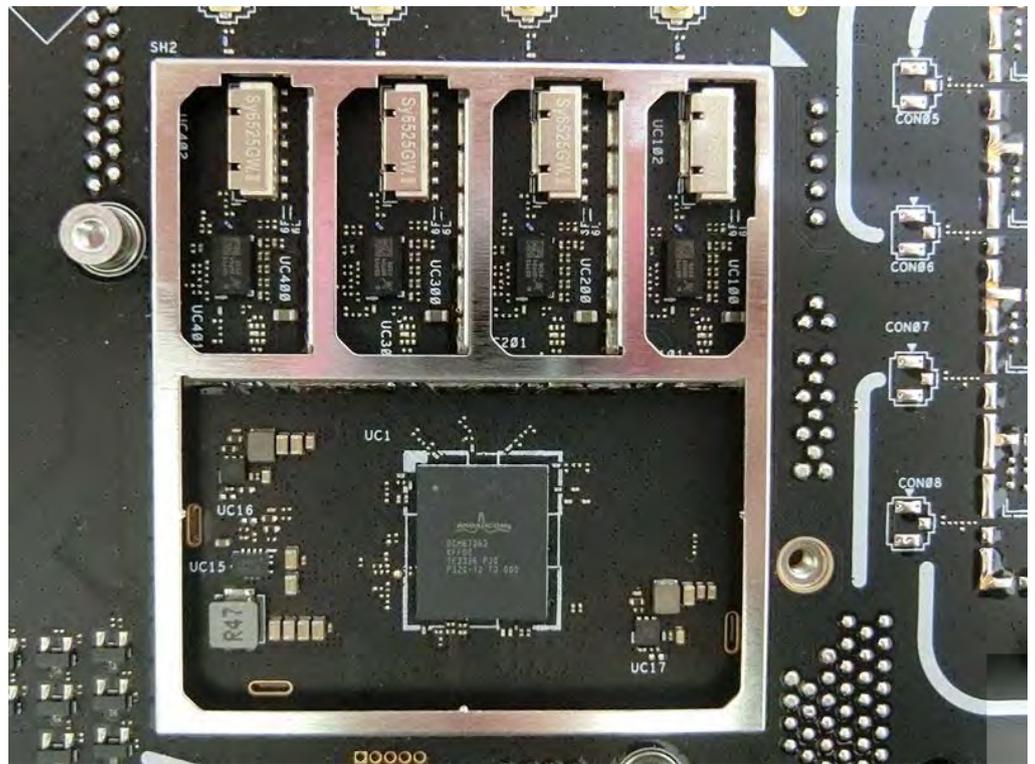


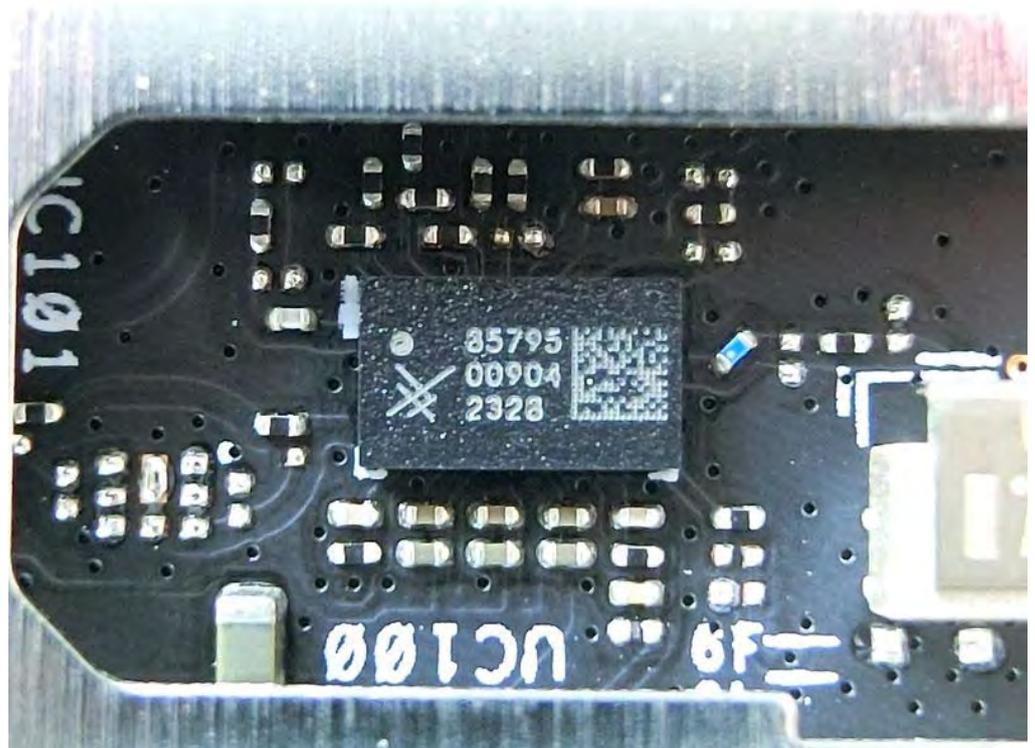


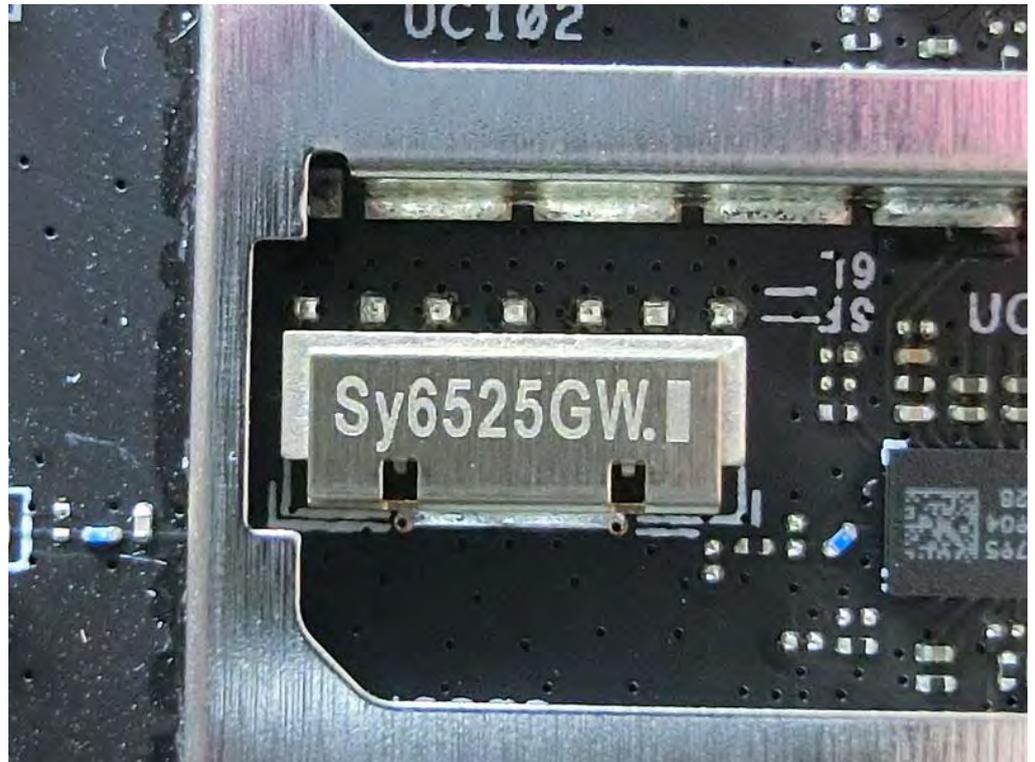


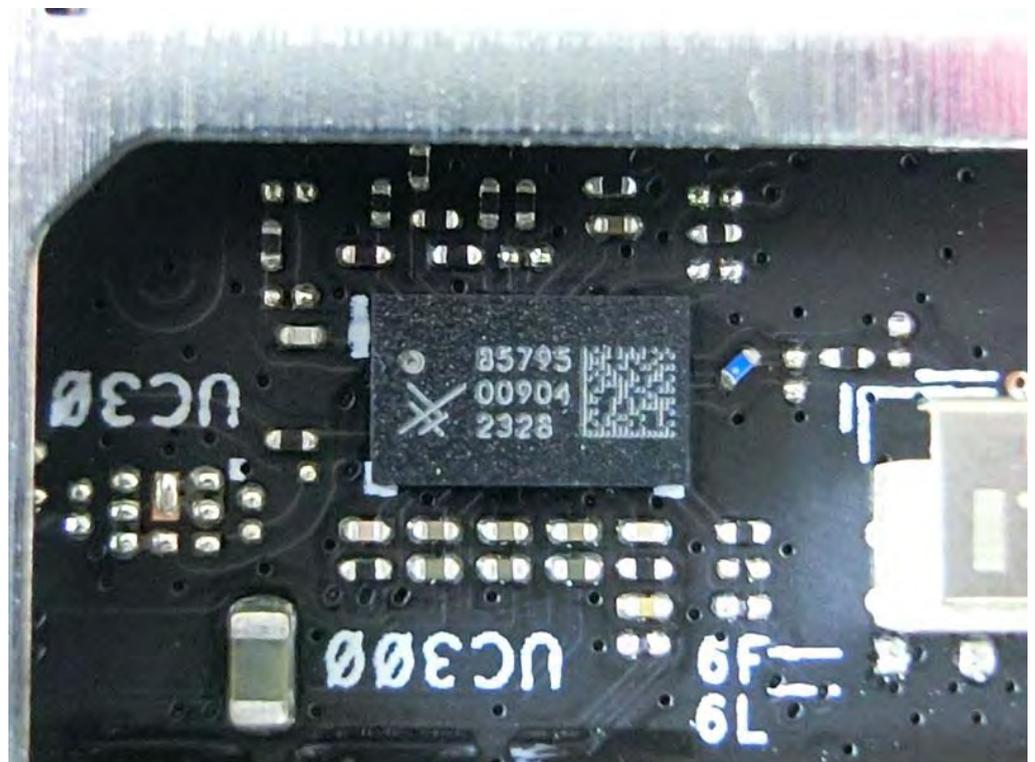
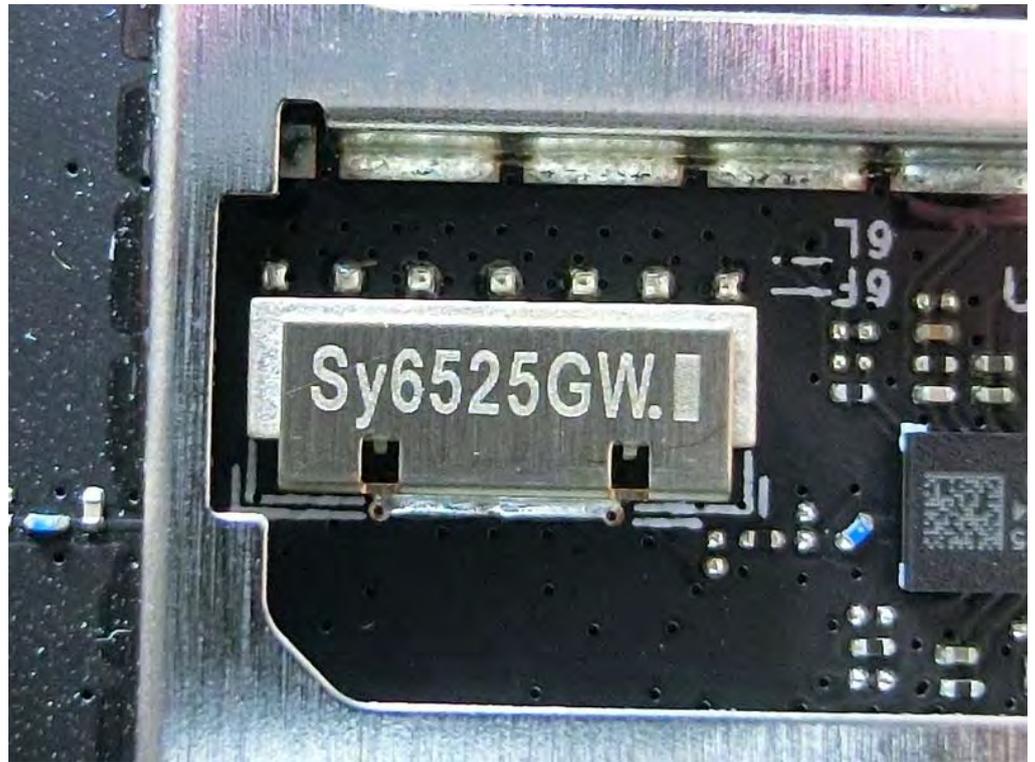


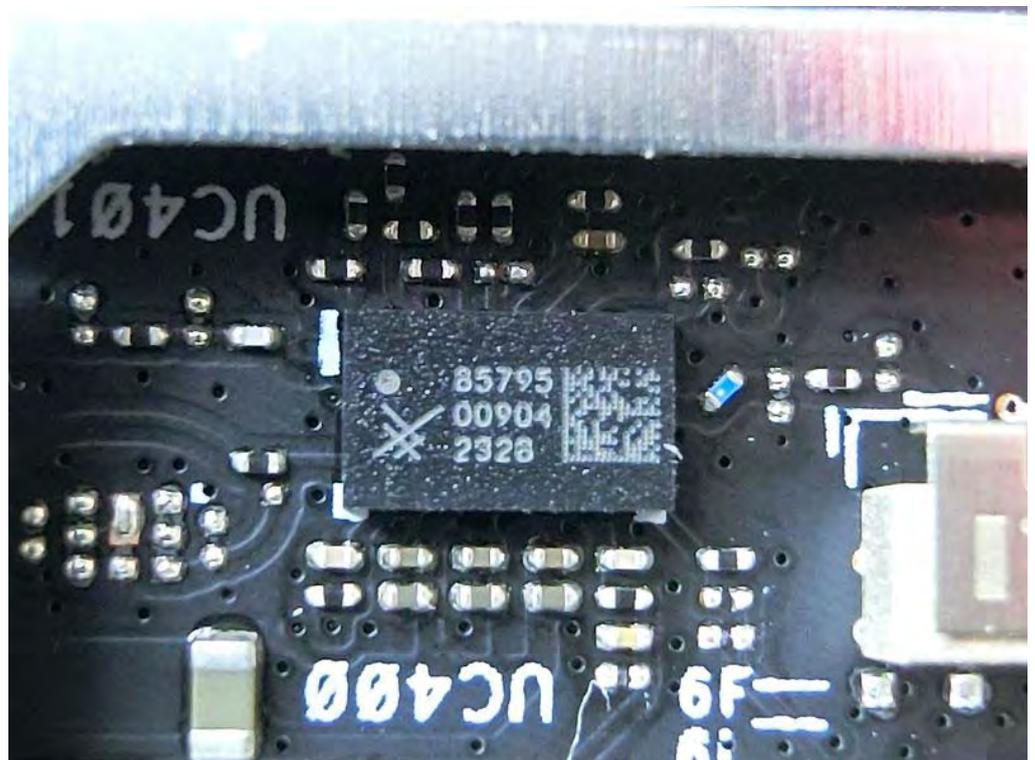
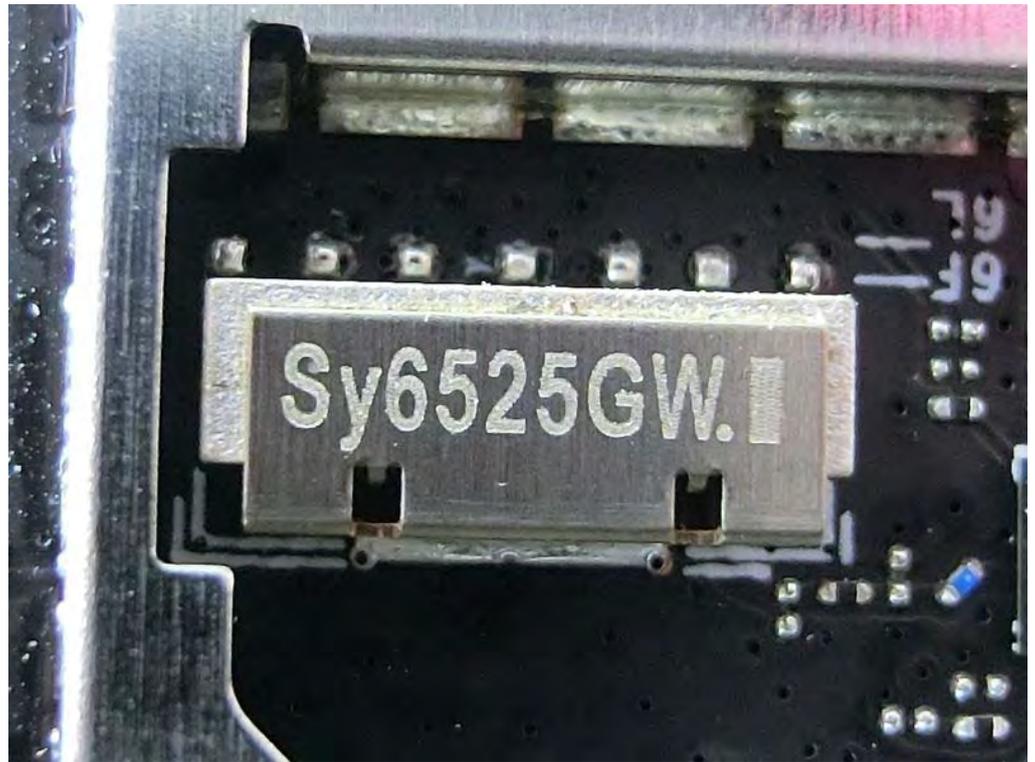


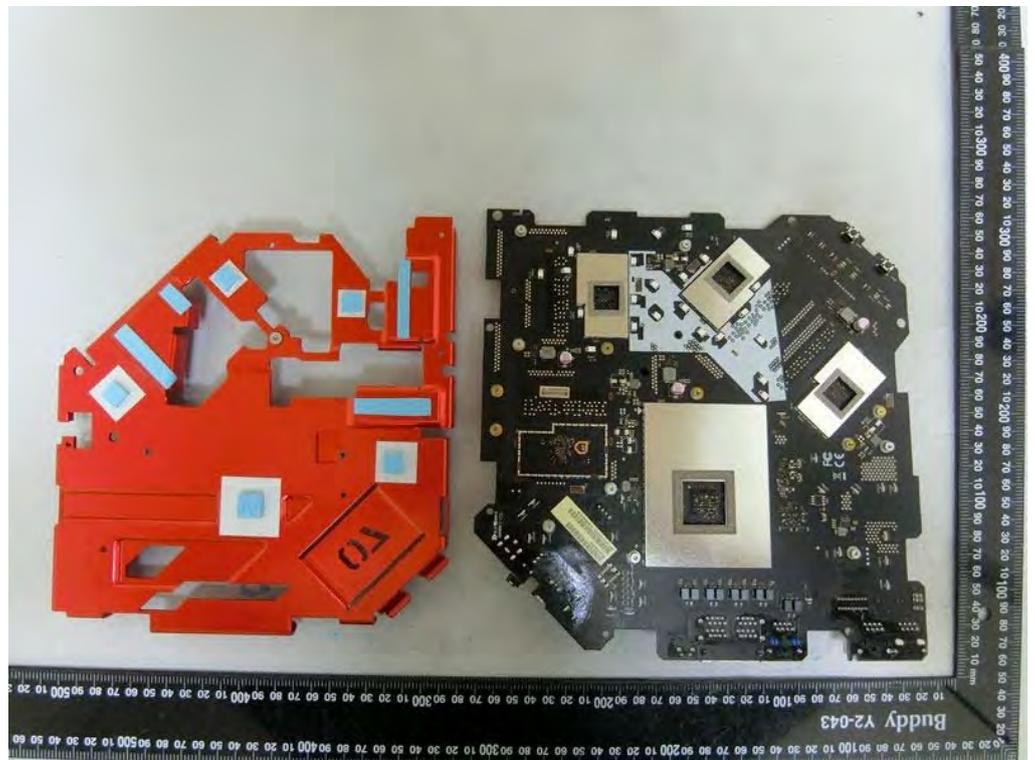
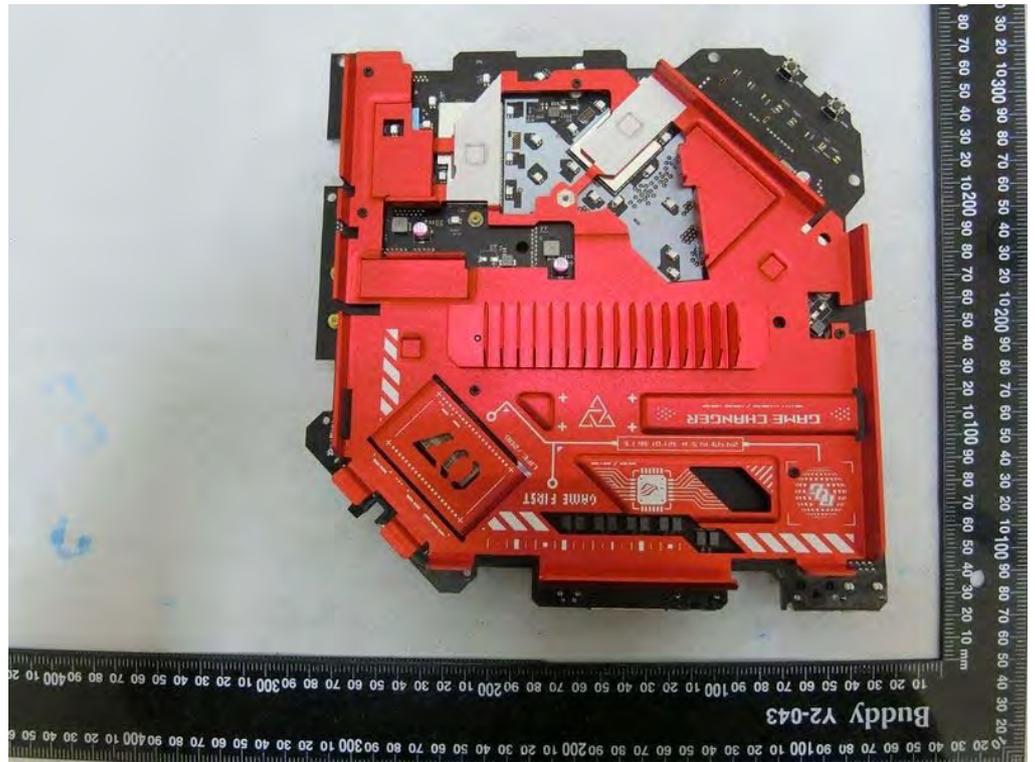


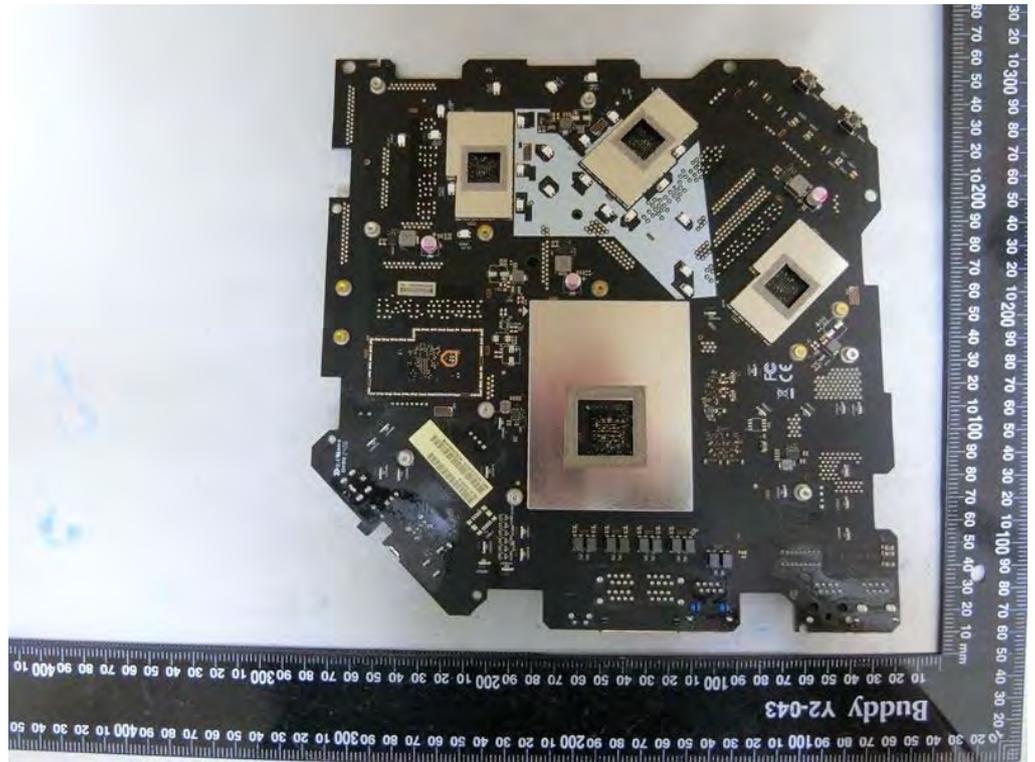


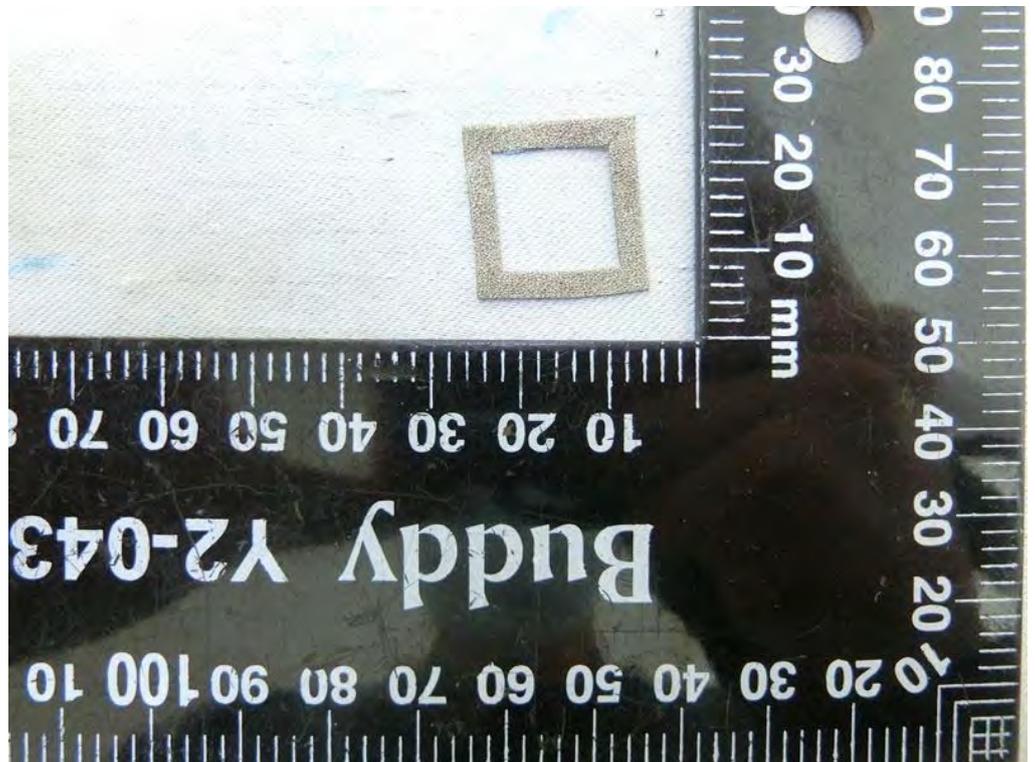
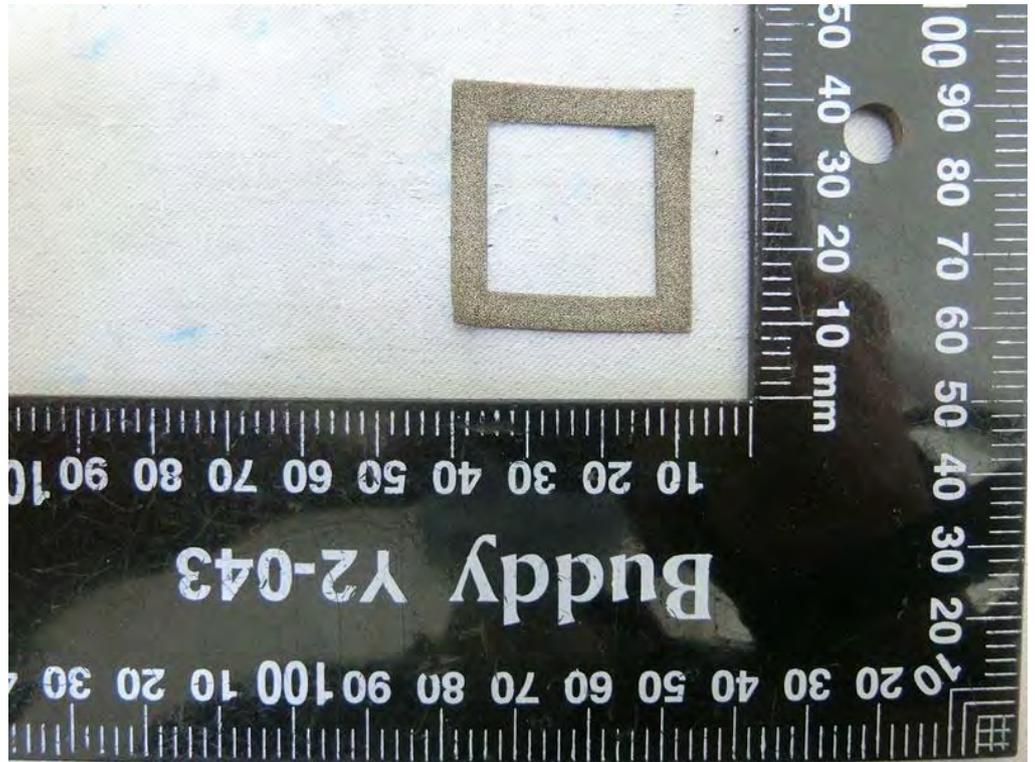


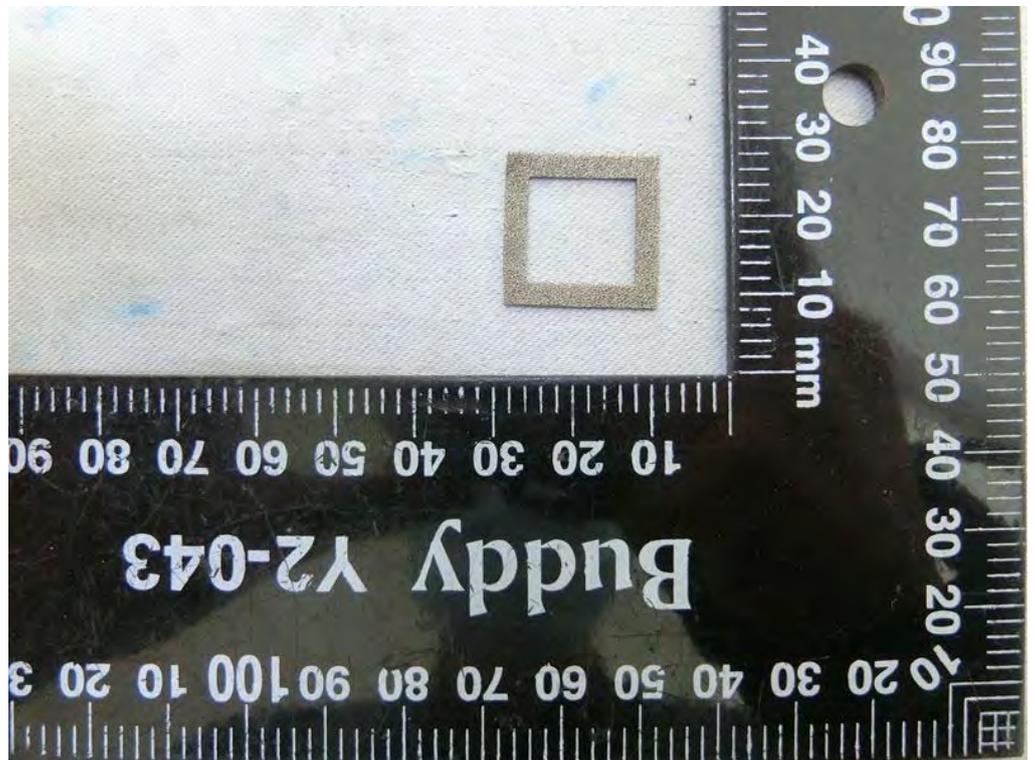


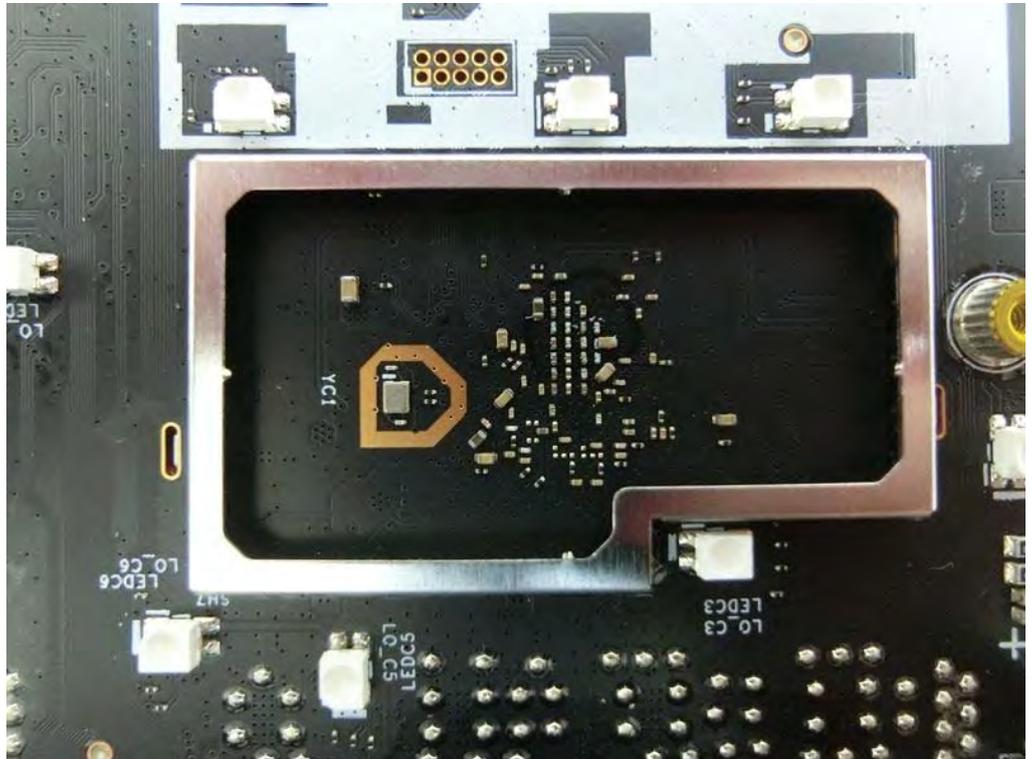
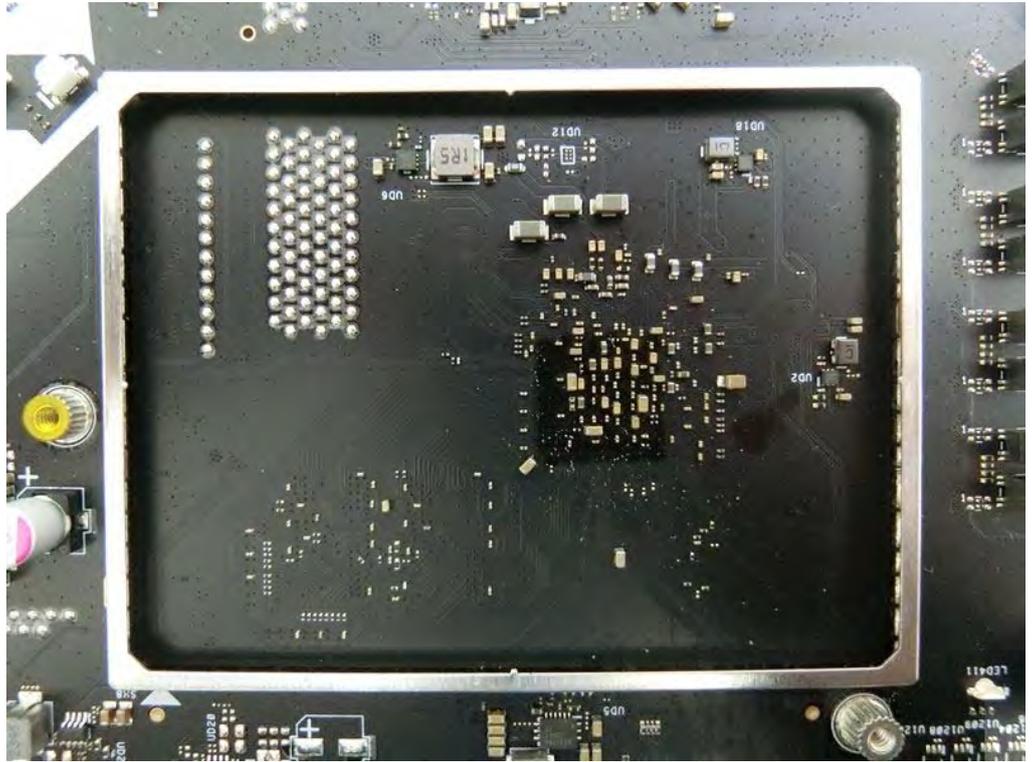


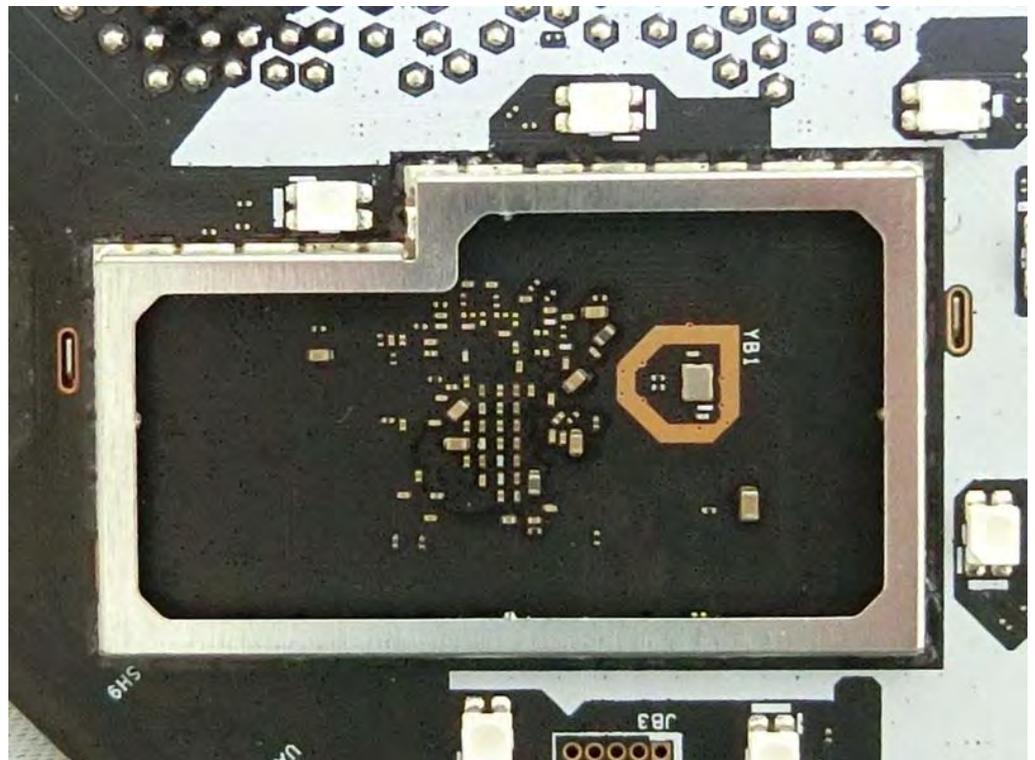
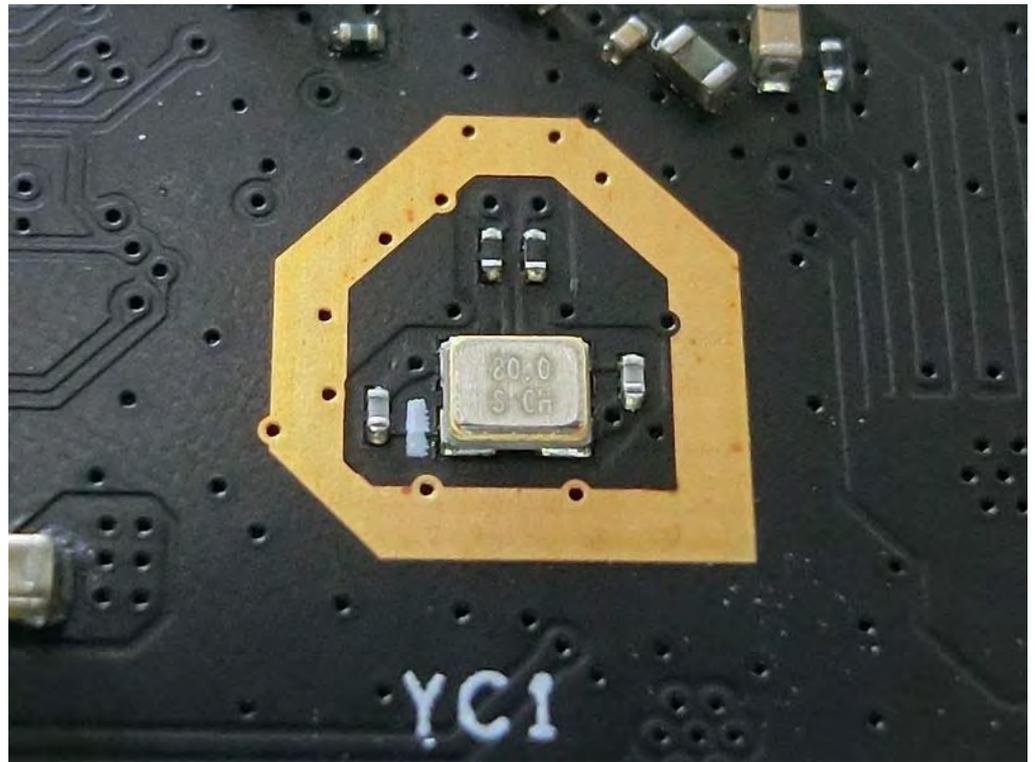


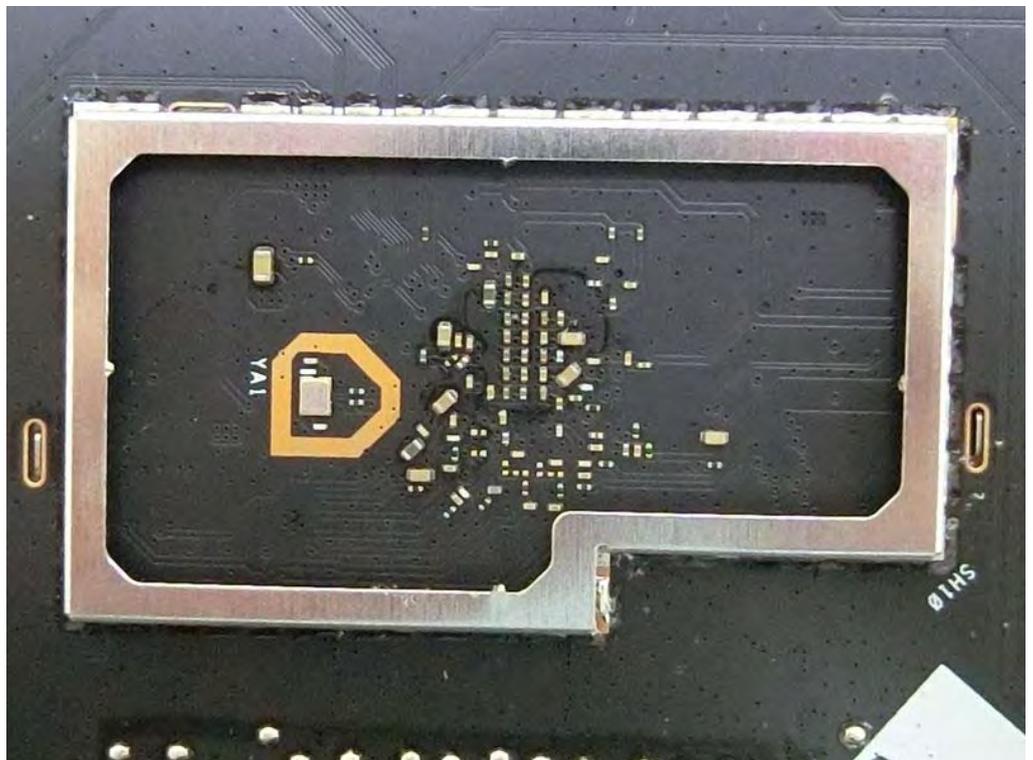
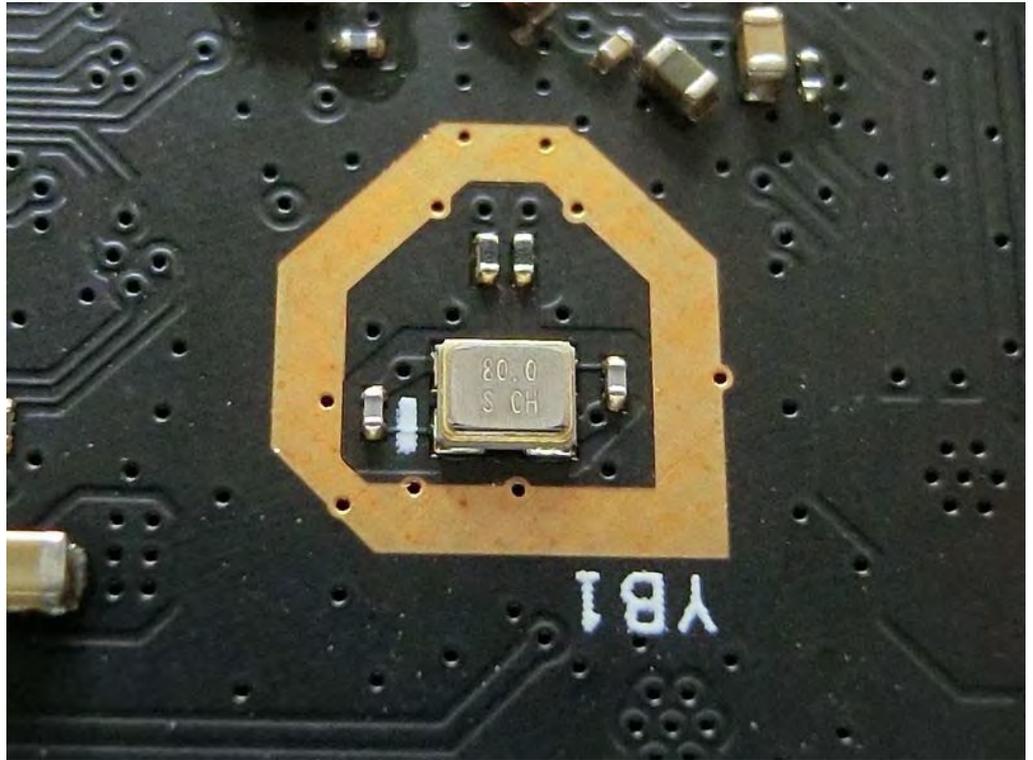






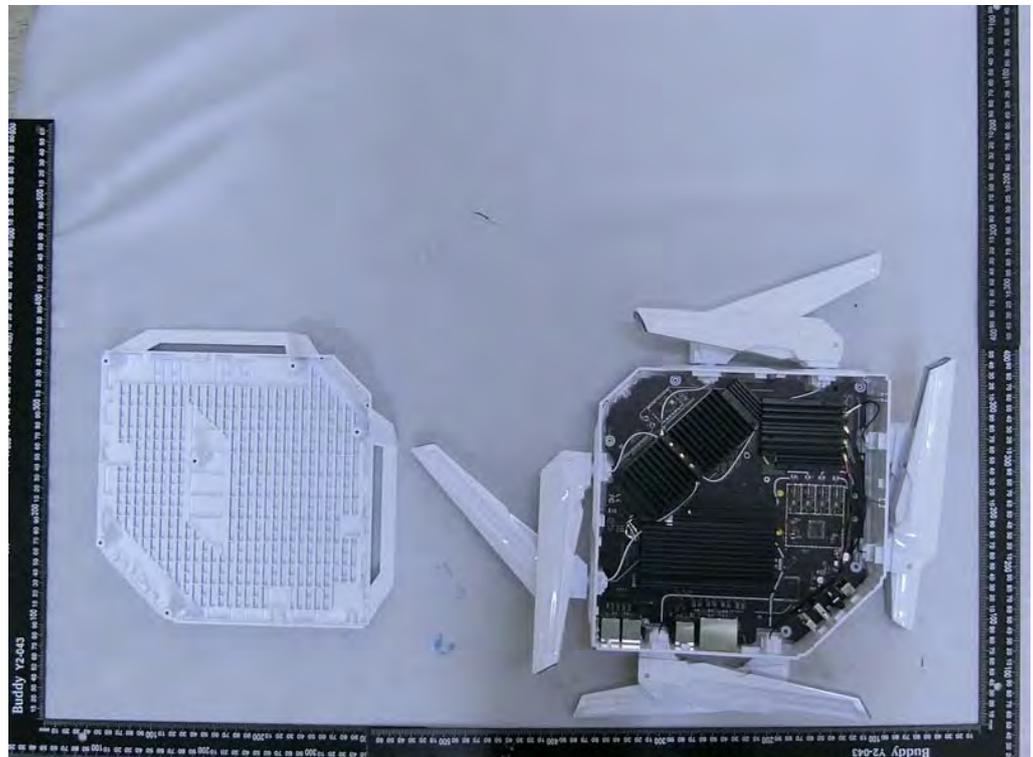


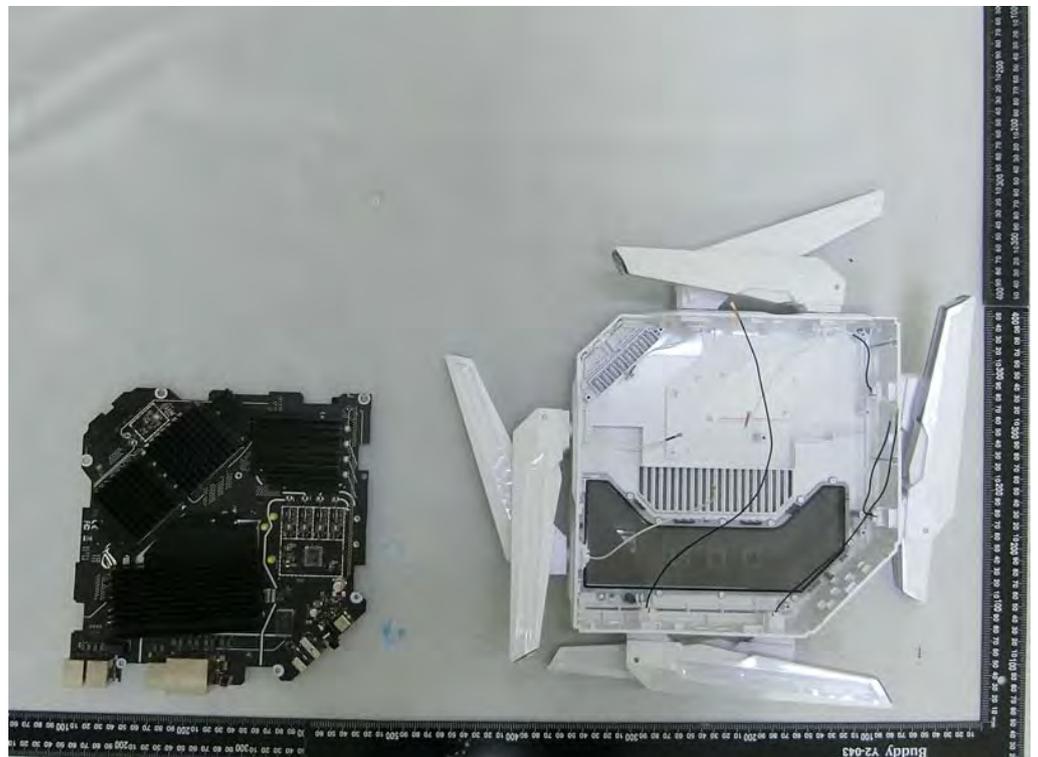
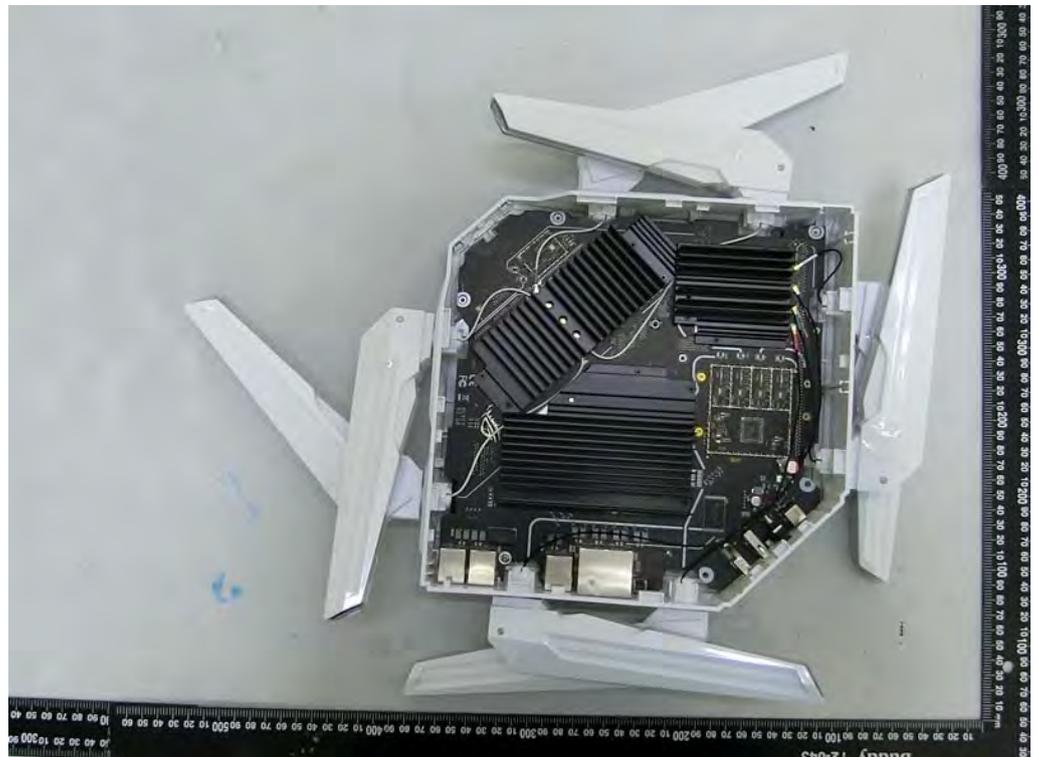


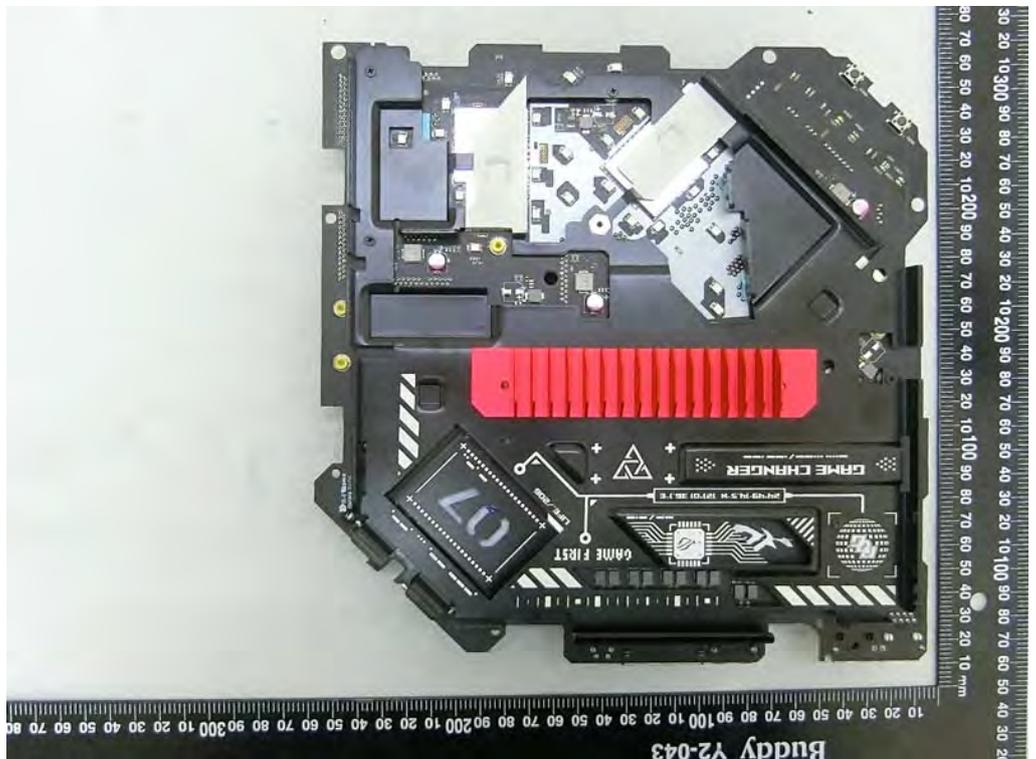
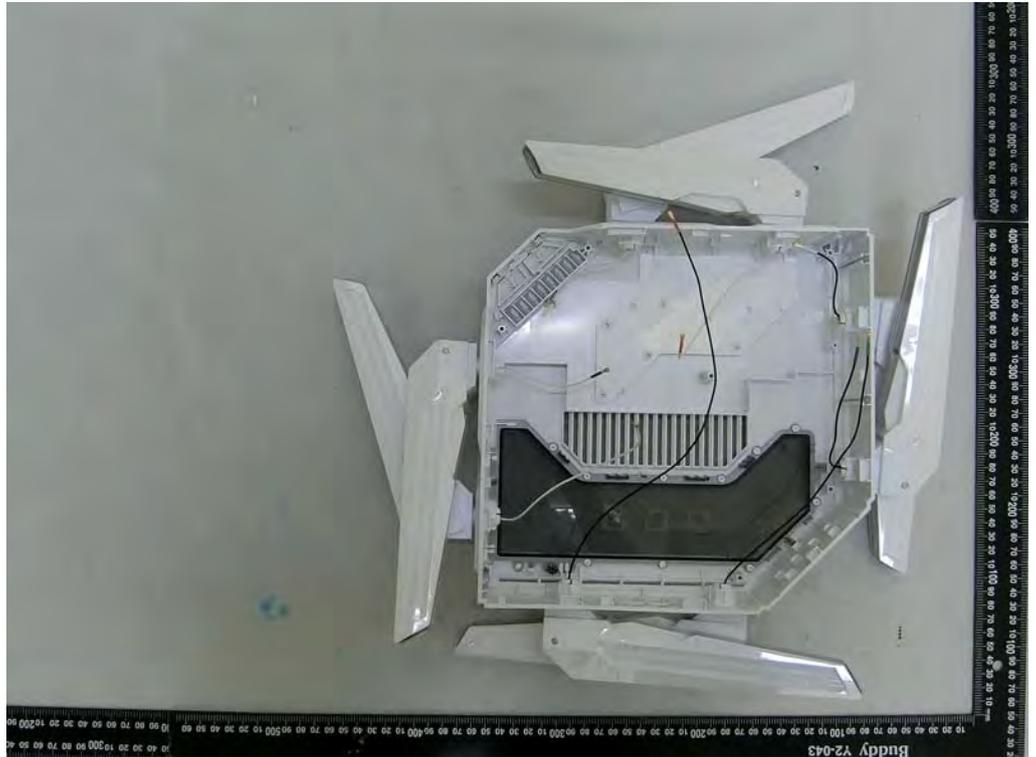




EUT 2, EUT 4



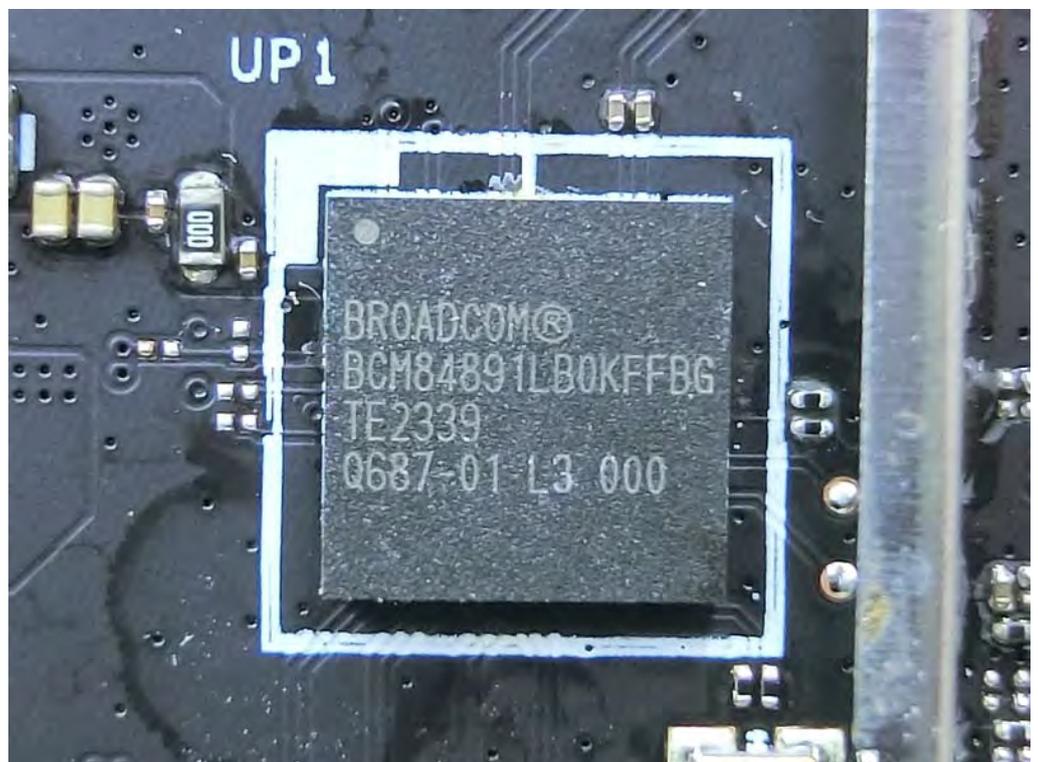






Integrated circuit packaging  
(Location: UP1)

FCFBGA Package  
(Used for EUT 3~5)



EUT 5

