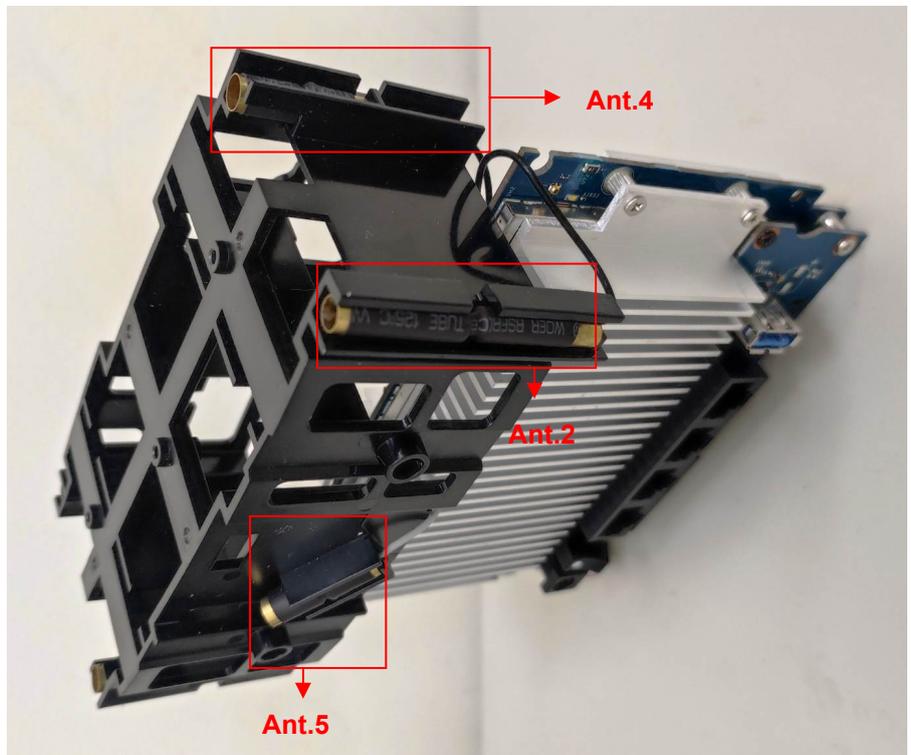
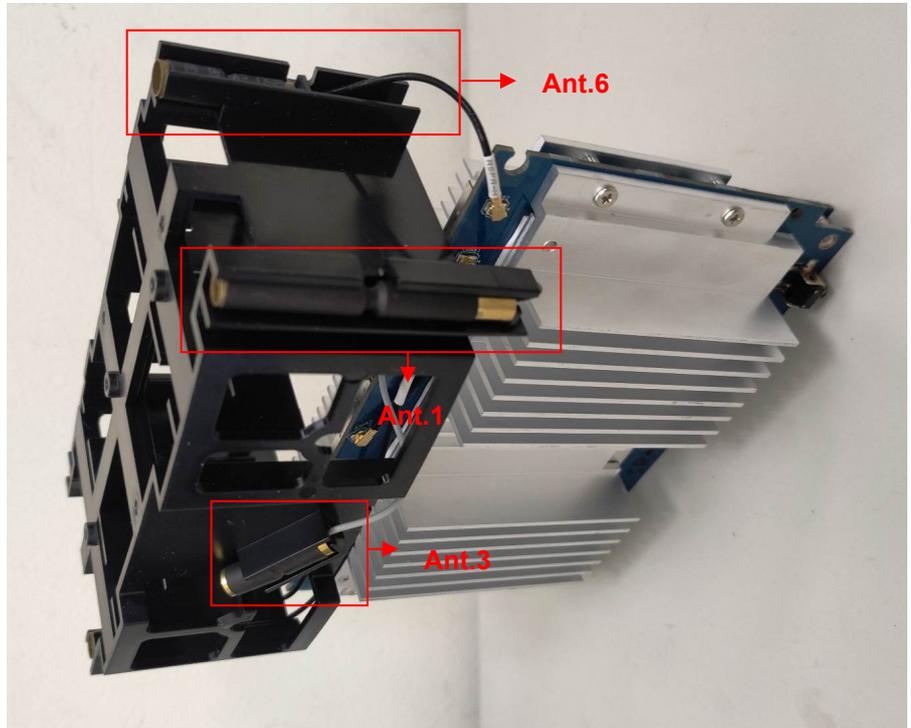
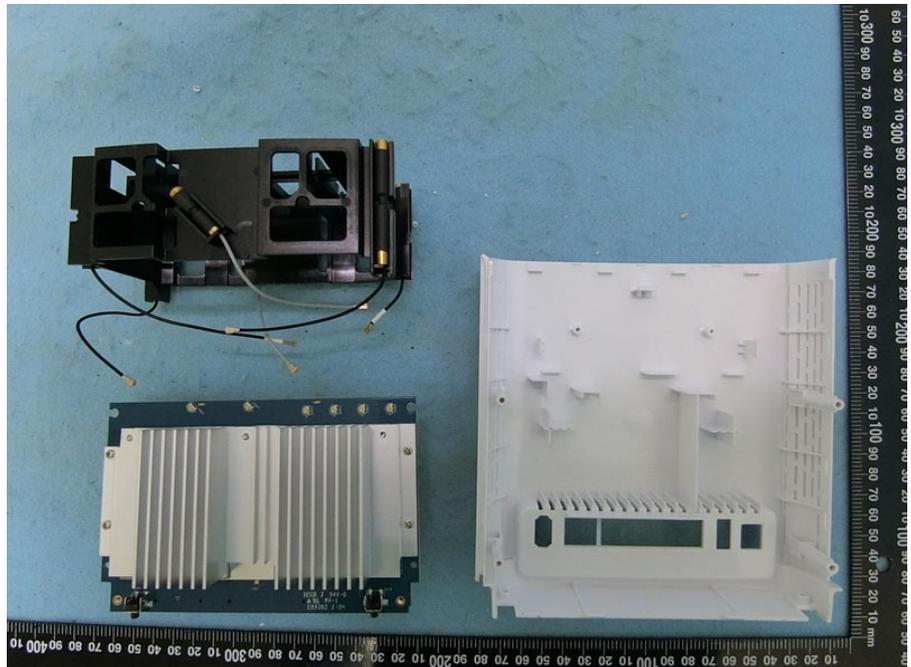
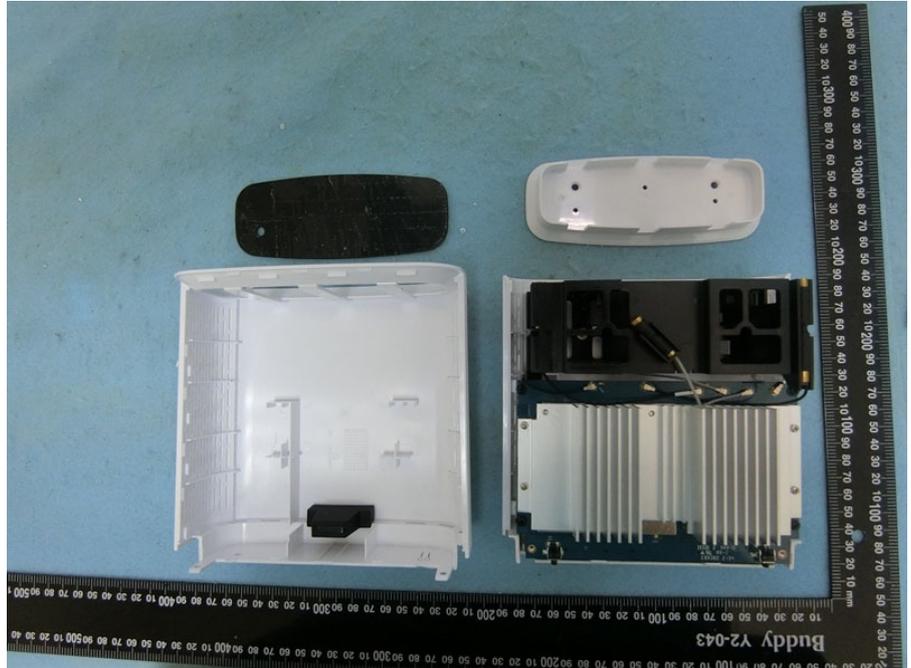
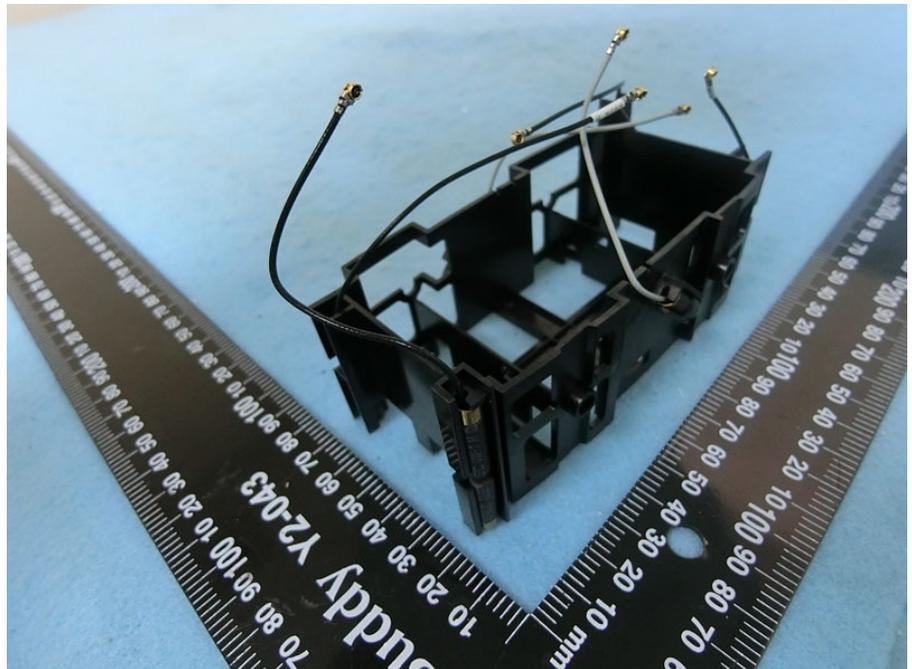
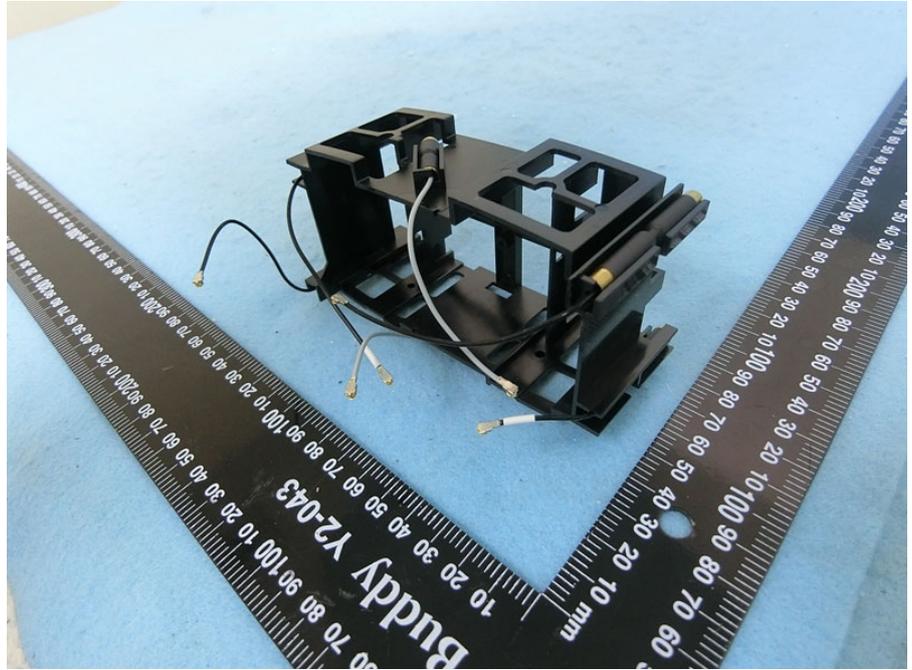
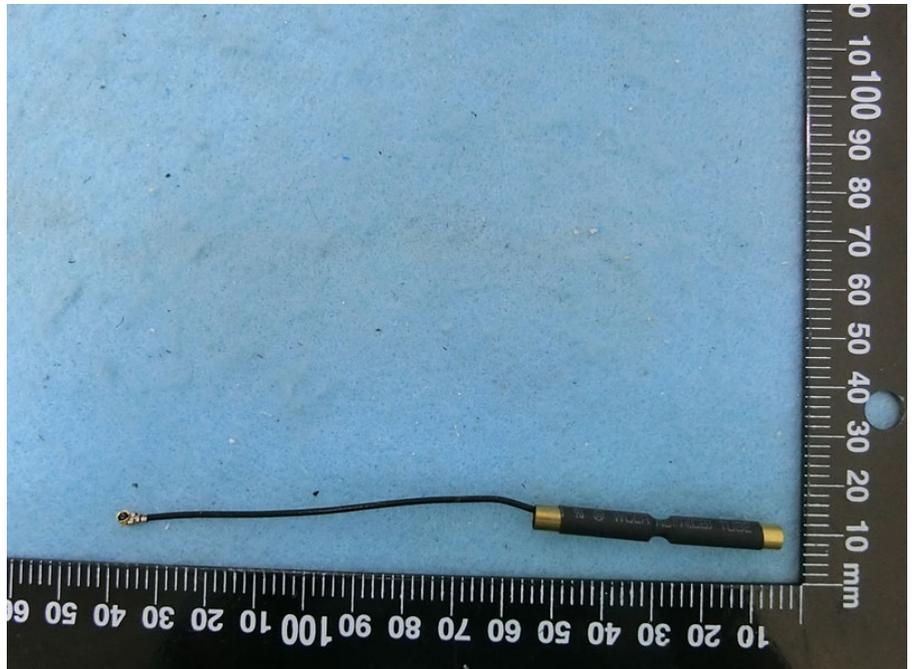
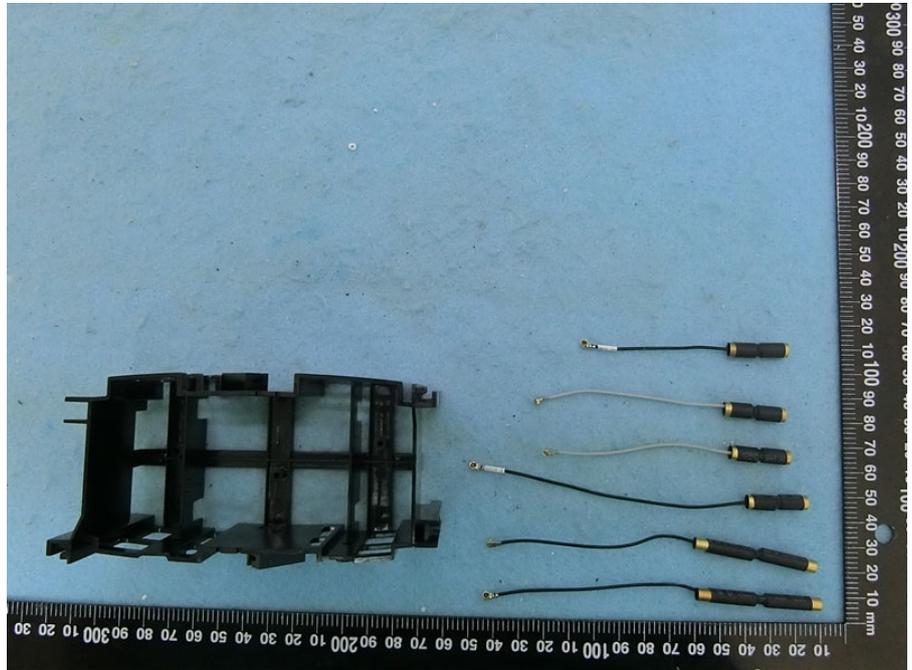


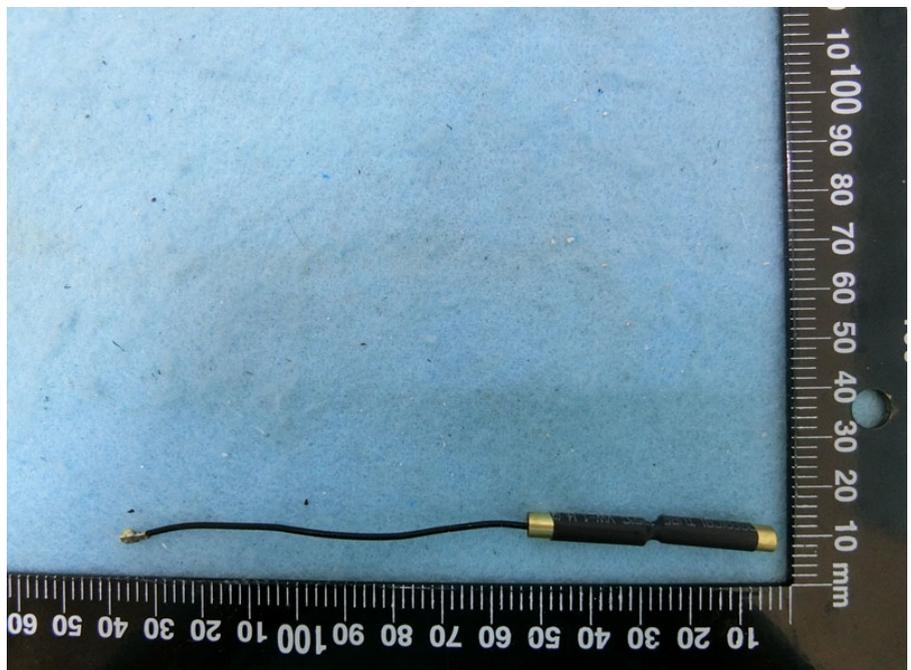
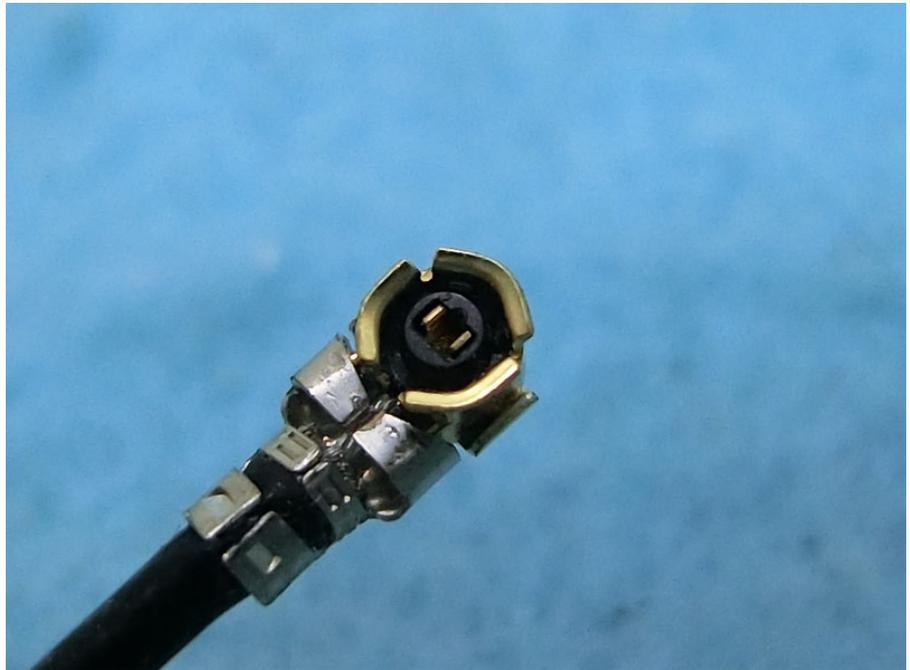
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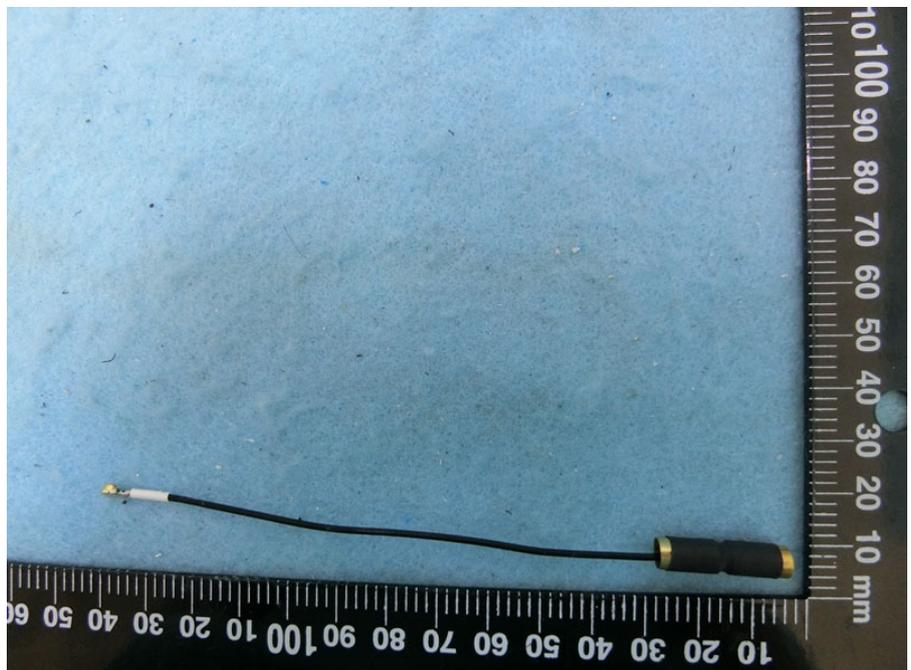
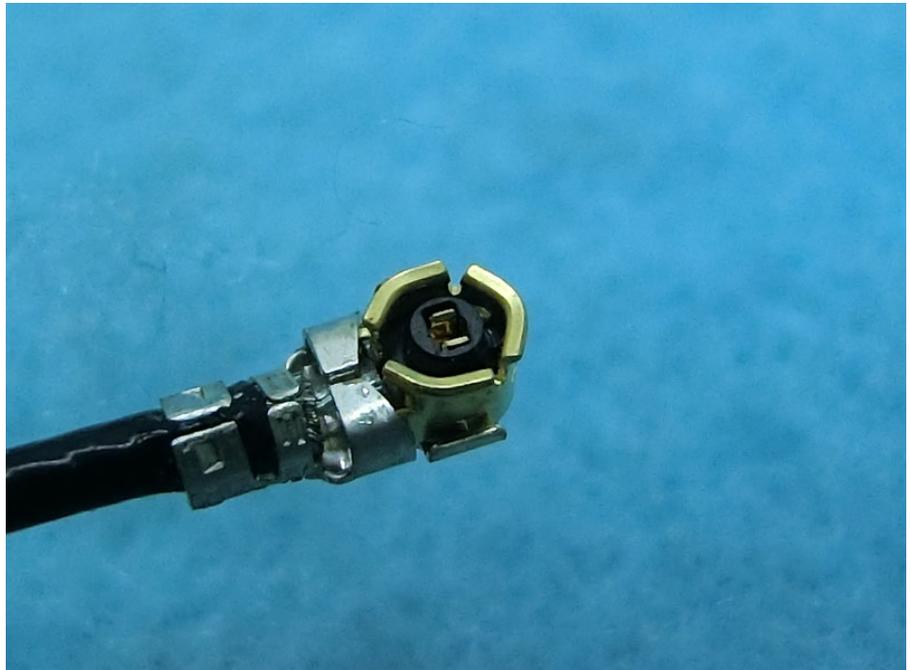


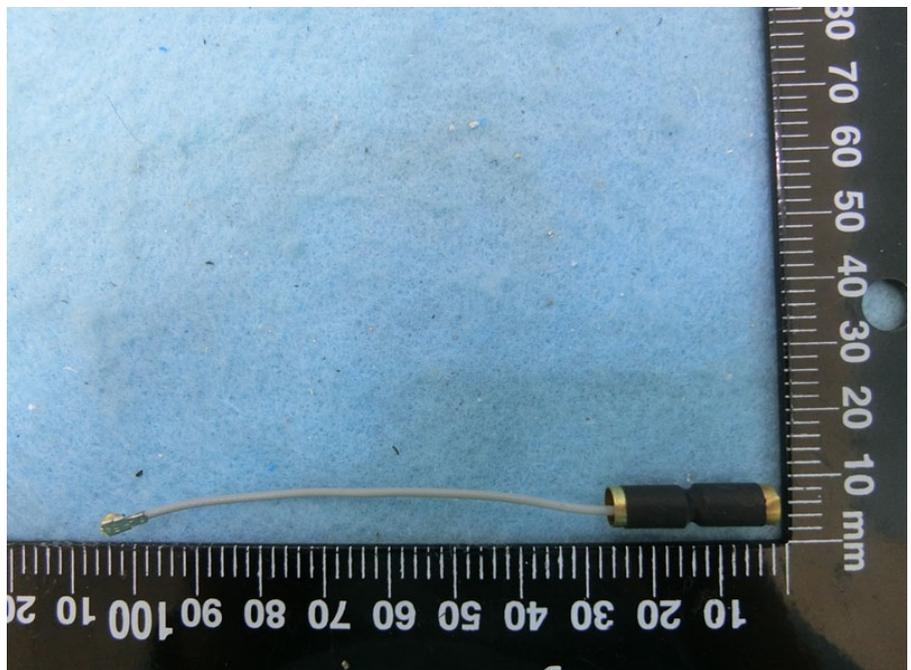
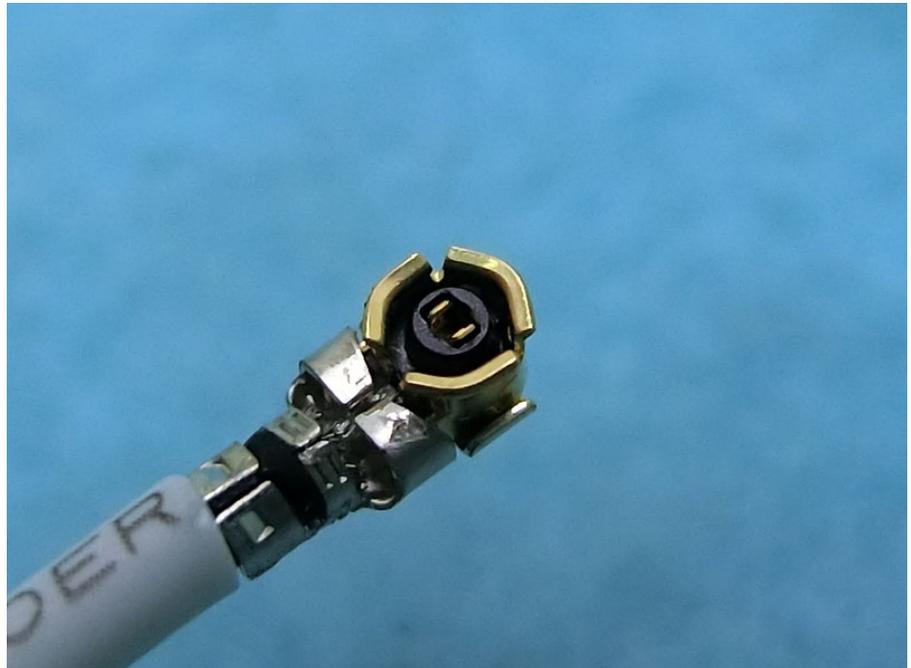


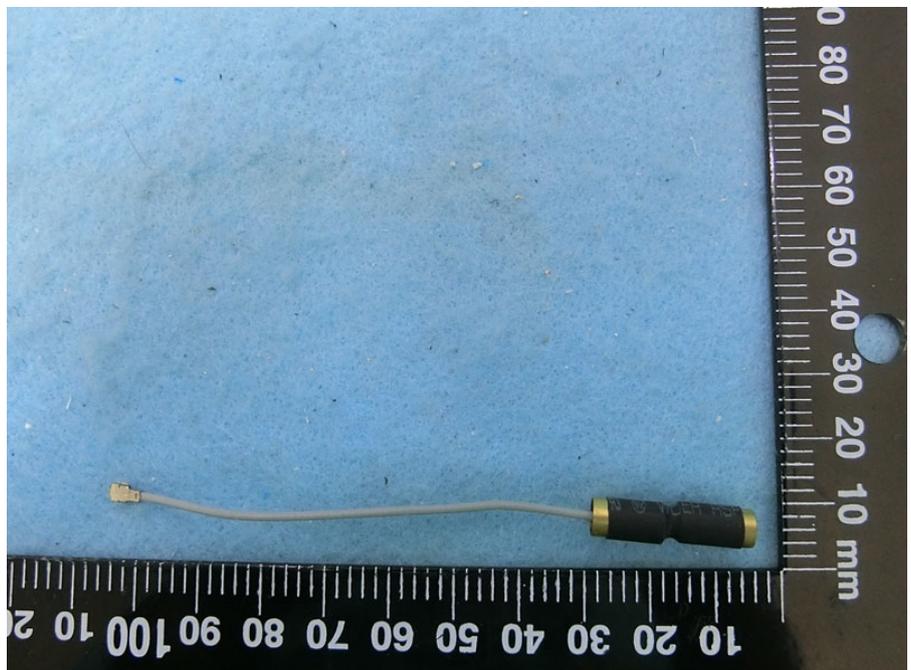
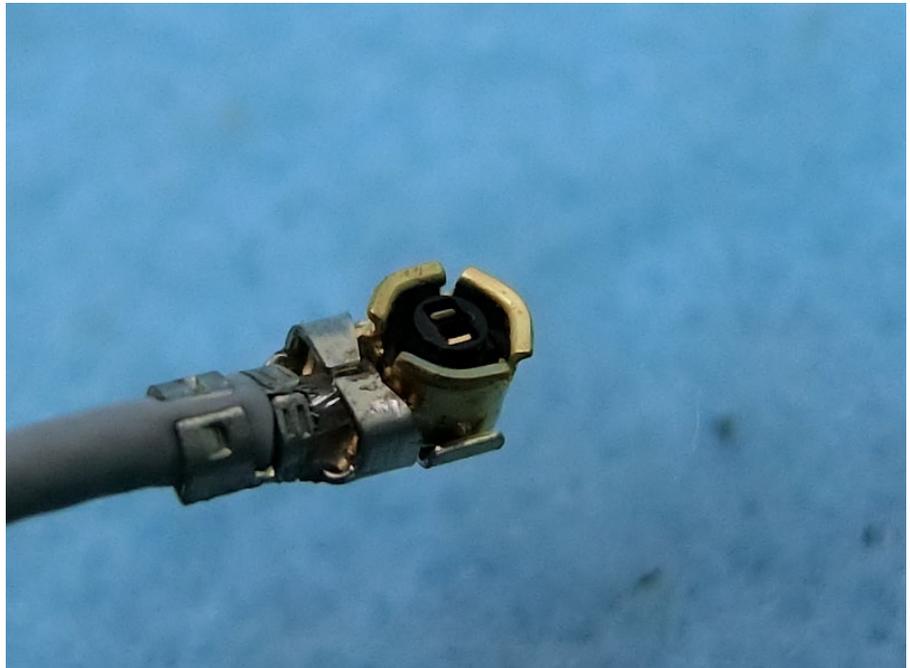


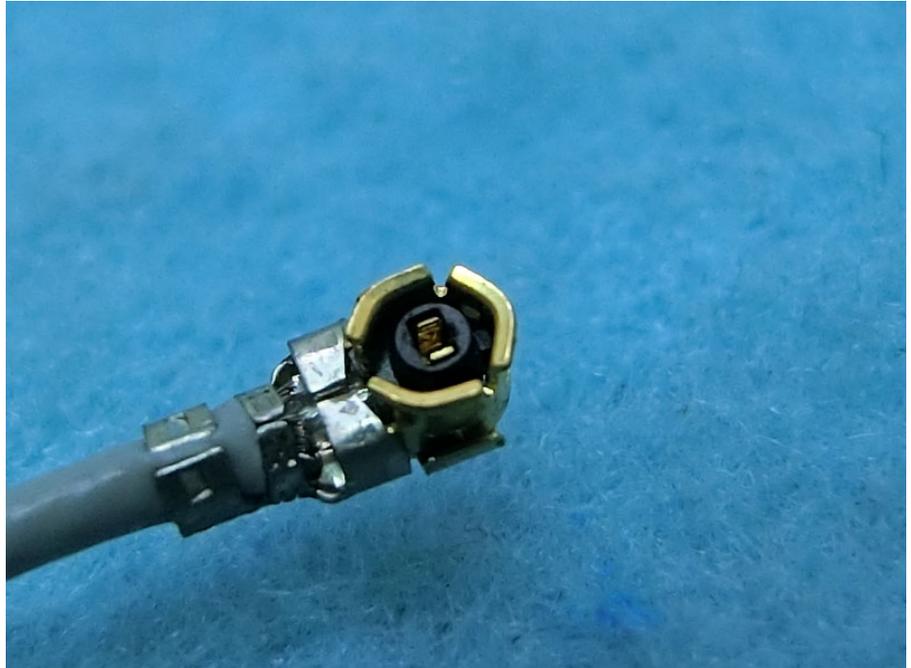


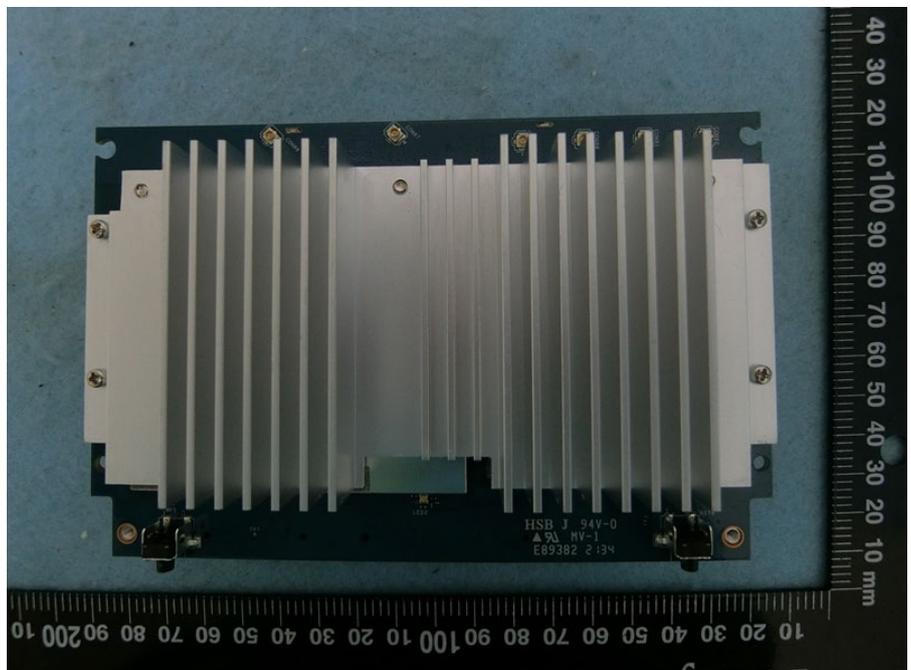
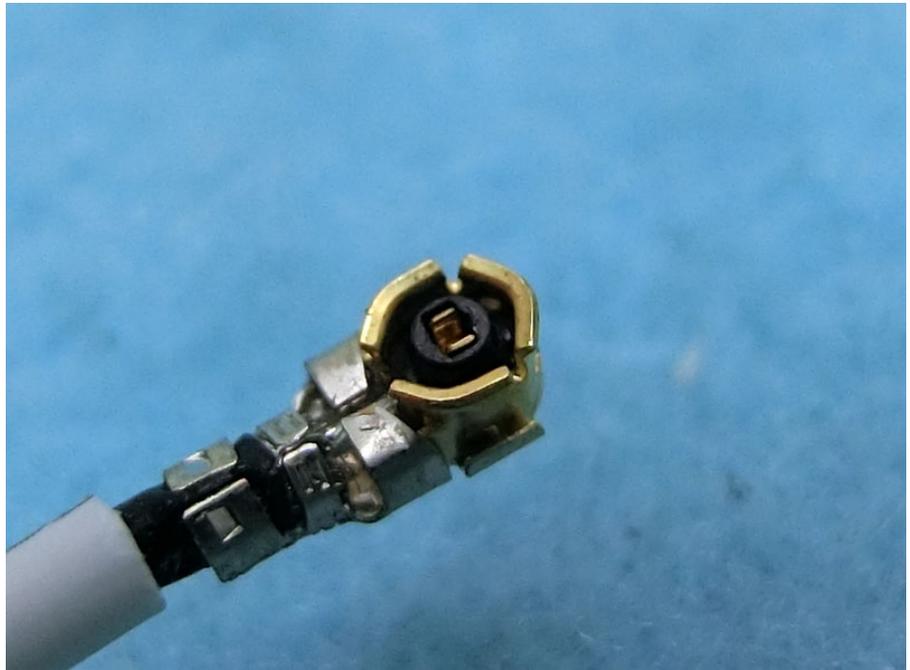


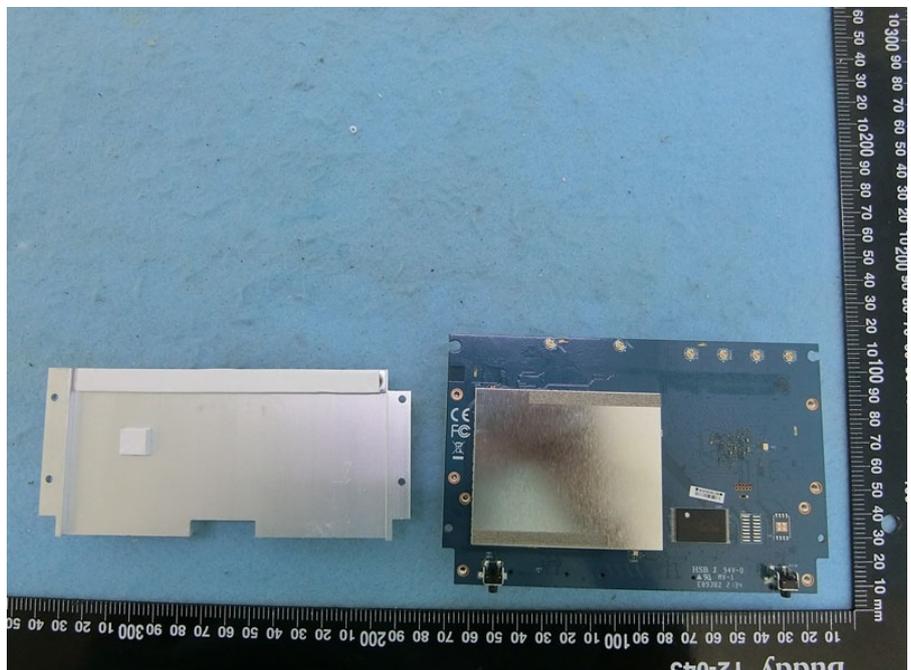
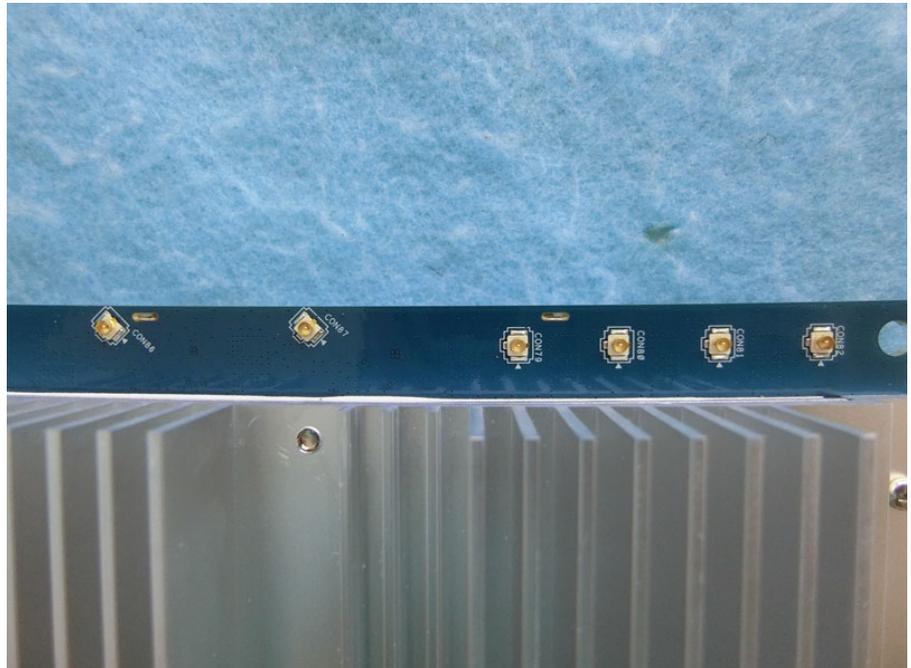


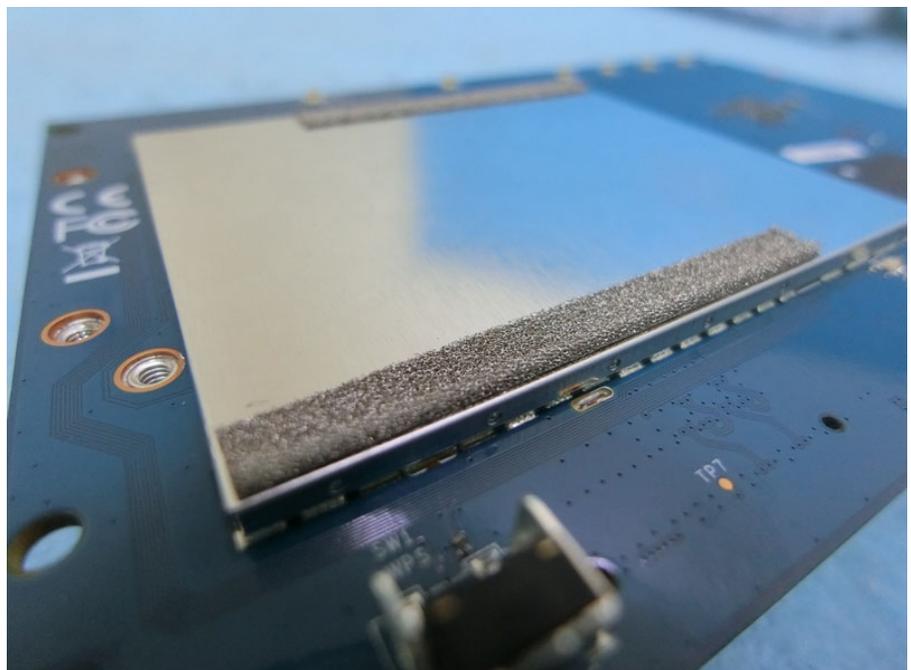
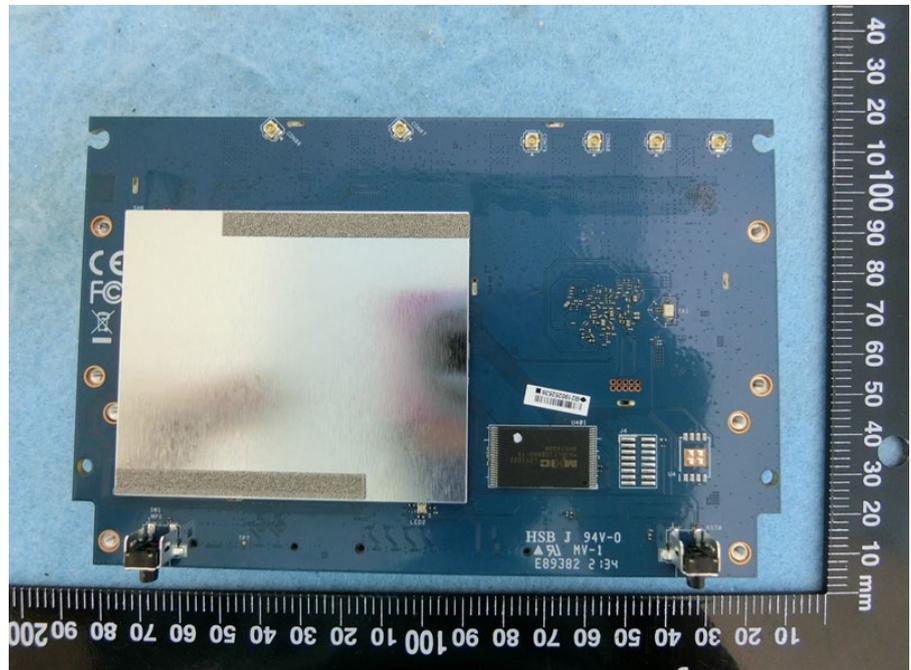


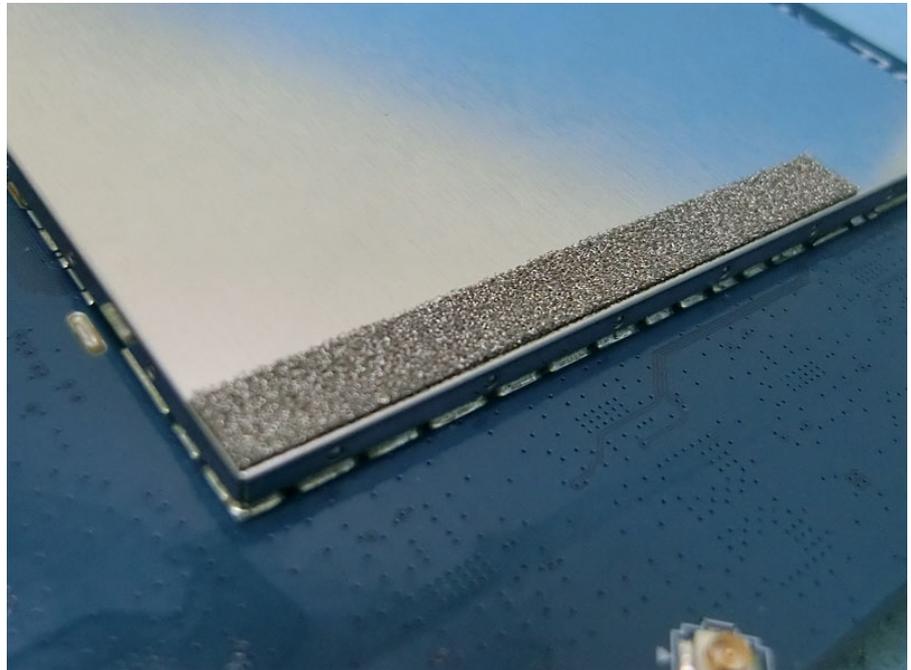


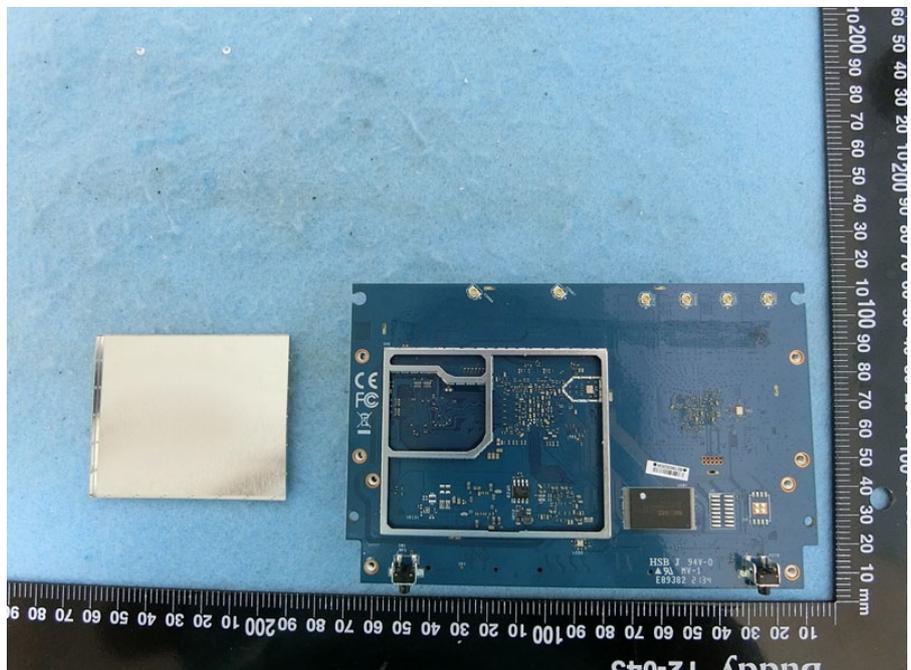
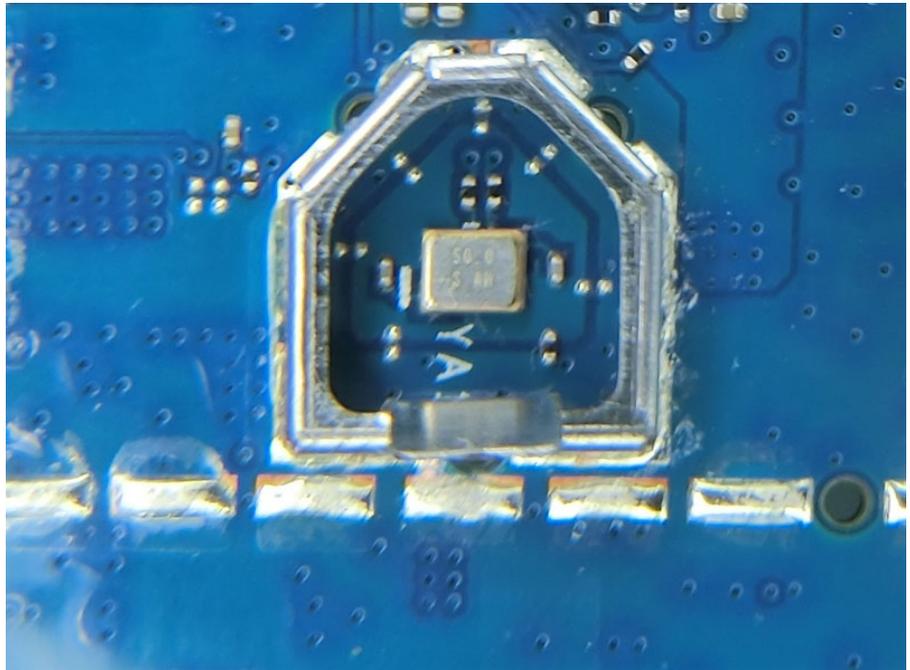


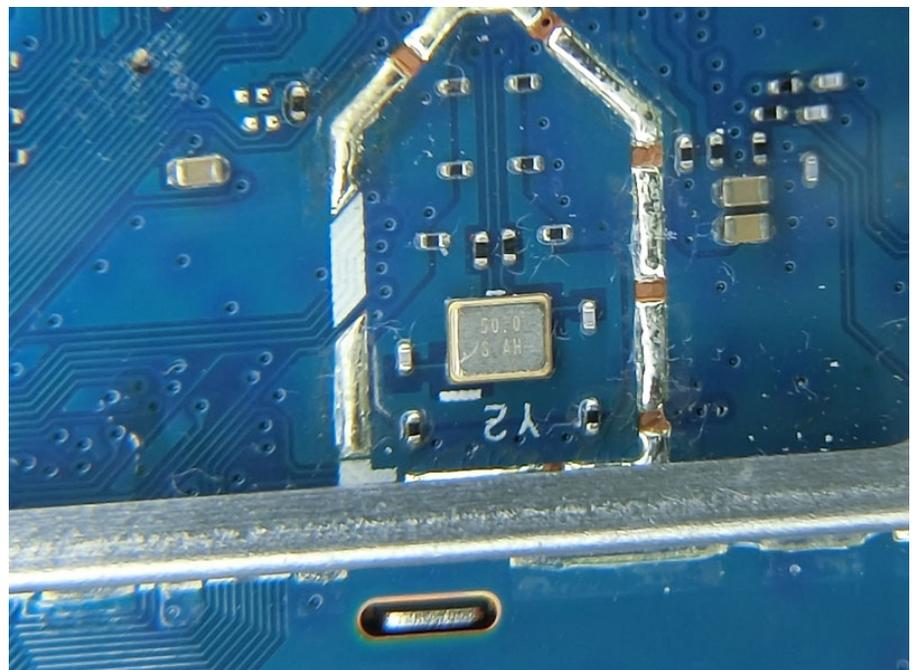
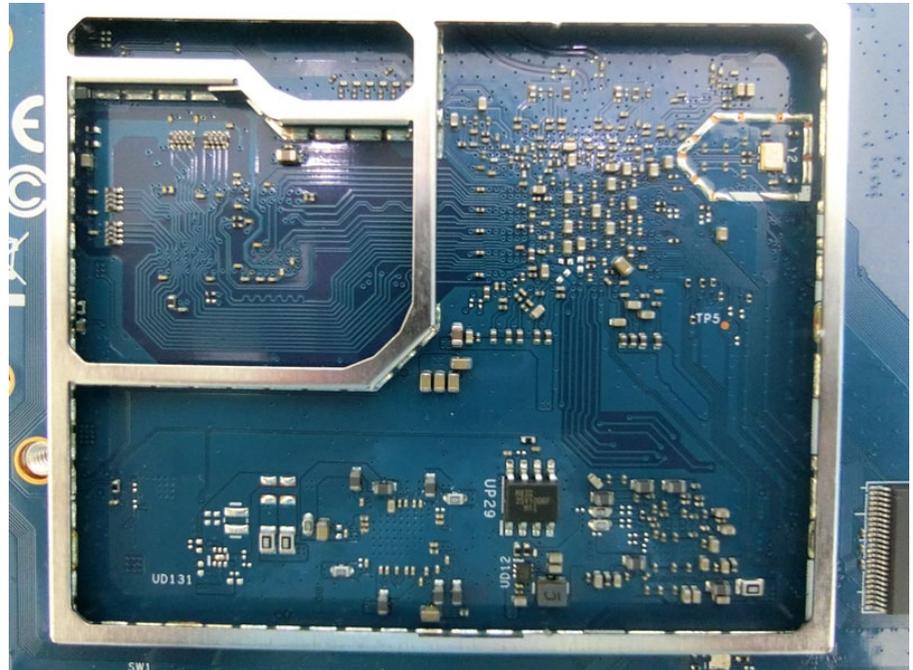


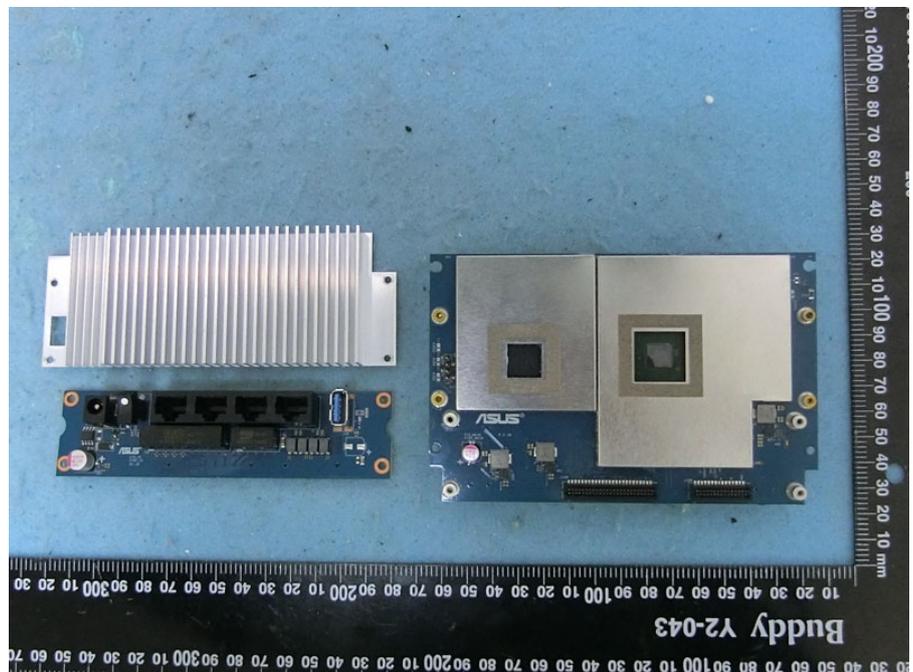
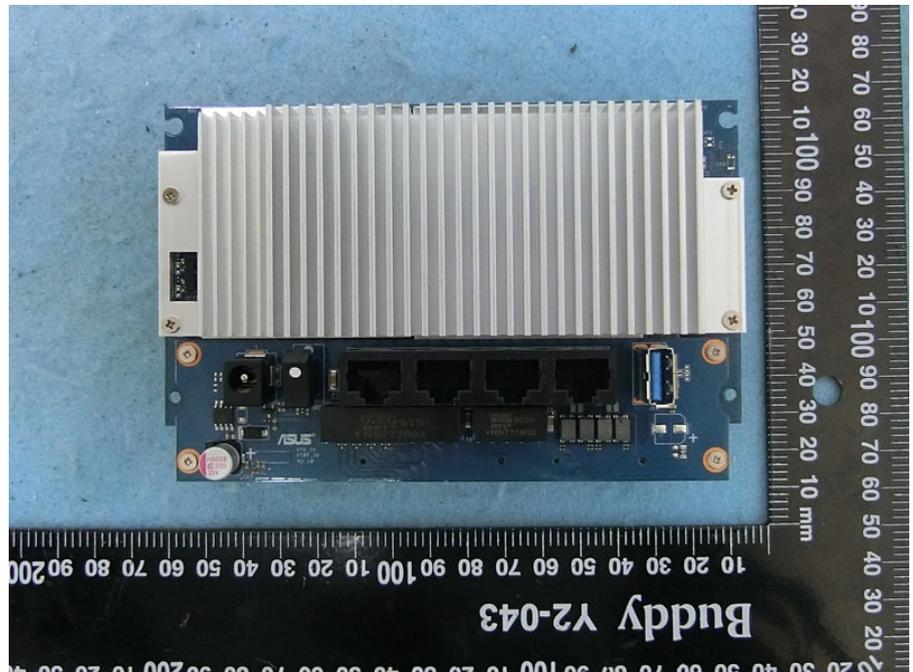


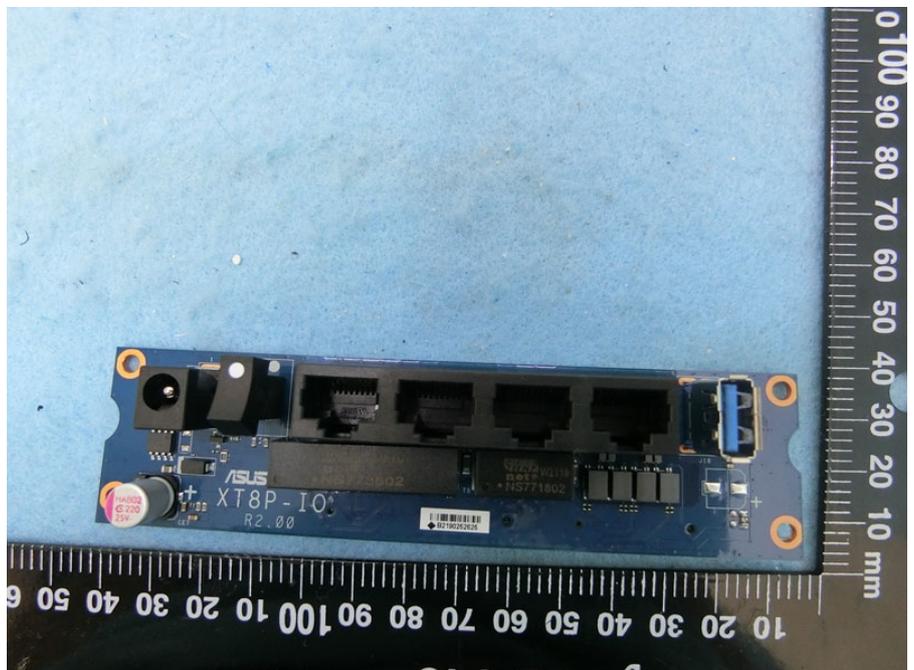
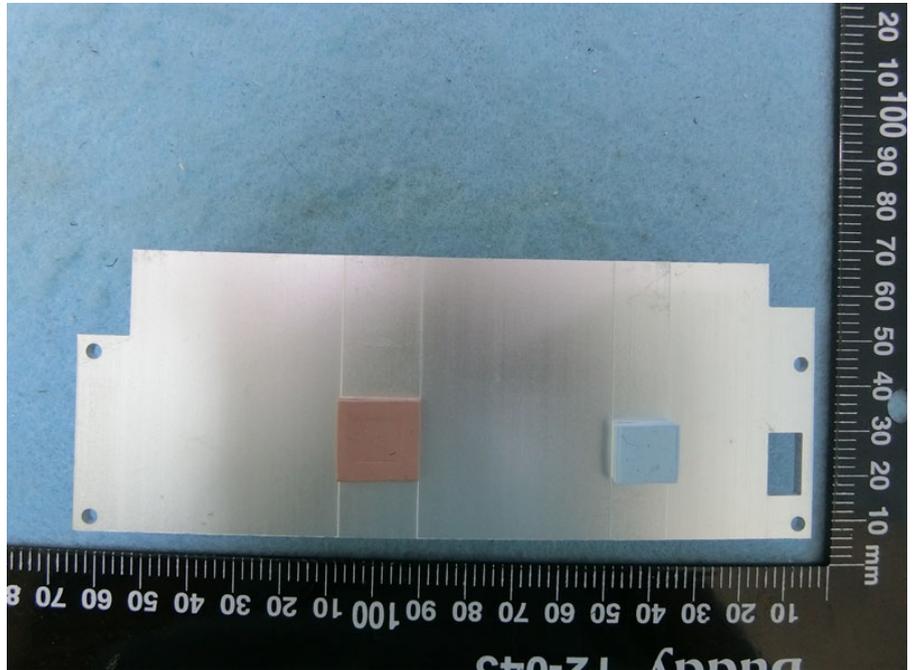












Reserve resistor position for
I/O board

Main source



