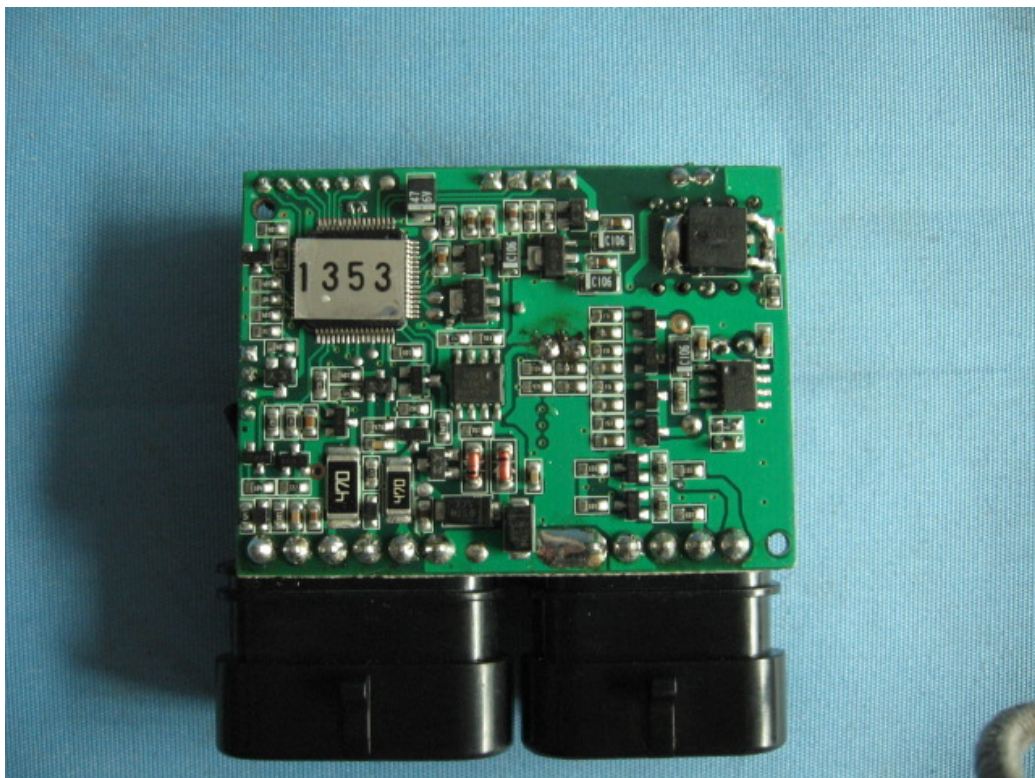


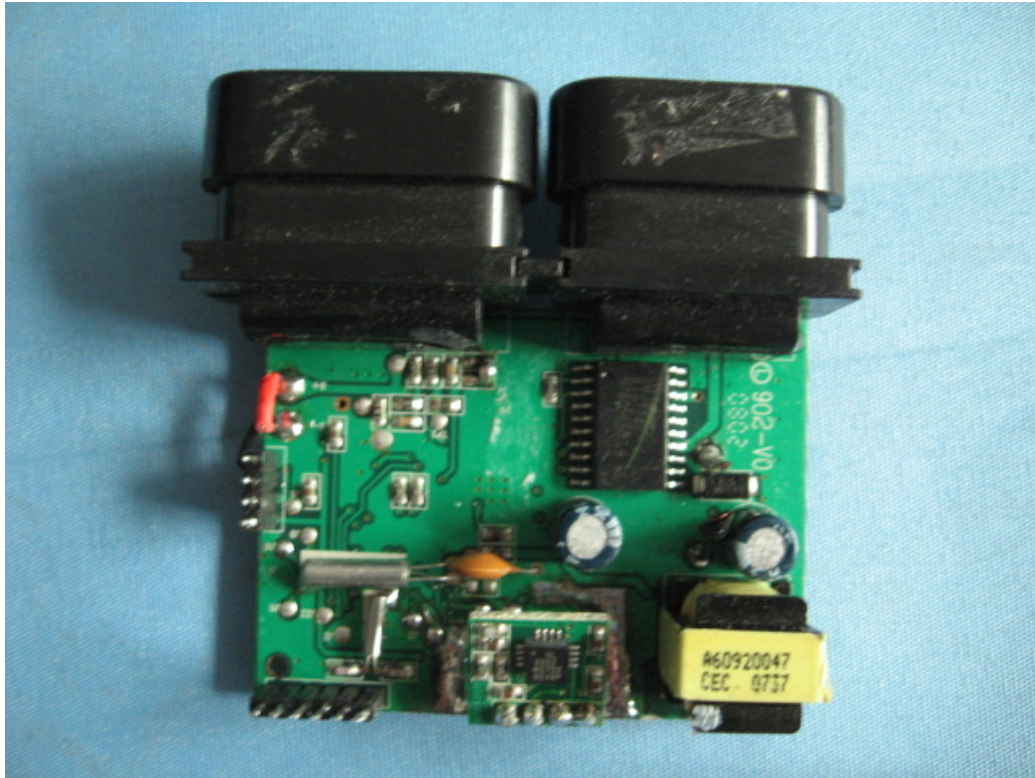
Inside view of EUT



PCB1 View 1



PCB1 View 2



PCB2 View 1



PCB2 View 2

