

EXHIBIT 4

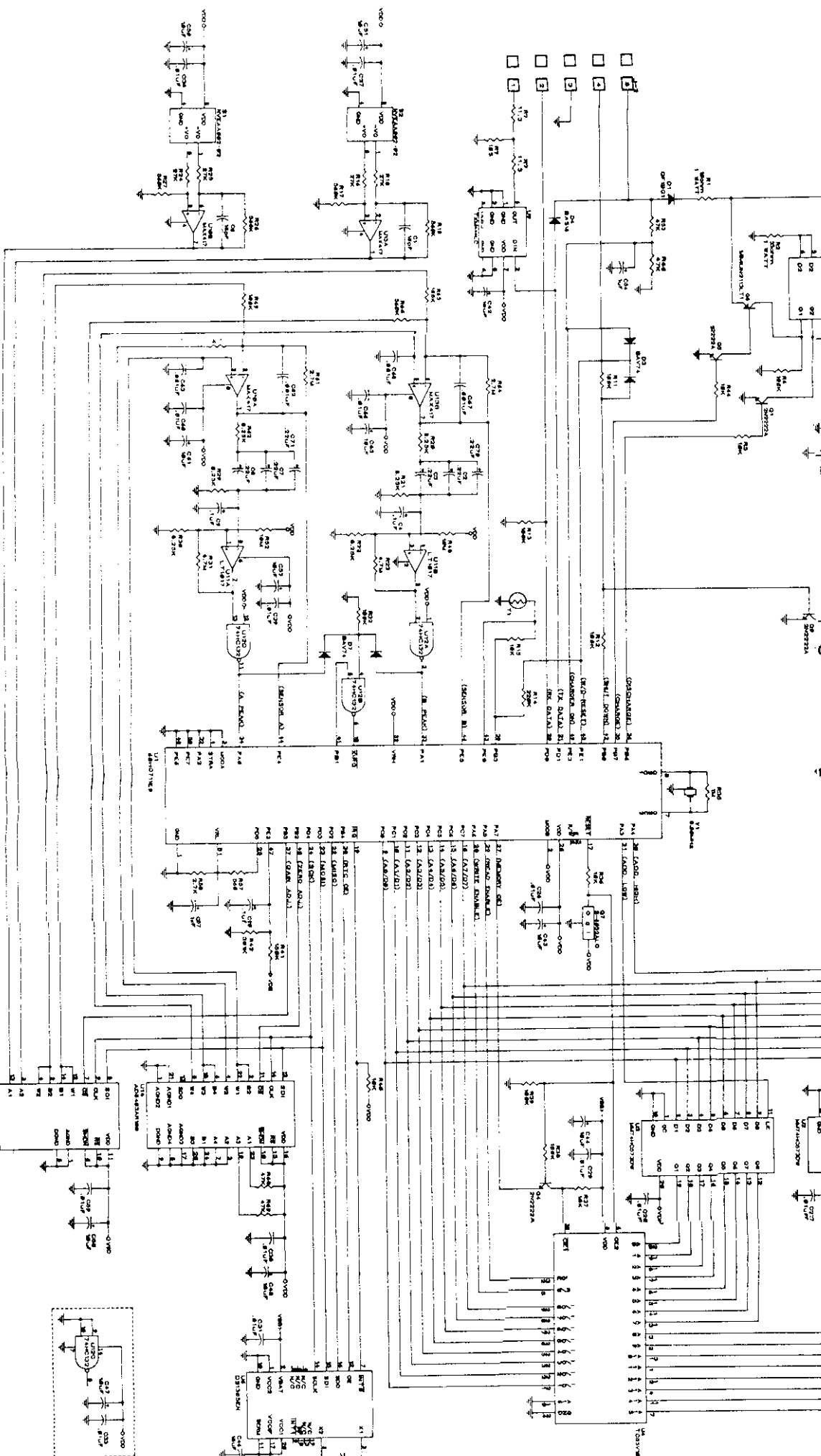
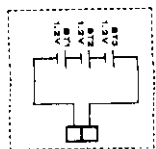
Block Diagram and Schematic

Para. 2.1033(b)(5)

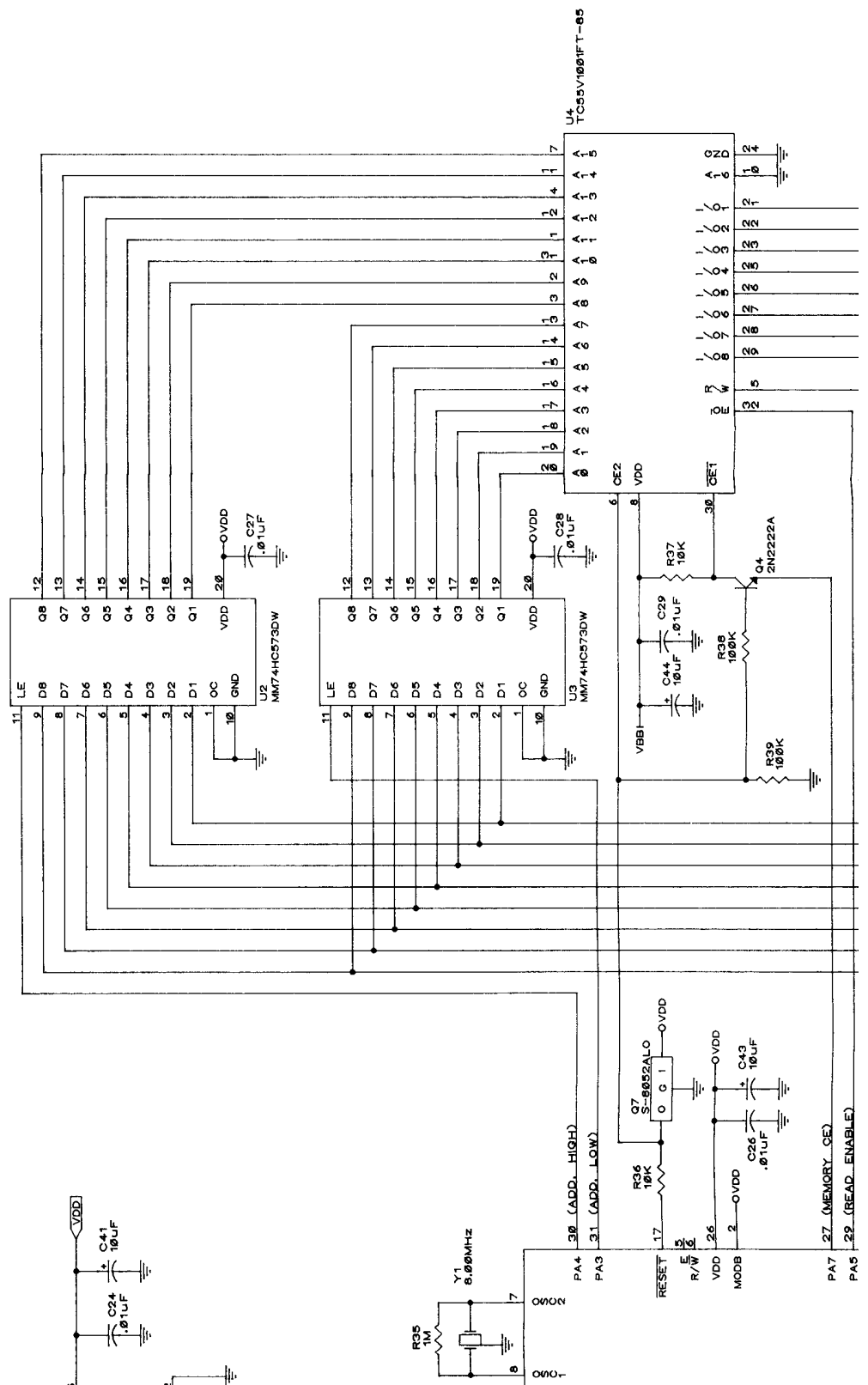


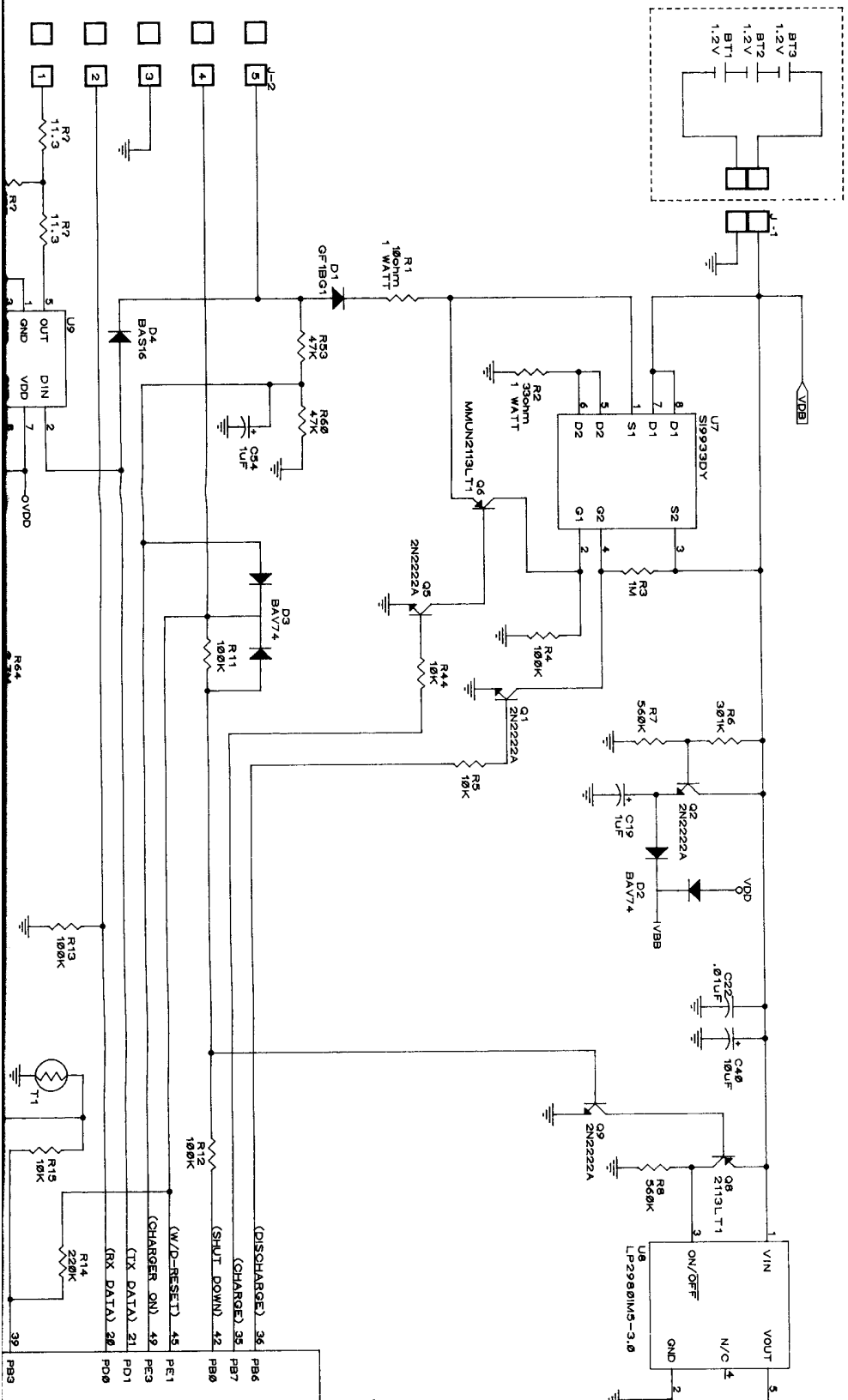
Retlif Testing Laboratories

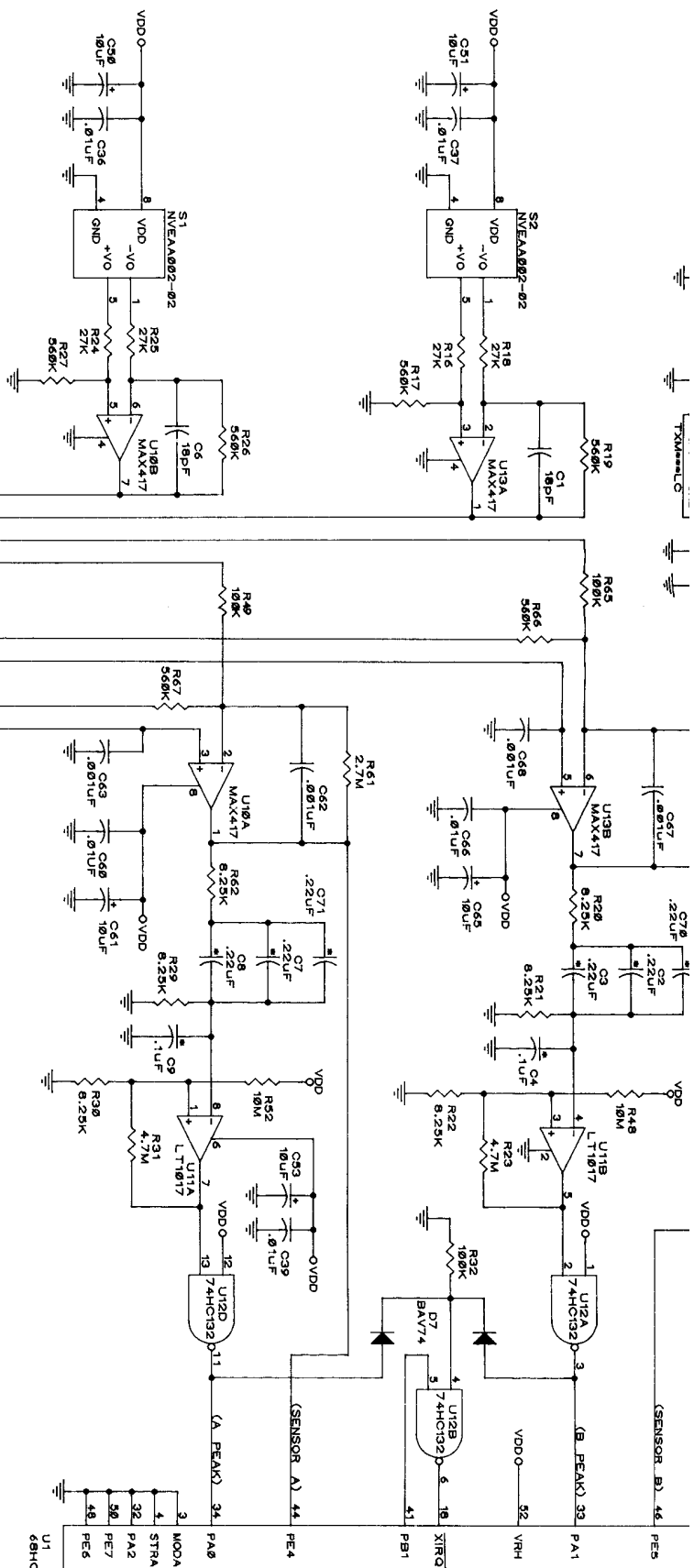
Test Report No. R-7170
FCC ID: MIKNUMETNC97A



NOTE: ALL WIRE BOUNDS BE PLACED AS CLOSE TO PINS AS POSSIBLE
 * CAPACITORS ARE IN PICO FARADS







NOTE: