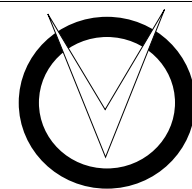
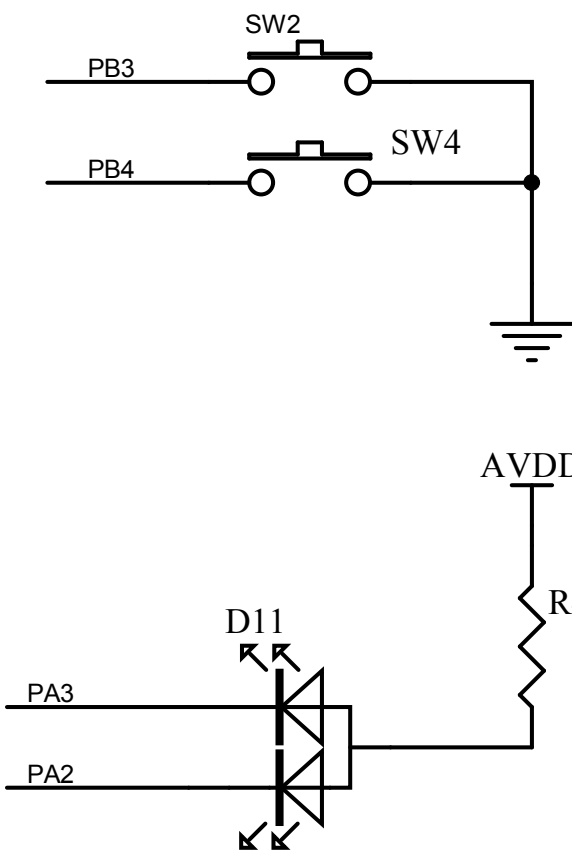
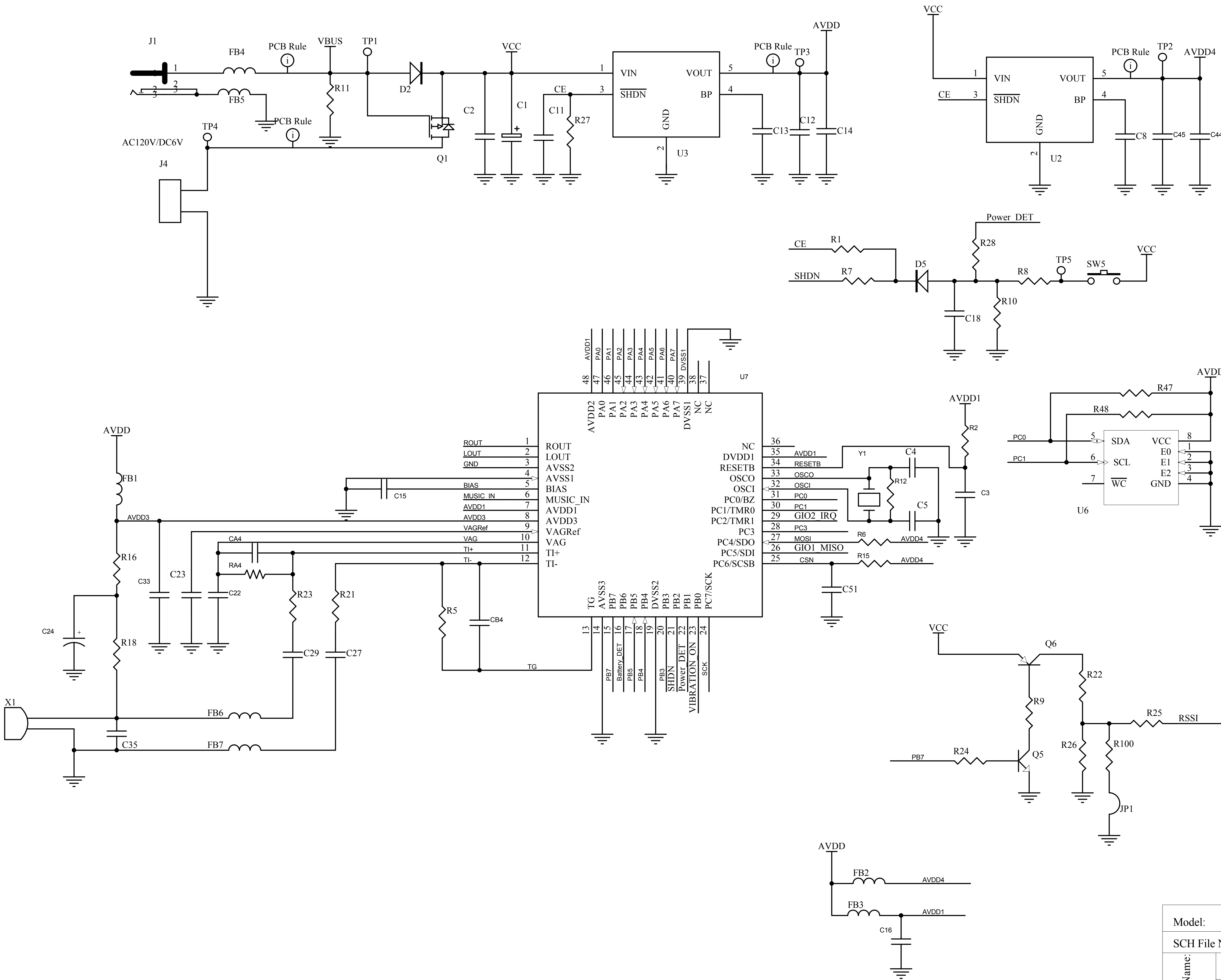


| Revision Record |          |          |       |
|-----------------|----------|----------|-------|
| LTR             | ECO No.: | Approved | Date: |
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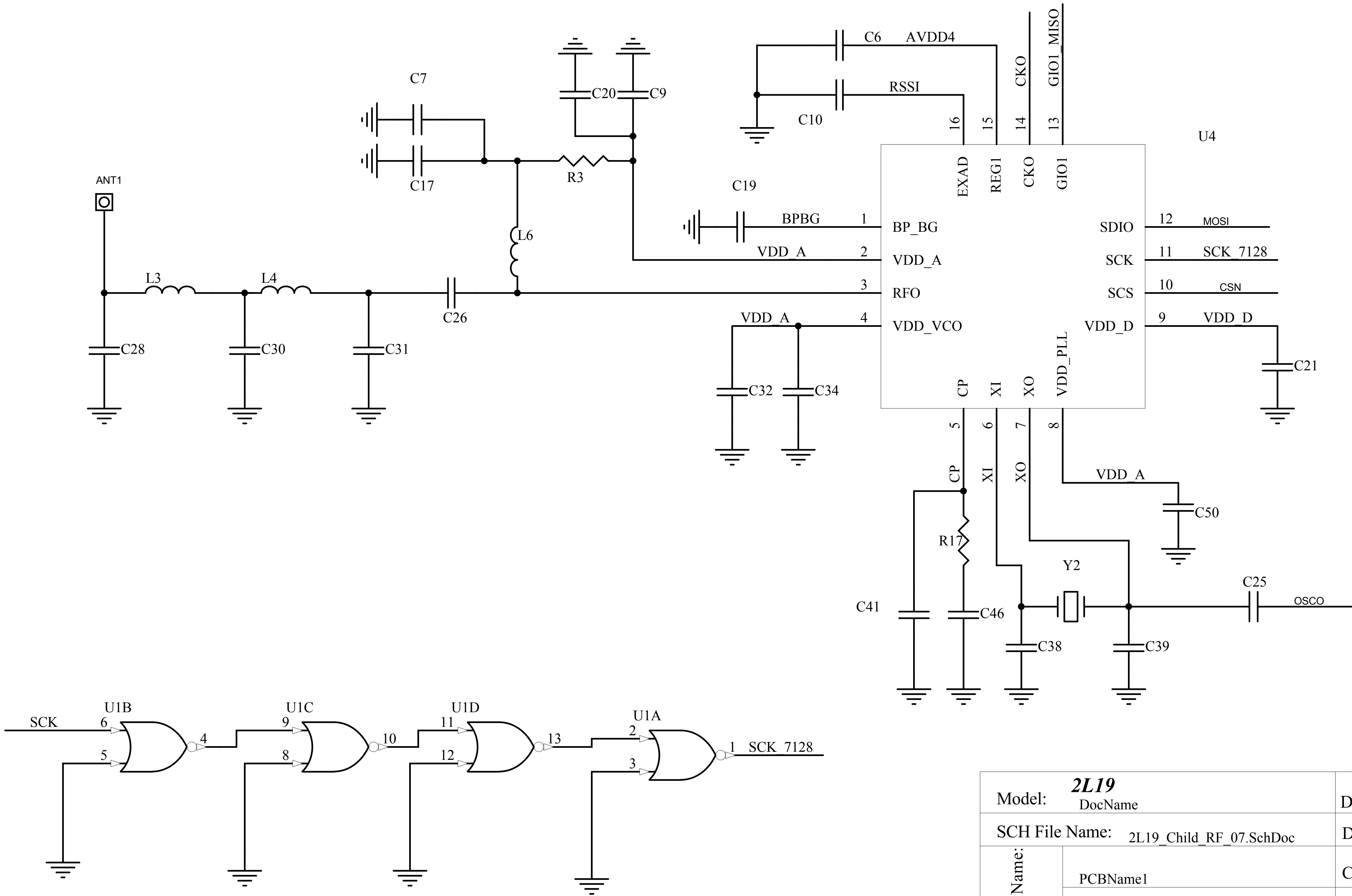
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| Model: <b>2L19</b><br>DocName            |          | Document No.: * |                   |
| SCH File Name: 2L19_Child_MAIN_07.SchDoc |          | Drawn by: DJH   | Date: SEP 29 2010 |
| PCB File Name:                           | PCBName1 | CHK By: *       | Date: SEP 29 2010 |
|                                          | PCBName2 | APPR By: *      | Date: SEP 29 2010 |
|                                          | PCBName3 | Sheet 2 of 2    | Rev: 07           |

E:\JHDENG\baby monitor\2L19\CHILD\2L19\_Child\_MAIN\_07.SchDoc

ModifiedDate: 2010-09-29 8



| Revision Record |          |          |       |
|-----------------|----------|----------|-------|
| LTR             | ECO No.: | Approved | Date: |
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|-----------------------------------------------------------|----------|---------------|-------------------|
| Model: <b>2L19</b><br>DocName                             |          | Document No.: |                   |
| SCH File Name: 2L19_Child_RF_07.SchDoc                    |          | Drawn by: DJH | Date: SEP 29 2010 |
| PCB File Name:                                            | PCBName1 | CHK By: *     | Date: SEP 29 2010 |
|                                                           | PCBName2 | APPR By: *    | Date: SEP 29 2010 |
|                                                           | PCBName3 | Sheet 1 of 2  | Rev: 07           |
| E:\JHDENG\baby monitor\2L19\CHILD\2L19_Child_RF_07.SchDoc |          |               |                   |