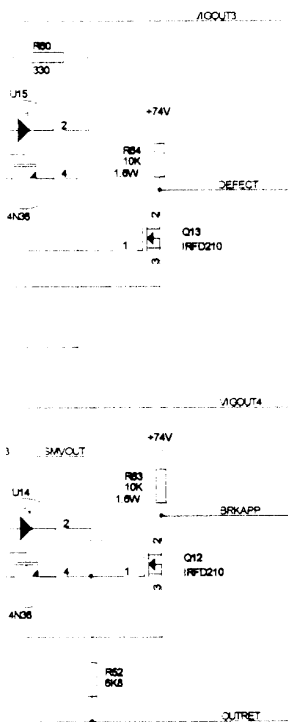


The diagram shows the output stage of the 68000 microprocessor. It features two 68000 microprocessors, one labeled '68000' and the other '68000'. The '68000' microprocessor has an 'ALU' block and an 'ALUOUT1' output. The '68000' microprocessor has an 'ALU' block and an 'ALUOUT2' output. The 'ALUOUT1' output is connected to the 'ALU' block of the '68000' microprocessor. The 'ALUOUT2' output is connected to the 'ALU' block of the '68000' microprocessor. The 'ALU' block of the '68000' microprocessor is connected to the 'ALUOUT1' output of the '68000' microprocessor. The 'ALU' block of the '68000' microprocessor is connected to the 'ALUOUT2' output of the '68000' microprocessor. The 'ALU' block of the '68000' microprocessor is connected to the 'ALUOUT1' output of the '68000' microprocessor. The 'ALU' block of the '68000' microprocessor is connected to the 'ALUOUT2' output of the '68000' microprocessor.

P1
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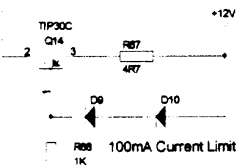
HEADER 24

COPY
CERTIFIED *[Signature]*
DATE 22 Jun 98

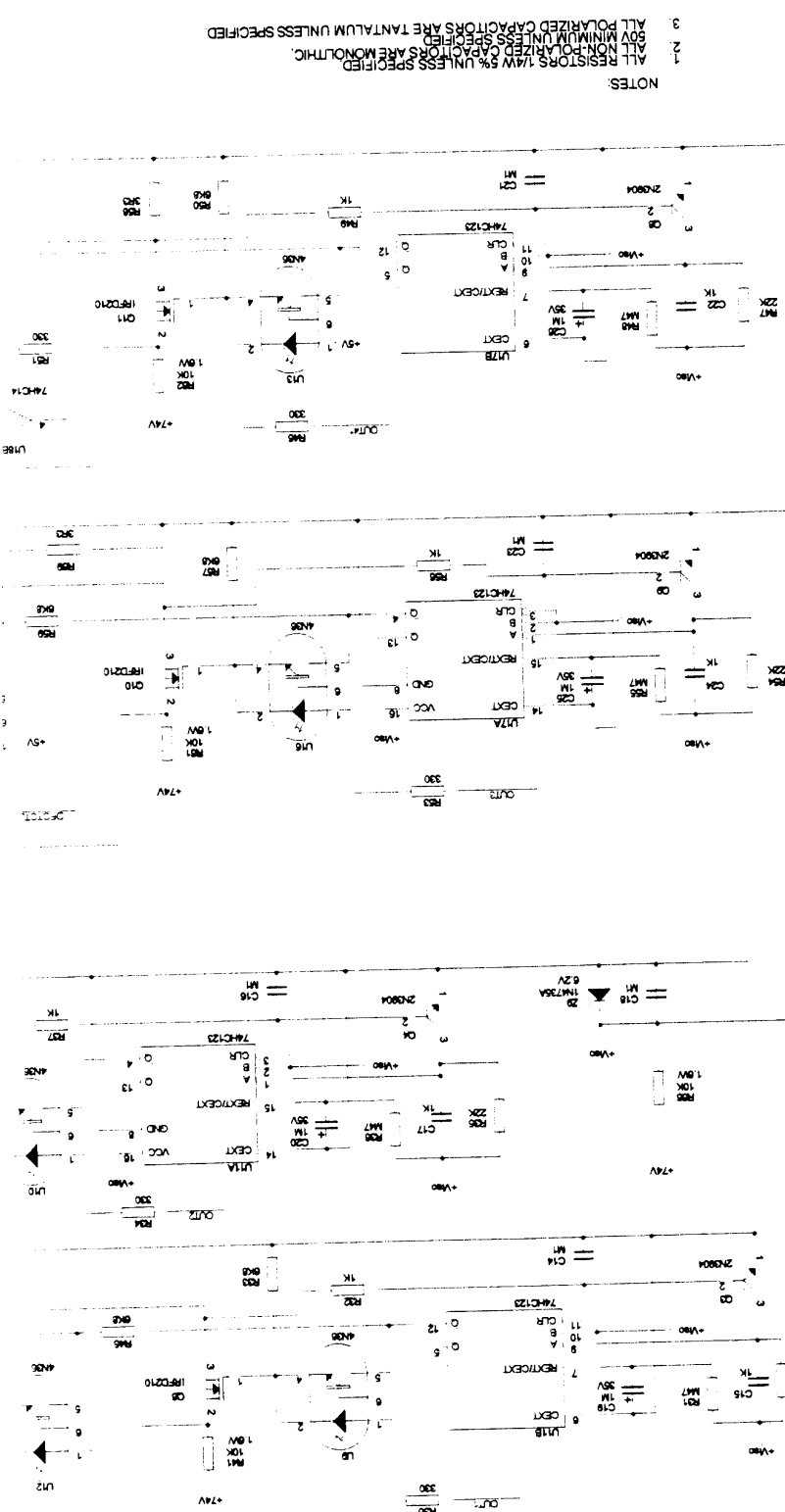
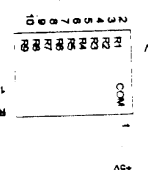
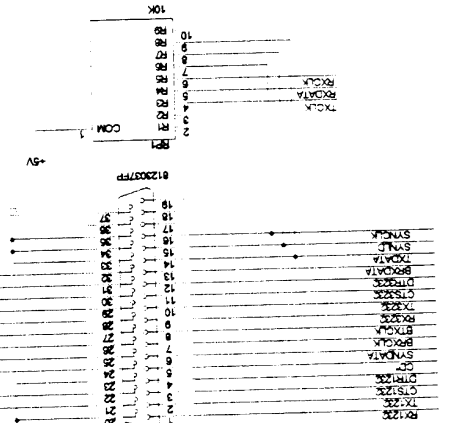
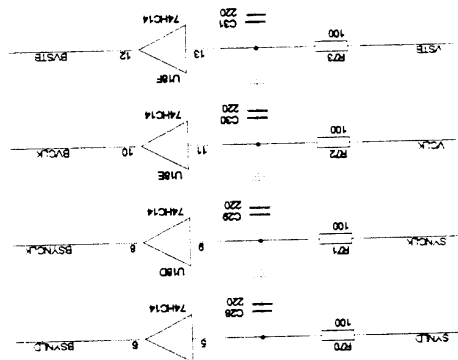
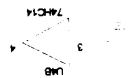


	P2
5X1232	1
2751232	2
7X1232	3
1X32	4
	5
	6
2753232	7
7X3232	8
27523232	9
	10
2751232	11
	12
	13
	14
	15
2200UT	16
	17
	18
22UK	19
25TB	20
5X3232	21
	22
	23
22DN	24

HEADER 24



ER CPU TRANSIENT SUPPRESSOR				
Drawn	EIH	27FEB95	P/N: 5821617,58216701	
Checked	WMJ	27FEB95	SIZE:C	REV: 1
Approved	DRB	02MAR95	DWG# Q-89216-17-A01	
DATE: Wednesday, December 10, 1997			SHEET 2	OF 2



NOTES:
 1. ALL RESISTORS 1%W 5% UNLESS SPECIFIED
 2. ALL NON-POLARIZED CAPACITORS ARE MONOLITHIC.
 3. ALL POLARIZED CAPACITORS ARE TANTALUM UNLESS SPECIFIED