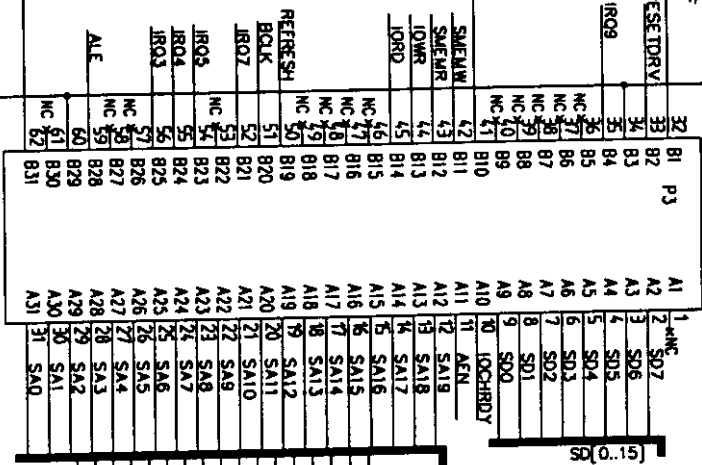
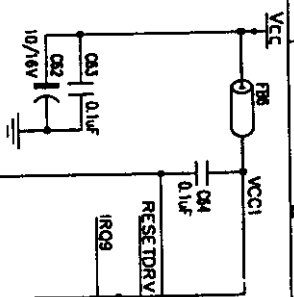
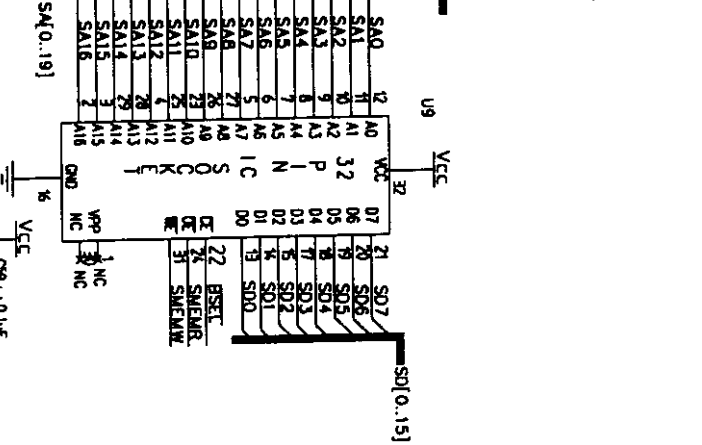
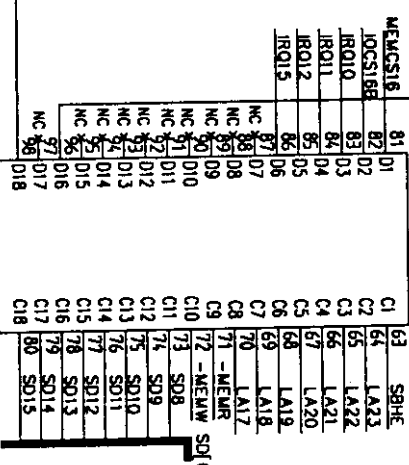


DISABLE EPROM

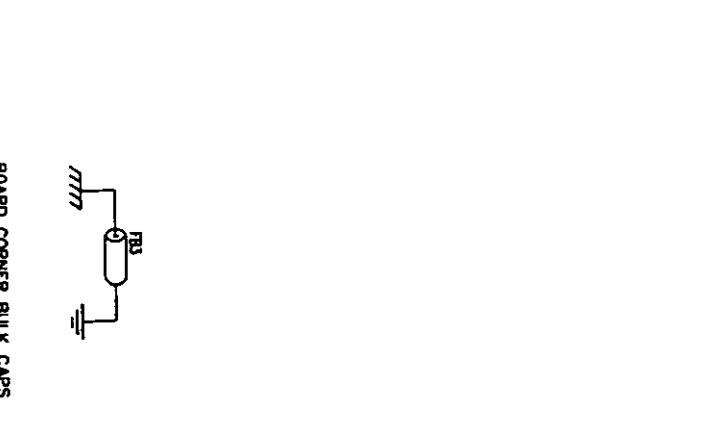
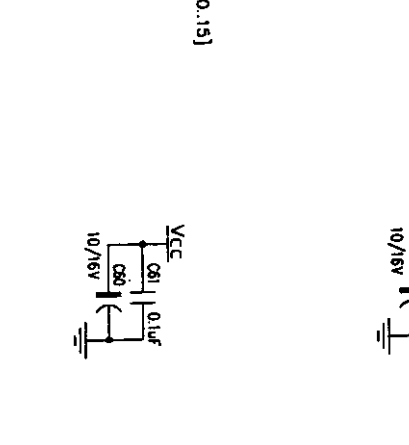




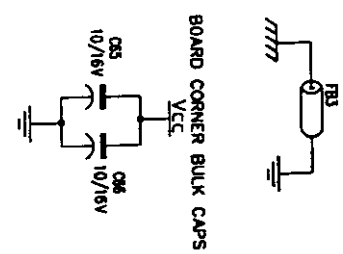
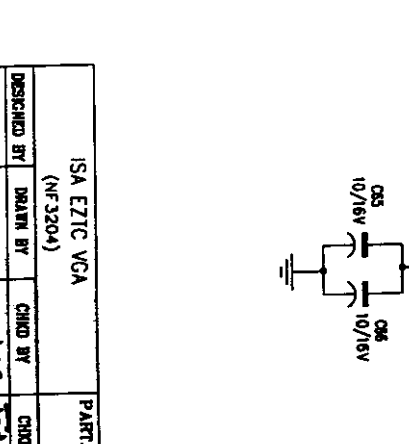
ISA CONN



ISA CONN

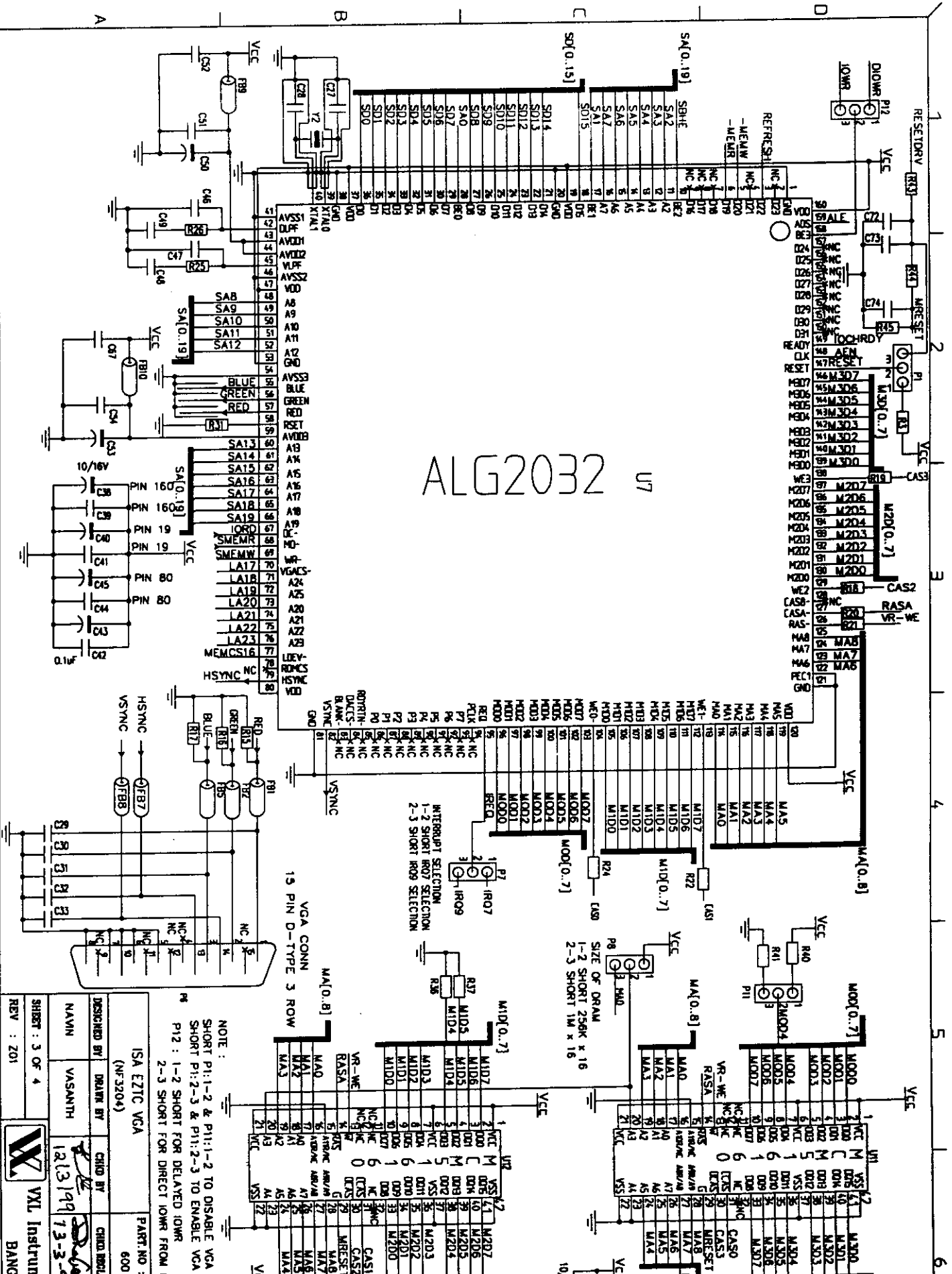


ISA CONN

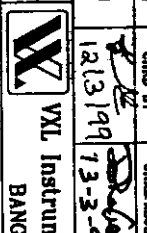


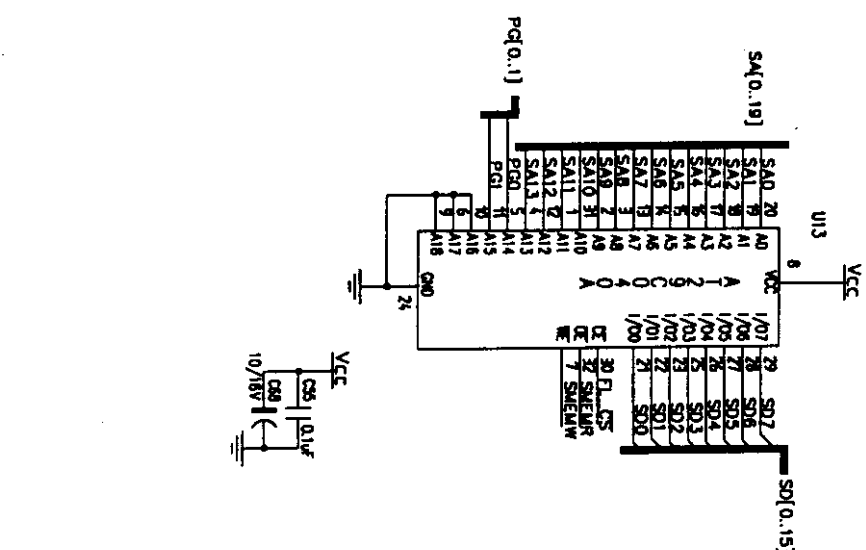
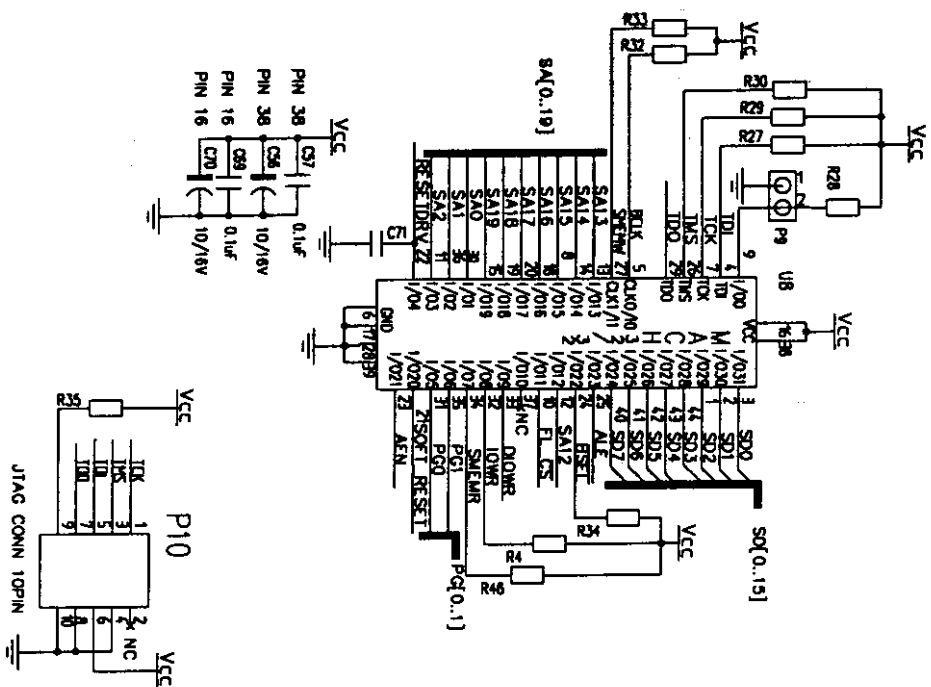
ISA EZTC VCA		PART NO : 600	
(NF3204)			
DESIGNED BY	DRAWN BY	CHD BY	CHD REEL
KARIM	VASANTH	12/3/99	05/10/00
SHEET : 2 OF 4		VXL Instrum	
REV : Z01		BANG	

ALG2032



SHEET : 3 OF 4
 REV : 201
 DESIGNED BY :
 DRAWN BY :
 CHECKED BY :
 PART NO :
 600 :
 ISA E7IC VGA
 (N73204)
 NOTE :
 SHORT P1:1-2 & P11:1-2 TO DISABLE VGA
 SHORT P1:2-3 & P11:2-3 TO ENABLE VGA
 P12 : 1-2 SHORT FOR DELAYED IOWR
 2-3 SHORT FOR DIRECT IOWR FROM IS

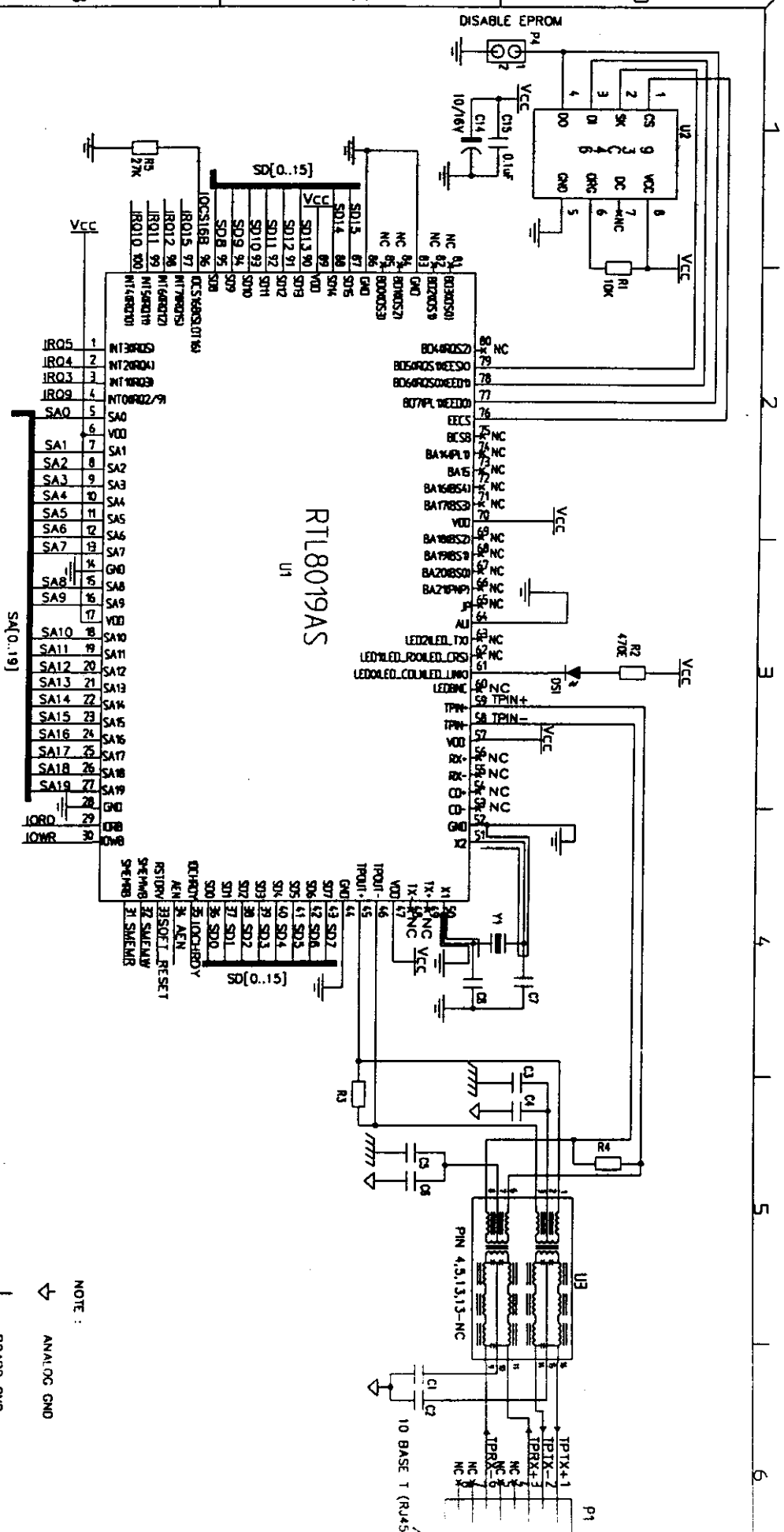




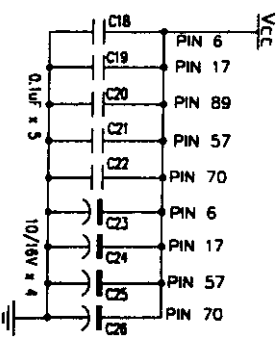
NOTE :
 Pg : ADDRESS SELECTION
 1-2 OPEN 00000-D7FF
 1-2 SHORT D8000-DFFF

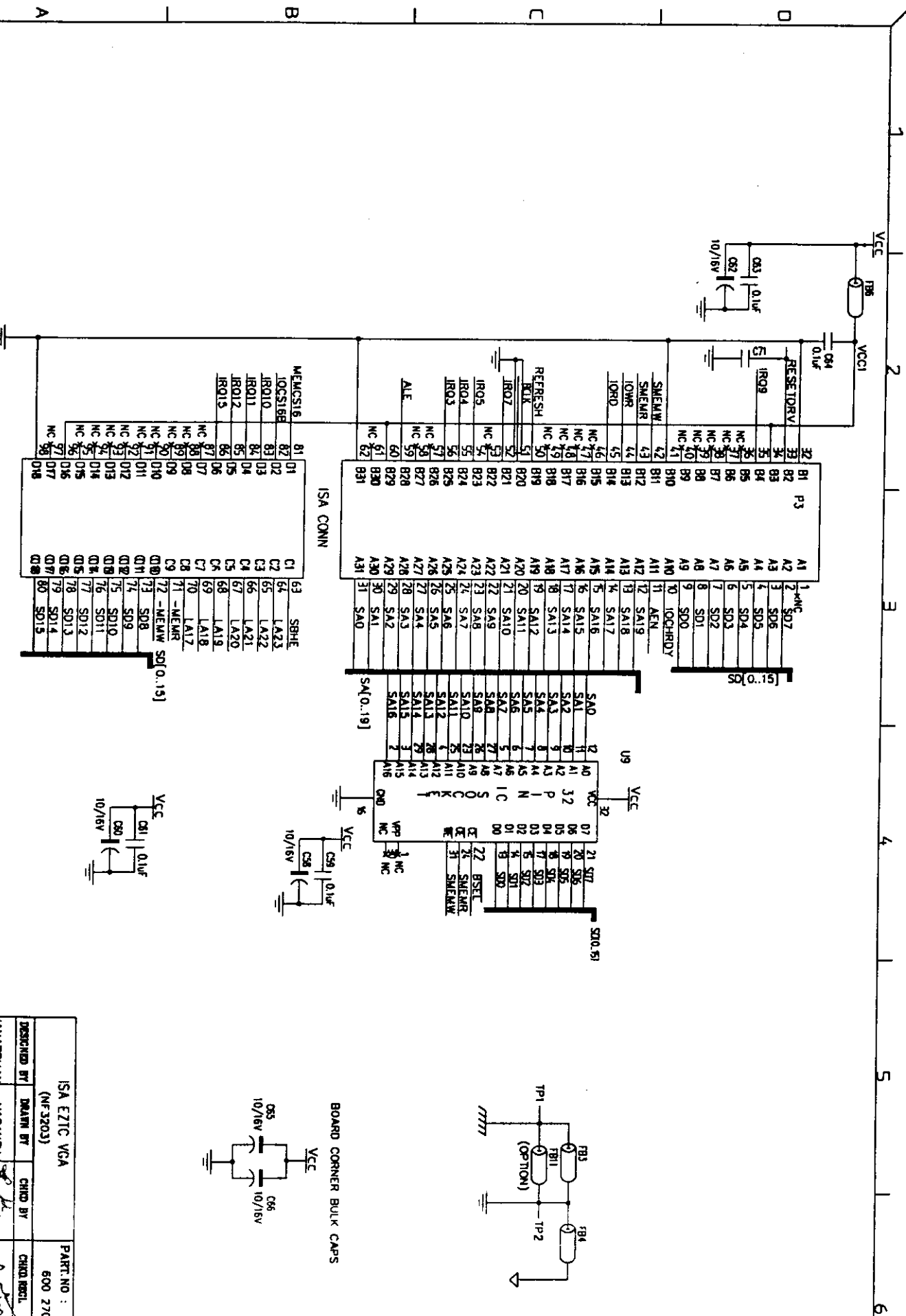
ISA EZIC VGA (NF3204)		PART NO : 600 2701	
DESIGNED BY	DRAWN BY	CHD BY	CHD RECL
ANITHA	VASANTH	12/13/19	13-344
SHEET : 4 OF 4		VXL Instrument	
REV : Z01		BANGALORE	

NOTE
P4 : 1-2 SHORT EPROM DISABLE
P4 : 1-2 OPEN EPROM ENABLE

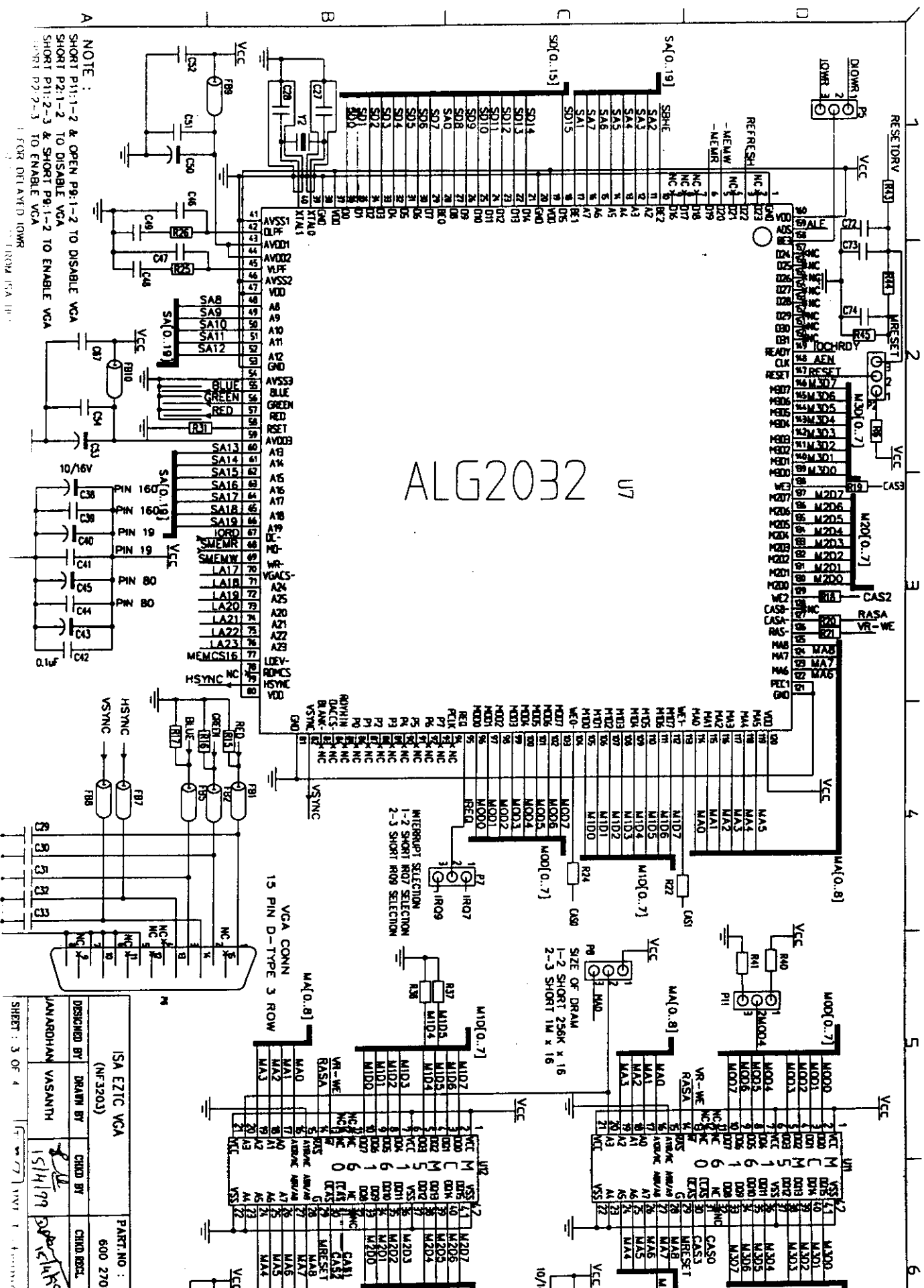


NOTE :
▽ ANALOG GND
⏏ BOARD GND
⏏ CHASSIS GND



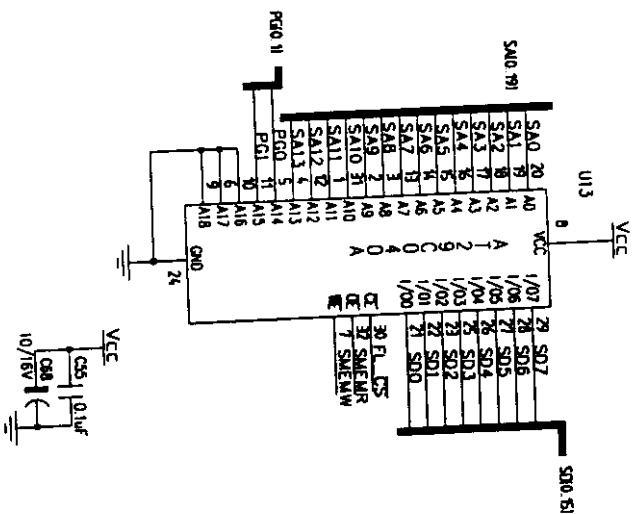
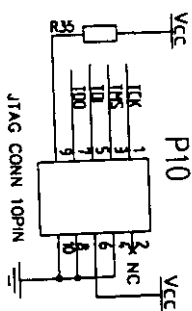
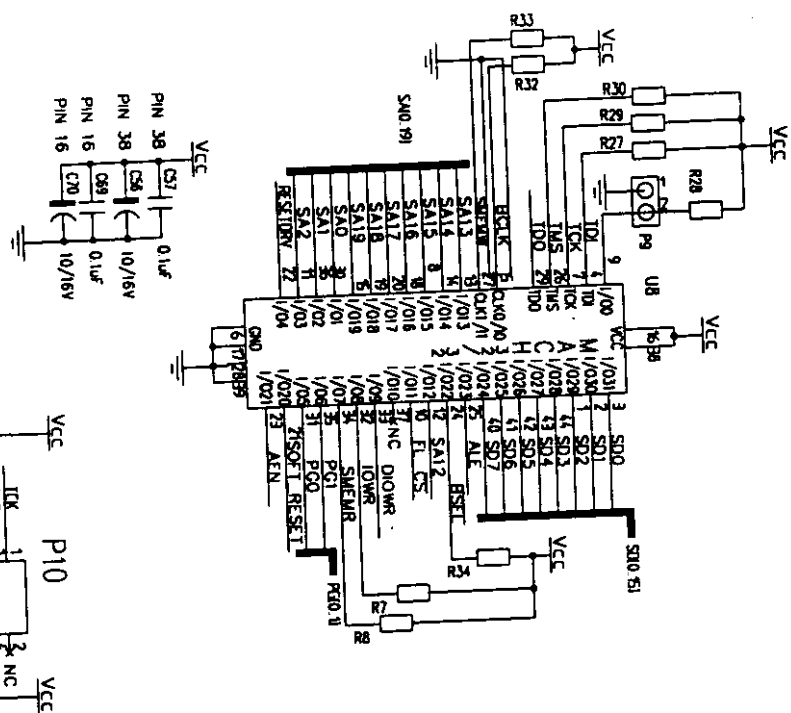


ALG2032 U7



NOTE :
SHORT P11:1-2 & OPEN P8:1-2 TO DISABLE VGA
SHORT P2:1-2 TO DISABLE VGA
SHORT P11:2-3 & SHORT P8:1-2 TO ENABLE VGA
SHORT P2:2-3 TO ENABLE VGA

ISA EZTC VGA		PART NO. :	
(NF3203)		600 2701	
DESIGNED BY	DRAWN BY	CHD BY	CHD RECL
JANARDHAN	VASANTH	15/11/99	15/11/99



ISA EZIC VGA
(NF3203)

PART. NO : 600 277

DESIGNED BY

DETAILED BY

CHD BY

CHD BY

JANARDHAN

WASANTH

15/11/99

15/11/99

SHEET : 4 OF 4

REV : A02



VXL Instrument

BANG.