

GAE-3020 BLOCK DIAGRAM

The diagram illustrates the GAE-3020 system architecture, centered around the HELIUM microcontroller. Key components and their connections include:

- Connectors:** RJ-45 and DB-9 connectors are connected to the SP3232 and ES2009 serial-to-parallel converters, respectively. An RJ-JJ connector is connected to a transformer (T2) and a pulse transformer (PULSE B2136 1:1.1).
- Microcontroller (HELIUM):** The central processing unit, connected to various peripheral components.
- Memory:** SDRAM (4Mx16) and FLASH (2Mx8) are connected to the HELIUM microcontroller. A 29LV040 FLASH is connected to the AD6449.
- ADCs:** AD8022 and AD8016 are connected to the HELIUM microcontroller. The AD6440 is connected to the AD8022 and the AD6449.
- Serial-to-Parallel Converters:** SP3232 and ES2009 are used for serial communication.
- Timing:** XTALP and XTALN are connected to a 20MHz crystal.
- GPIOs:** GPIO0 is connected to the EPM3020A.
- LEDs:** DSL_TX LED, DSL_RX LED, and DSL_LINK LED are connected to the EPM3020A.
- Other Components:** A VCX0 component is connected to the AD6449. A SW1 (switch) is connected to the SDRAM.

The diagram also includes a title block with the following information:

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