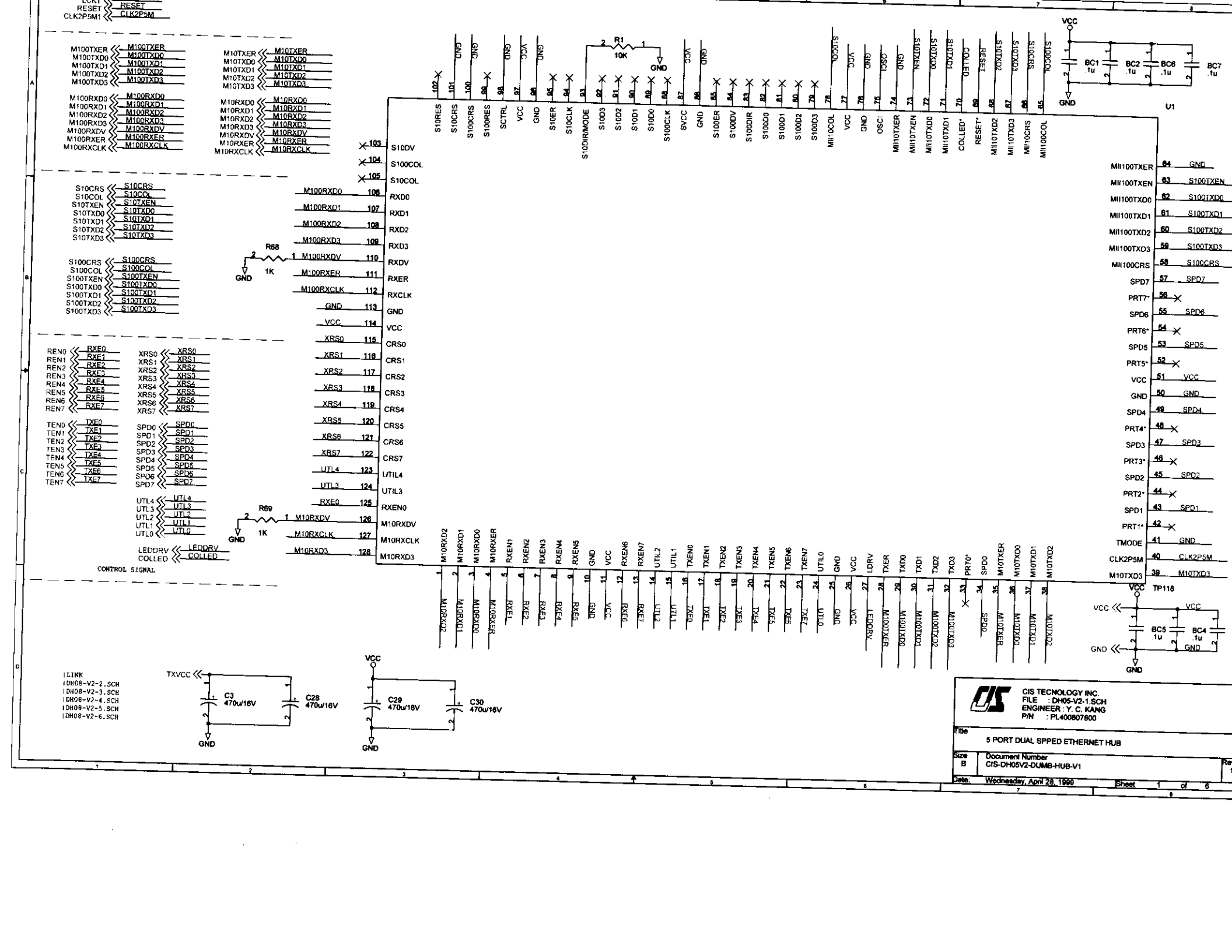
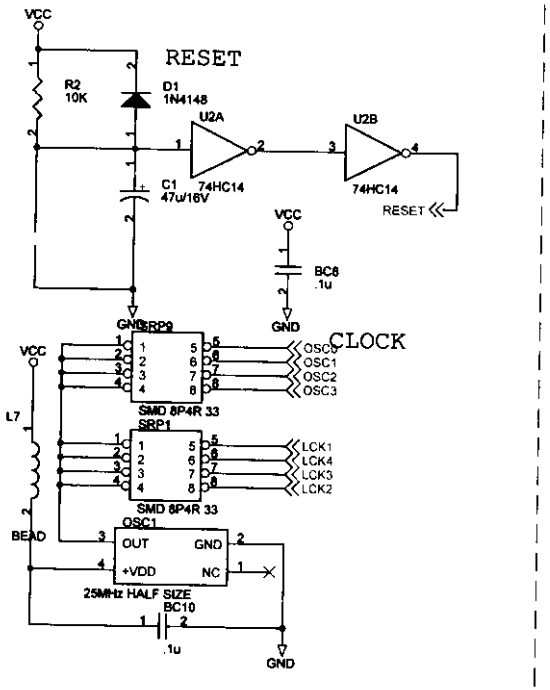
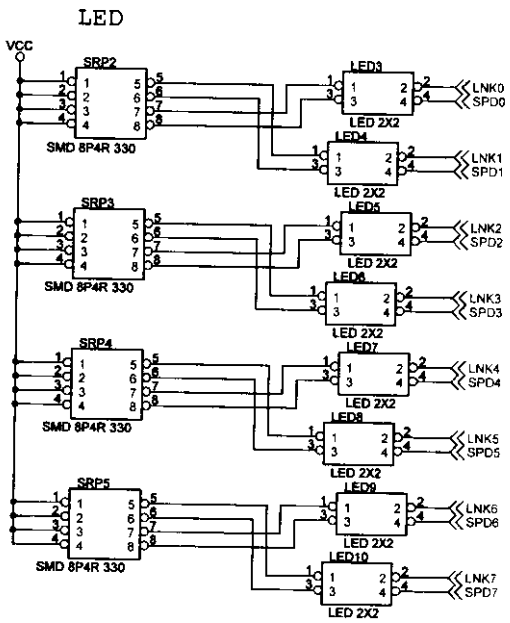
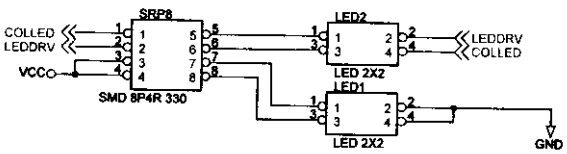
EXHIBIT D

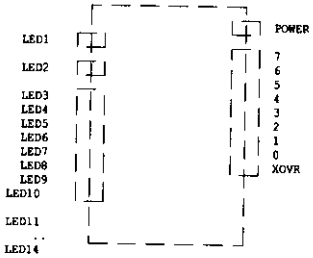
Circuit Diagram

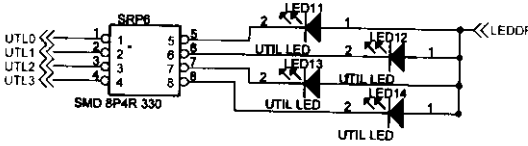

RESET
CLK2P5M1
M100TXER
M100TXD0
M100TXD1
M100TXD2
M100TXD3
M100RXD0
M100RXD1
M100RXD2
M100RXD3
M100RXDV
M100RXER
M100RXCLK
M10TXER
M10TXD0
M10TXD1
M10TXD2
M10TXD3
M10RXD0
M10RXD1
M10RXD2
M10RXD3
M10RXDV
M10RXER
M10RXCLK
S10CRS
S10COL
S10TXEN
S10TXD0
S10TXD1
S10TXD2
S10TXD3
S100CRS
S100COL
S100TXEN
S100TXD0
S100TXD1
S100TXD2
S100TXD3
REN0
REN1
REN2
REN3
REN4
REN5
REN6
REN7
RXE0
RXE1
RXE2
RXE3
RXE4
RXE5
RXE6
RXE7
XRS0
XRS1
XRS2
XRS3
XRS4
XRS5
XRS6
XRS7
TEN0
TEN1
TEN2
TEN3
TEN4
TEN5
TEN6
TEN7
TXE0
TXE1
TXE2
TXE3
TXE4
TXE5
TXE6
TXE7
SPD0
SPD1
SPD2
SPD3
SPD4
SPD5
SPD6
SPD7
UTL4
UTL3
UTL2
UTL1
UTL0
LEDDRV
COLLED
CONTROL SIGNAL
R68
1K
R69
1K
R1
10K
GND
VCC
U1
S10RES
S10CRS
S100CRS
S100RES
SCTRL
S10ER
S10CLK
S10DIR/MODE
S10D3
S10D2
S10D1
S10D0
S100CLK
SVCC
S100ER
S100DV
S100DIR
S100D0
S100D1
S100D2
S100D3
MII10COL
OSCI
MII10TXER
MII10TXEN
MII10TXD0
MII10TXD1
COLLED*
RESET*
MII10TXD2
MII10TXD3
MII10CRS
MII100COL
MII100TXER
MII100TXEN
MII100TXD0
MII100TXD1
MII100TXD2
MII100TXD3
MII100CRS
SPD7
PRT7*
SPD6
PRT6*
SPD5
PRT5*
SPD4
PRT4*
SPD3
PRT3*
SPD2
PRT2*
SPD1
PRT1*
TMODE
CLK2P5M
M10TXD3
TP118
BC1 .1u
BC2 .1u
BC6 .1u
BC7 .1u
BC5 .1u
BC4 .1u
LINK
DH08-V2-2.SCH
DH08-V2-3.SCH
DH08-V2-4.SCH
DH08-V2-5.SCH
DH08-V2-6.SCH
TXVCC
C3 470u/16V
C28 470u/16V
C29 470u/16V
C30 470u/16V
CIS TECNOLOGY INC.
FILE : DH05-V2-1.SCH
ENGINEER : Y. C. KANG
P/N : PL400807600
5 PORT DUAL SPPED ETHERNET HUB
Size B
Document Number
CIS-DH05V2-DUMB-HUB-V1
Date: Wednesday, April 28, 1999
Sheet 1 of 6

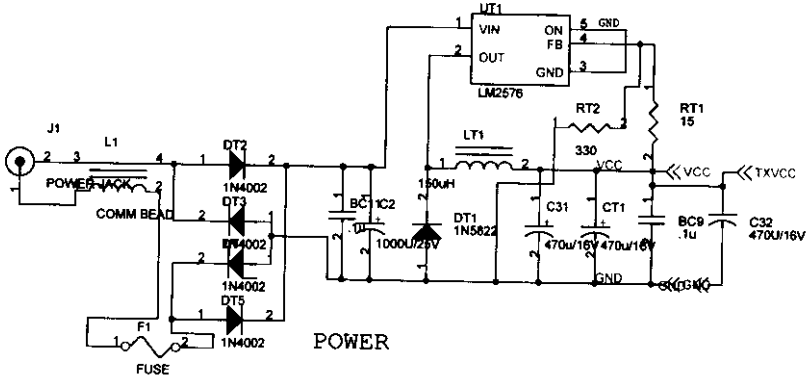










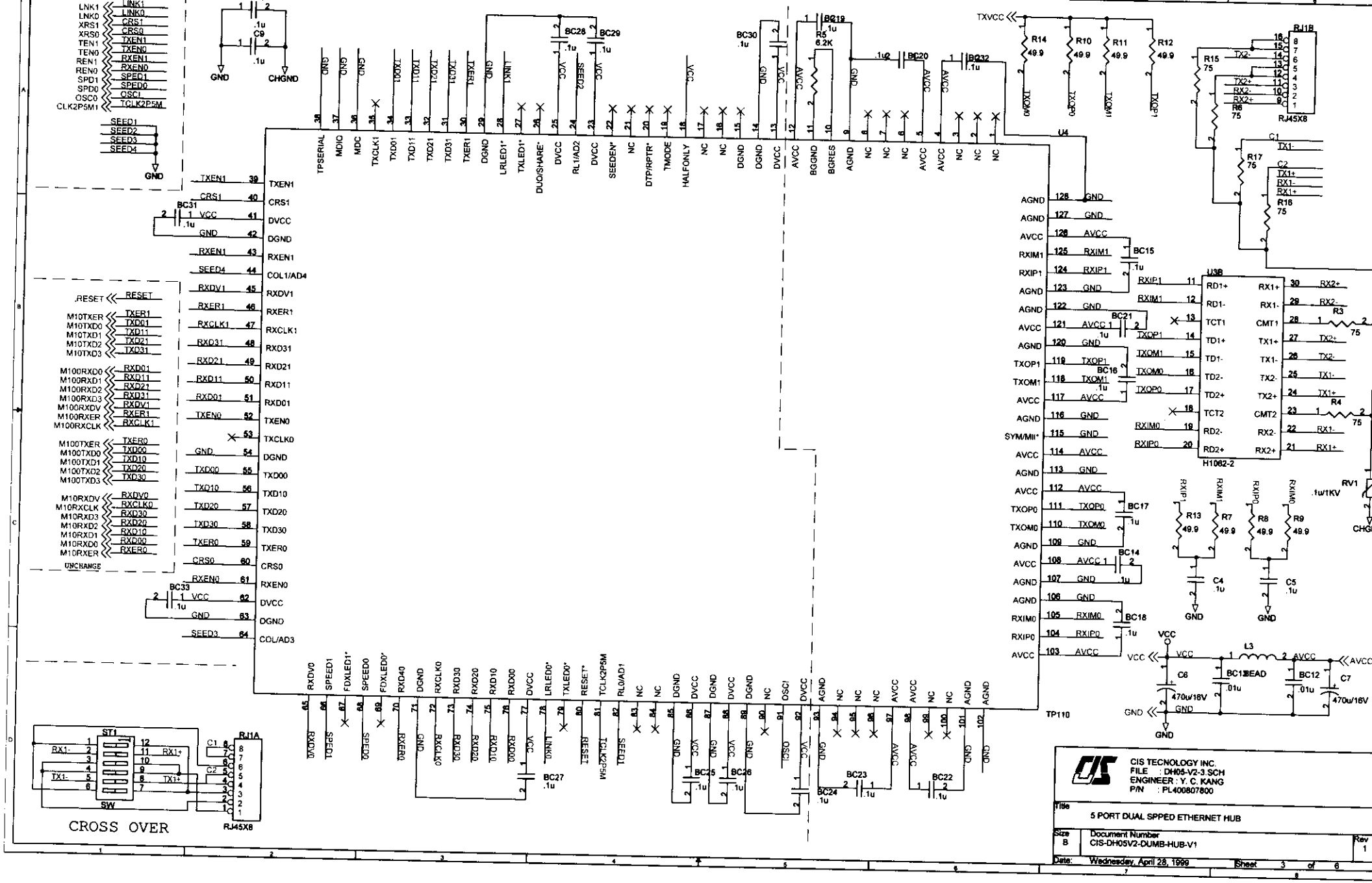


CIS TECNOLOGY INC.
FILE : DH05-V2-2.SCH
ENGINEER : Y. C. KANG
P/N : PL400807800

Title: 5 PORT DUAL SPPED ETHERNET HUB

Size: B | Document Number: CIS-DH05V2-DUMB-HUB-V1

Date: Wednesday, April 26, 1999 | Sheet 2 of 6


CIS TECNOLOGY INC.
FILE : DH05-V2-3.SCH
ENGINEER : Y. C. KANG
P/N : PL400807800
5 PORT DUAL SPPED ETHERNET HUB
Document Number
CIS-DH05V2-DUMB-HUB-V1
Wednesday, April 28, 1999
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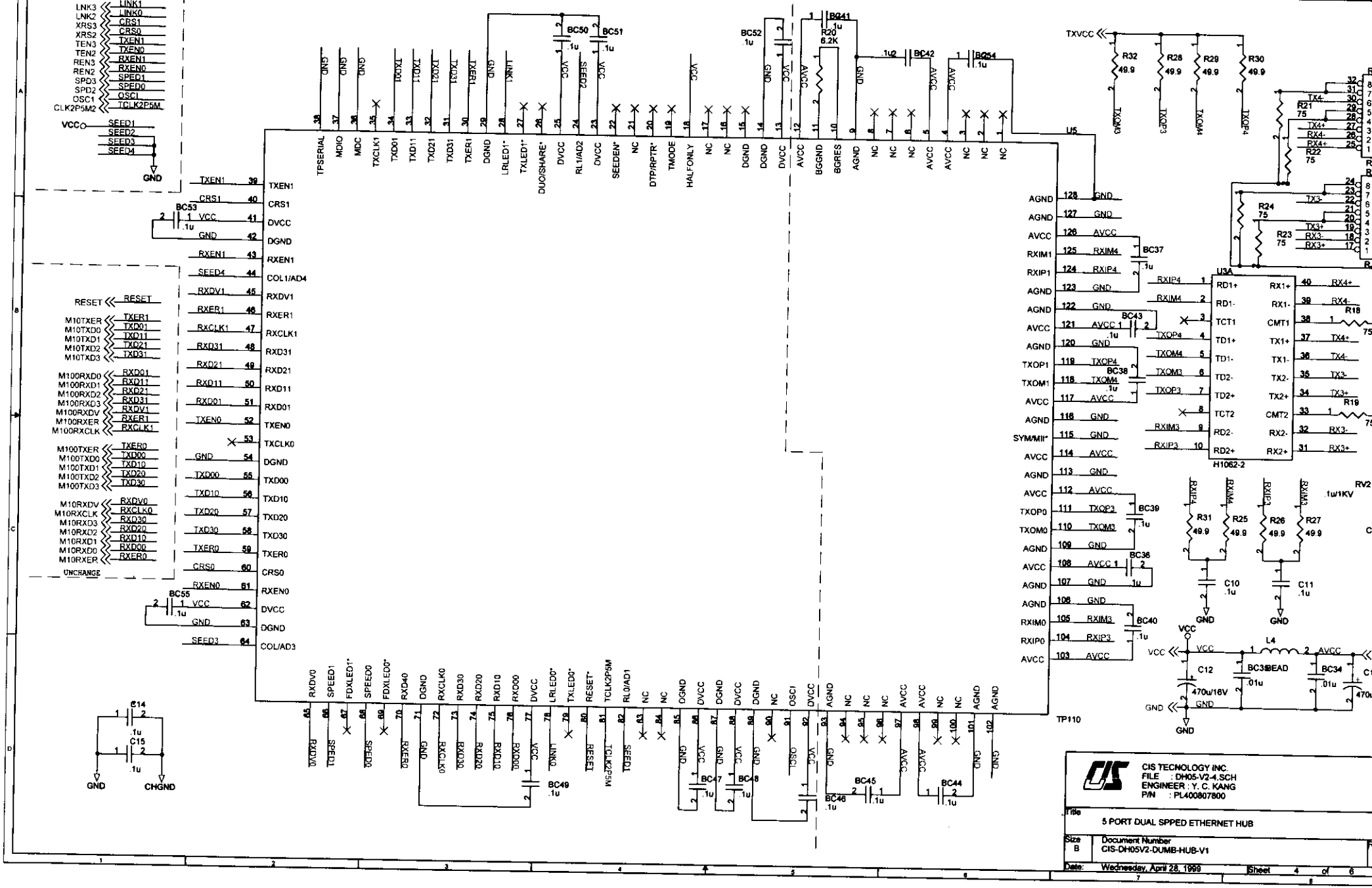
CROSS OVER
U4
U3B
H1062-2
RJ45X8
TP110


U5
U3A
H1062-2
TP110
CIS TECNOLOGY INC.
FILE : DH05-V2-4.SCH
ENGINEER : Y. C. KANG
P/N : PL400807800
5 PORT DUAL SPPED ETHERNET HUB
Document Number
CIS-DH05V2-DUMB-HUB-V1
Wednesday, April 28, 1999
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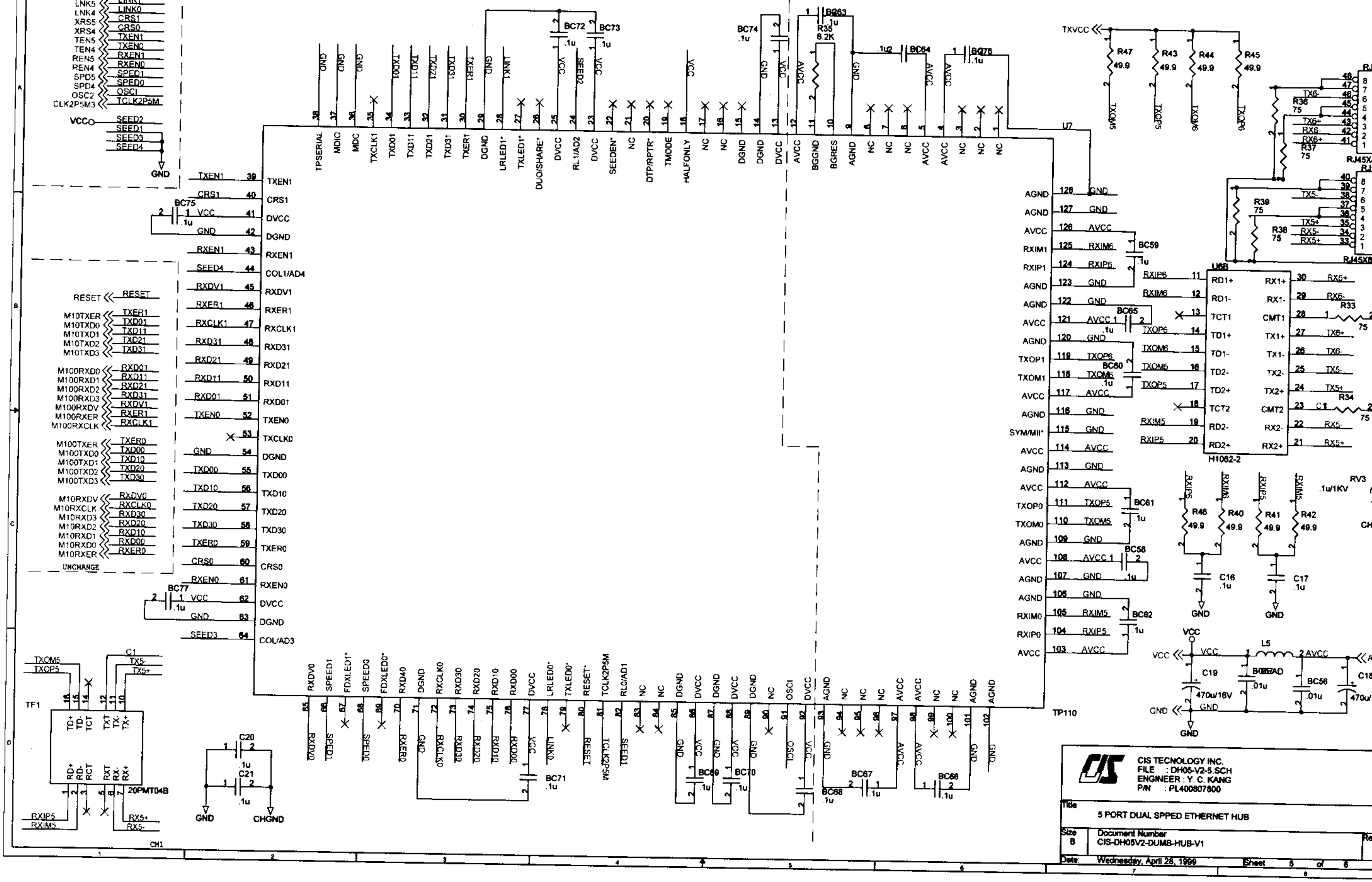
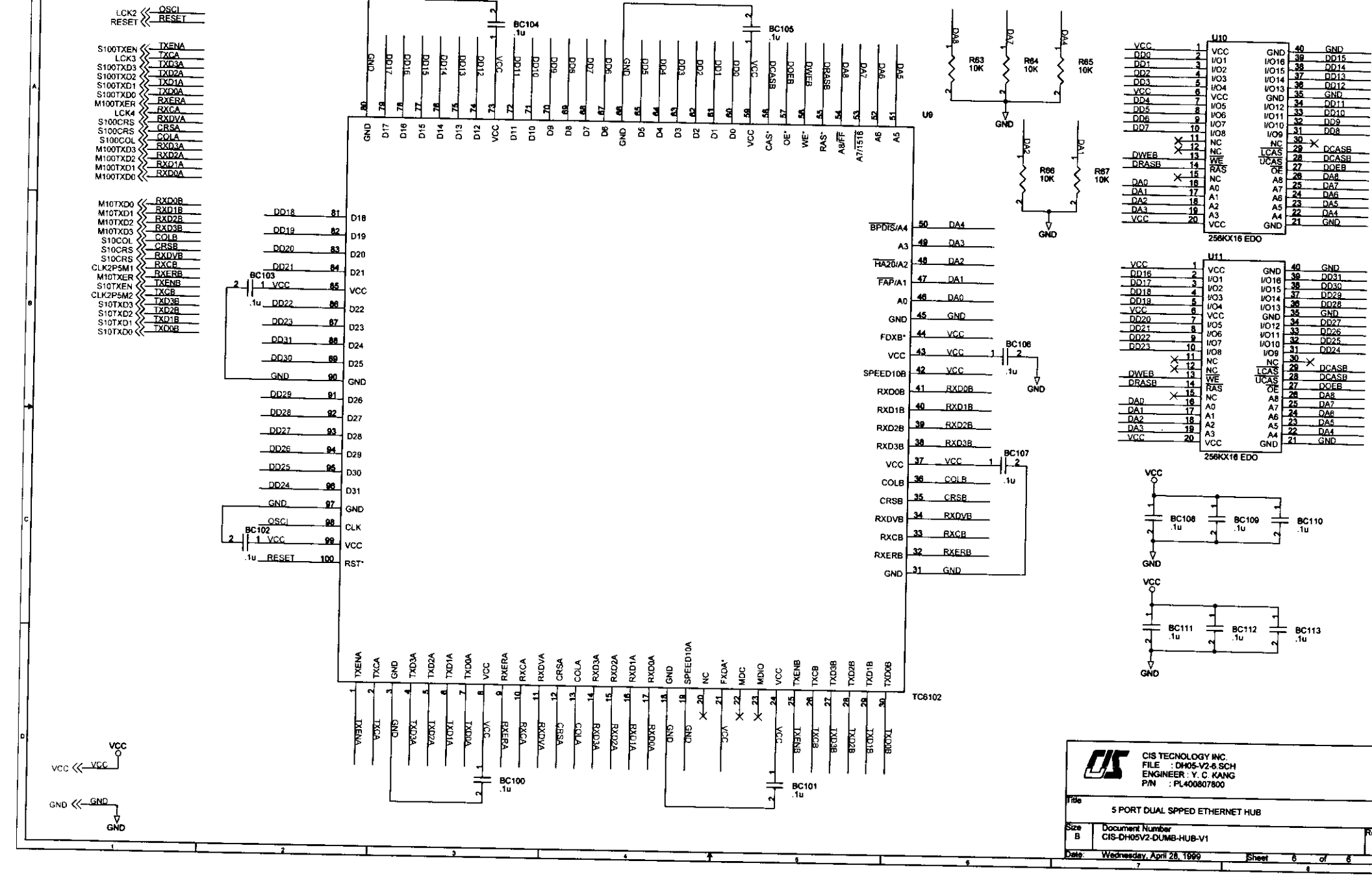


CIS TECNOLOGY INC.
FILE : DH05-V2-5.SCH
ENGINEER : Y. C. KANG
P/N : PL400807800

Title	5 PORT DUAL SPPED ETHERNET HUB
Size B	Document Number CIS-DH05V2-DUMB-HUB-V1
Date:	Wednesday, April 28, 1999 Sheet 5 of 6


U9
TC6102
U10
U11
256KX16 EDO
256KX16 EDO
BC100 .1u
BC101 .1u
BC102 .1u RESET
BC103 .1u VCC
BC104 .1u
BC105 .1u
BC106 .1u
BC107 .1u
BC108 .1u
BC109 .1u
BC110 .1u
BC111 .1u
BC112 .1u
BC113 .1u
R63 10K
R64 10K
R65 10K
R66 10K
R67 10K
LCK2 OSCI
RESET
S100TXEN TXENA
LCK3 TXCA
S100TXD3 TXD3A
S100TXD2 TXD2A
S100TXD1 TXD1A
S100TXD0 TXD0A
M100TXER RXERA
LCK4 RXCA
S100CRS RXDVA
S100CRS CRSA
S100COL COLA
M100TXD3 RXD3A
M100TXD2 RXD2A
M100TXD1 RXD1A
M100TXD0 RXD0A
M10TXD0 RXD0B
M10TXD1 RXD1B
M10TXD2 RXD2B
M10TXD3 RXD3B
S10COL COLB
S10CRS CRSB
S10CRS RXDVB
CLK2P5M1 RXCB
M10TXER RXERB
S10TXEN TXENB
CLK2P5M2 TXCB
S10TXD3 TXD3B
S10TXD2 TXD2B
S10TXD1 TXD1B
S10TXD0 TXD0B
VCC
GND
CIS TECNOLOGY INC.
FILE : DH05-V2-6.SCH
ENGINEER : Y. C. KANG
P/N : PL400807800
5 PORT DUAL SPPED ETHERNET HUB
Size B
Document Number CIS-DH05V2-DUMB-HUB-V1
Date: Wednesday, April 28, 1999
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