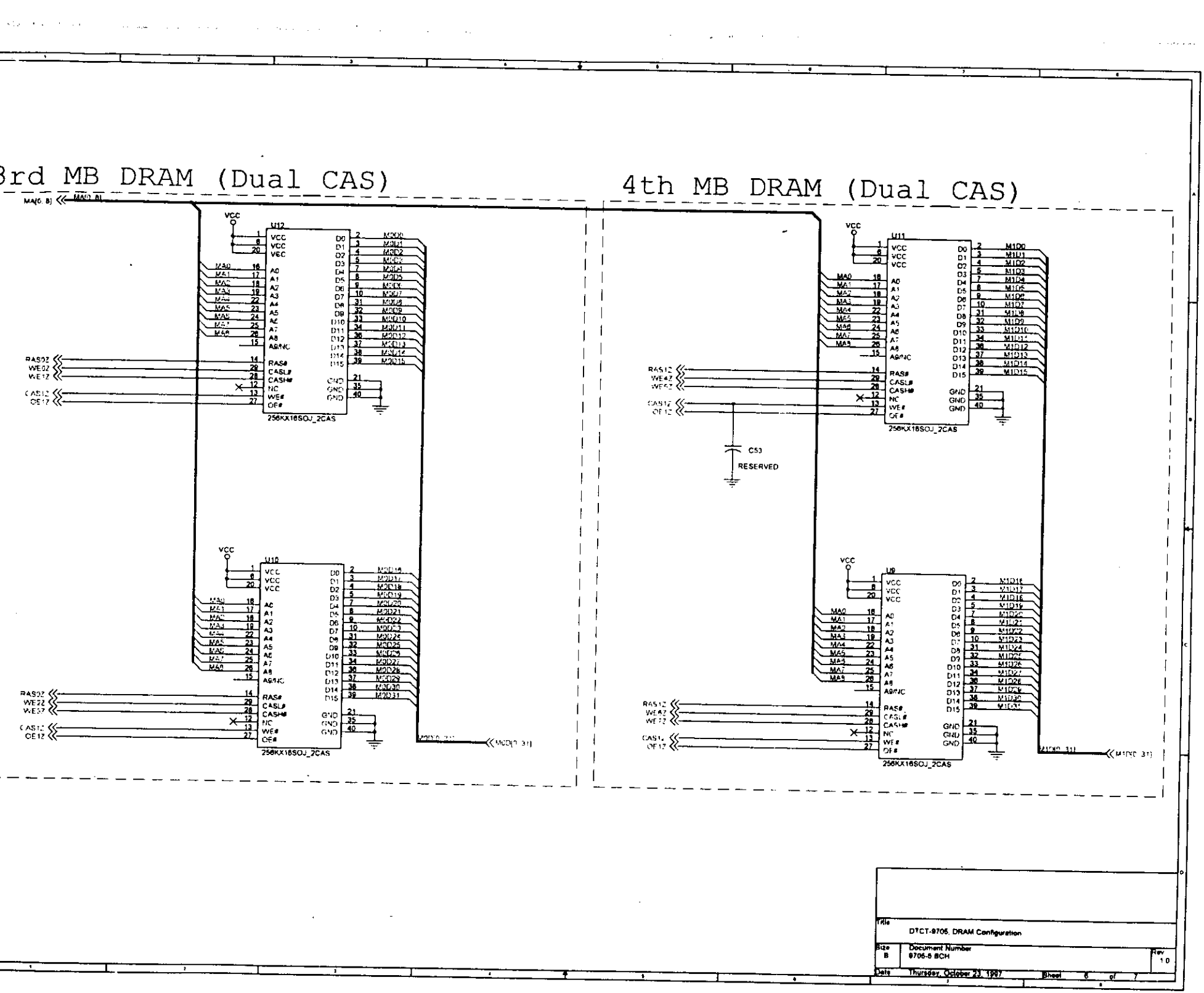
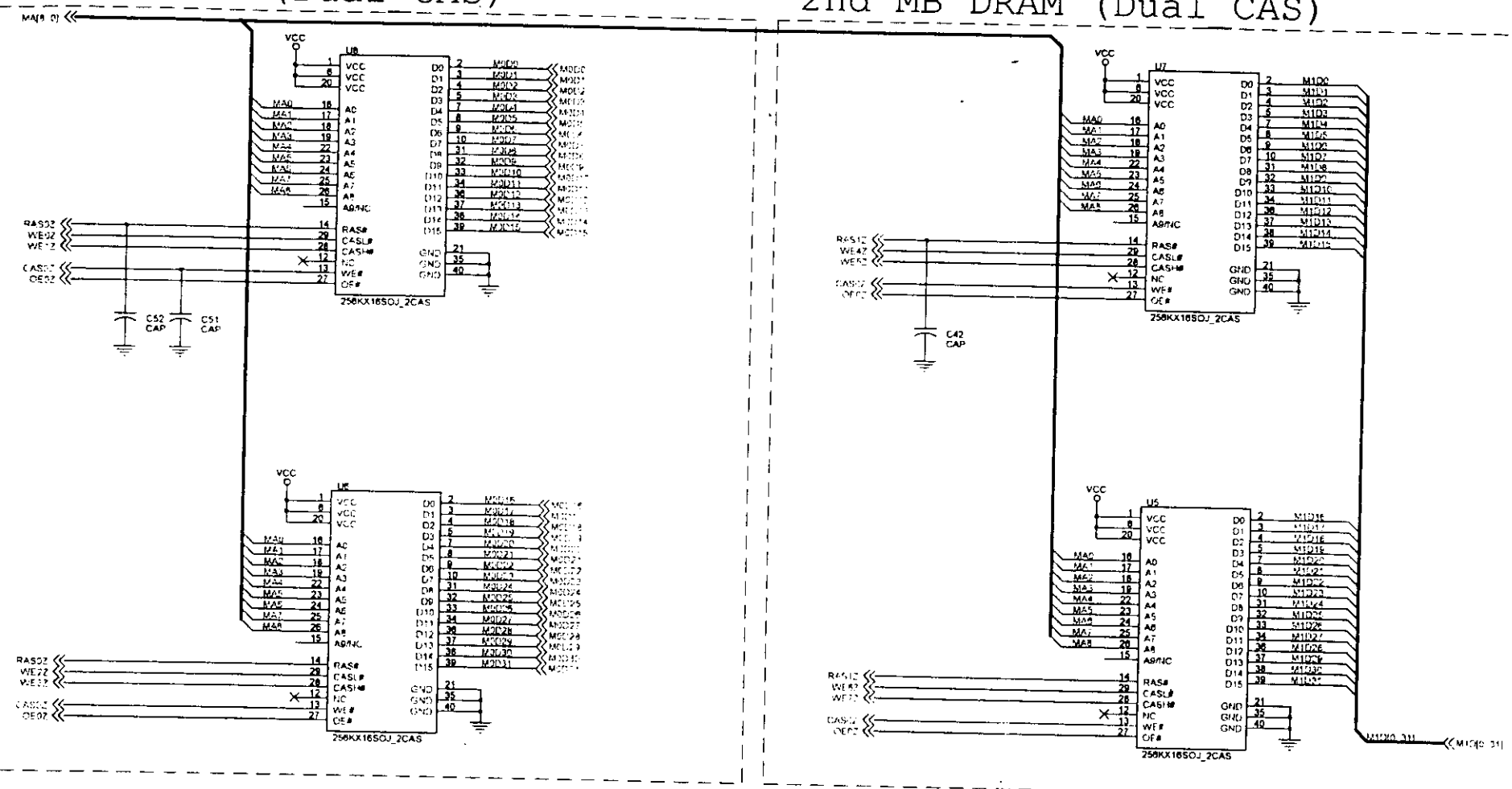

3rd MB DRAM (Dual CAS)
4th MB DRAM (Dual CAS)
U12
U10
U11
U9
256KX16SOJ_2CAS
C53
RESERVED
Title
DTCT-9705, DRAM Configuration
Size
B
Document Number
9705-8 BCH
Rev
1.0
Date
Thursday, October 23, 1997
Sheet
6
of
7

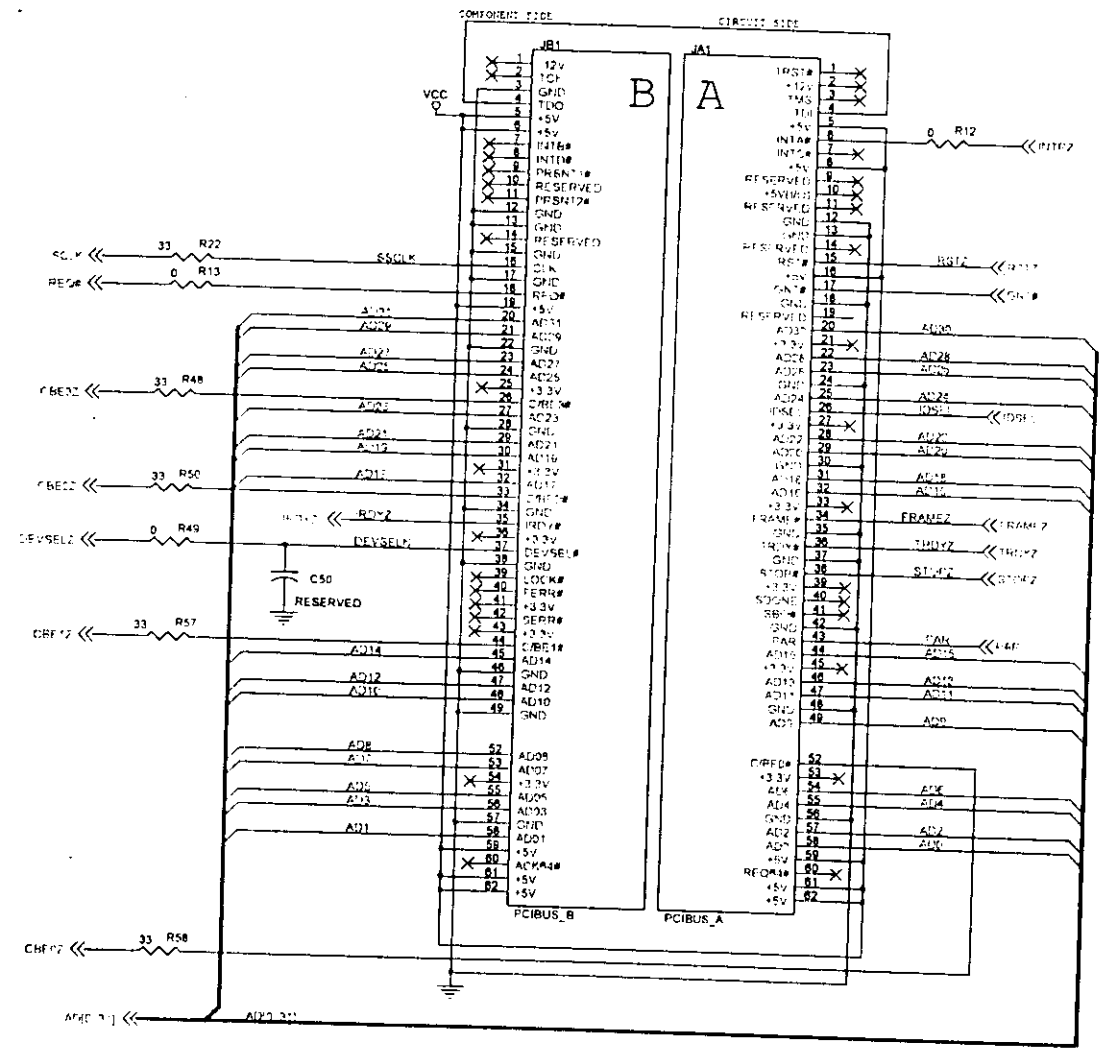
st MB DRAM (Dual CAS)

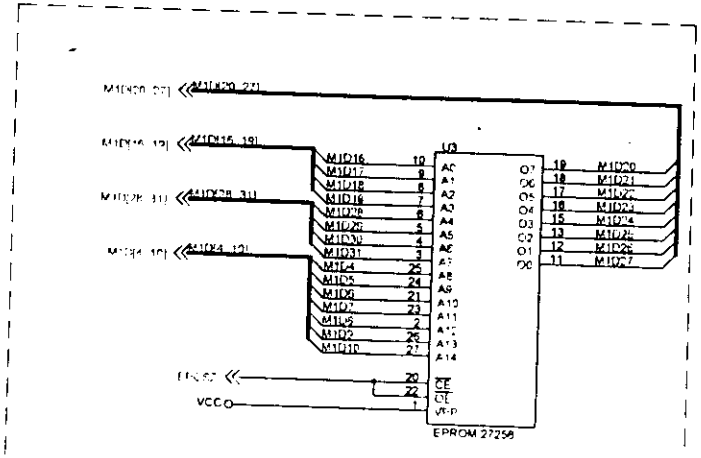
2nd MB DRAM (Dual CAS)



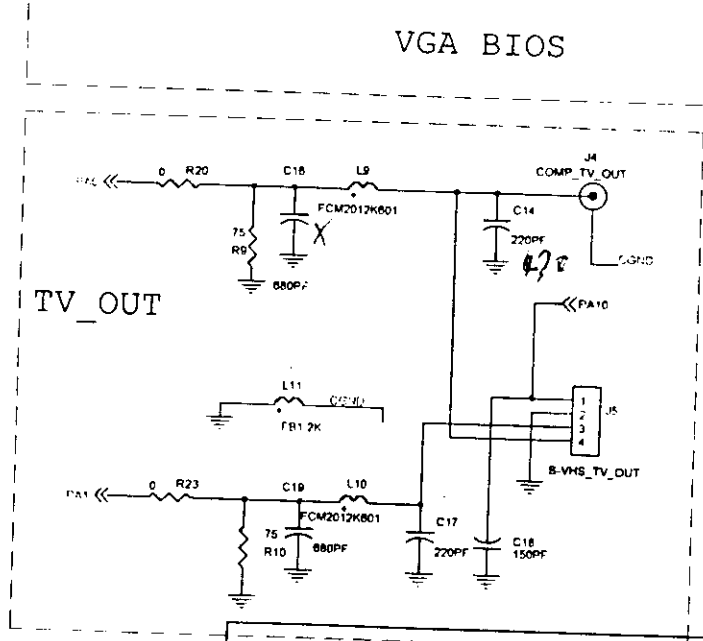
Title	DTCT-9705, DRAM Configuration	
Size: B	Document Number: 9705-4.SCH	Rev: 2.0
Date: Thursday, October 23, 1997	Sheet 5 of 7	

PCI Bus Connector





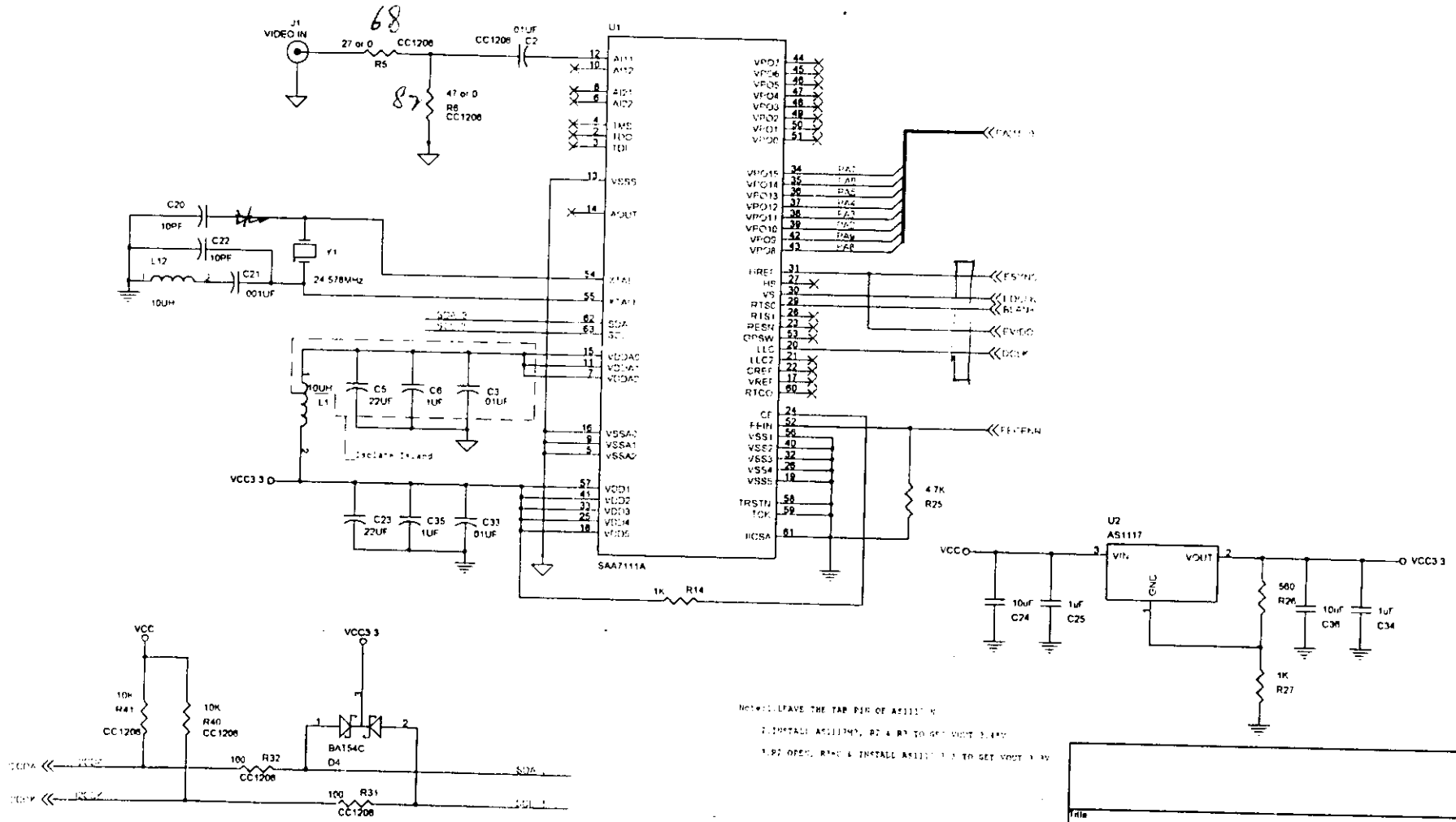
VGA BIOS



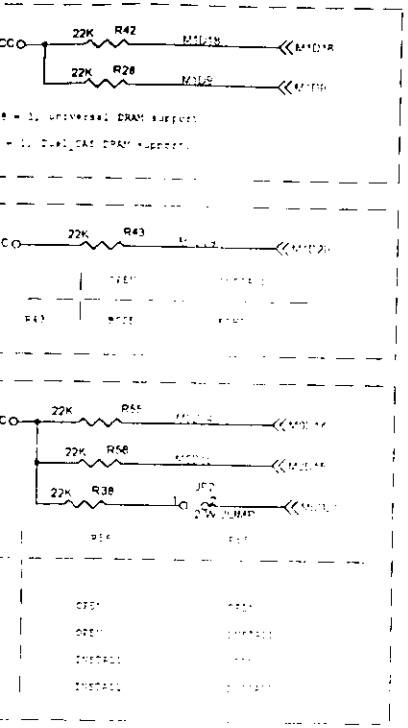
TV_OUT

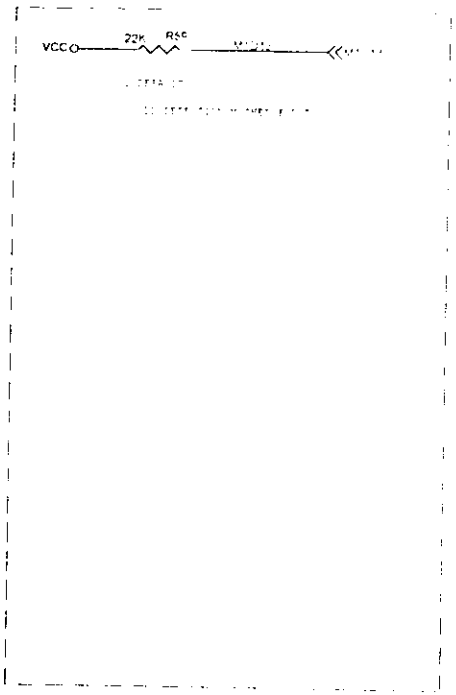
NOTE:
Please do not connect to TV.

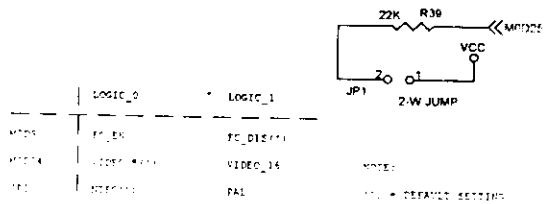
Title	DTCT-9705, PCI BUS & RIOS	
Size B	Document Number 9705-2.SCH	Rev 1.0
Date	Friday, October 24, 1997	Sheet 3 of 7

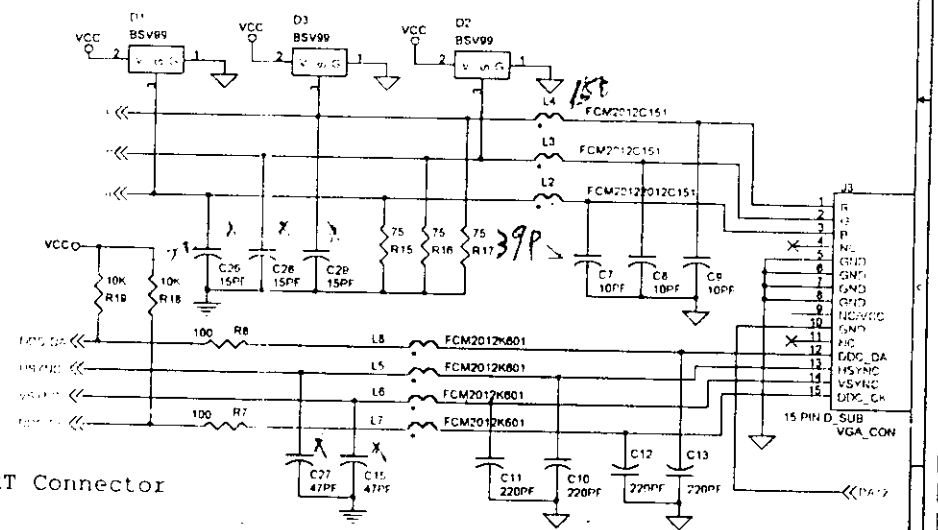

J1
VIDEO IN
27 or 0
R5
CC1206
68
47 or 0
R6
CC1206
87
01UF
C2
U1
SAA7111A
C20
10PF
C22
10PF
L12
10UH
C21
001UF
Y1
24 578MHz
C5
22UF
C6
1UF
C3
01UF
L1
10UH
C23
22UF
C35
1UF
C31
01UF
VCC3 3
4.7K
R25
1K
R14
U2
AS1117
VIN
VOUT
GND
VCC
10uF
C24
1uF
C25
560
R26
10uF
C36
1uF
C34
1K
R27
VCC3 3
10K
R41
10K
R40
CC1206
BAT54C
D4
100
R32
CC1206
100
R31
CC1206
SDA
SCL
Title
DTCT-9705, 7111A SUB CIRCUIT
Size
B
Document Number
9705-6 BCH
Rev
1 0
Date
Friday, October 24, 1997
Sheet
7
of
7

Configuration Resisters



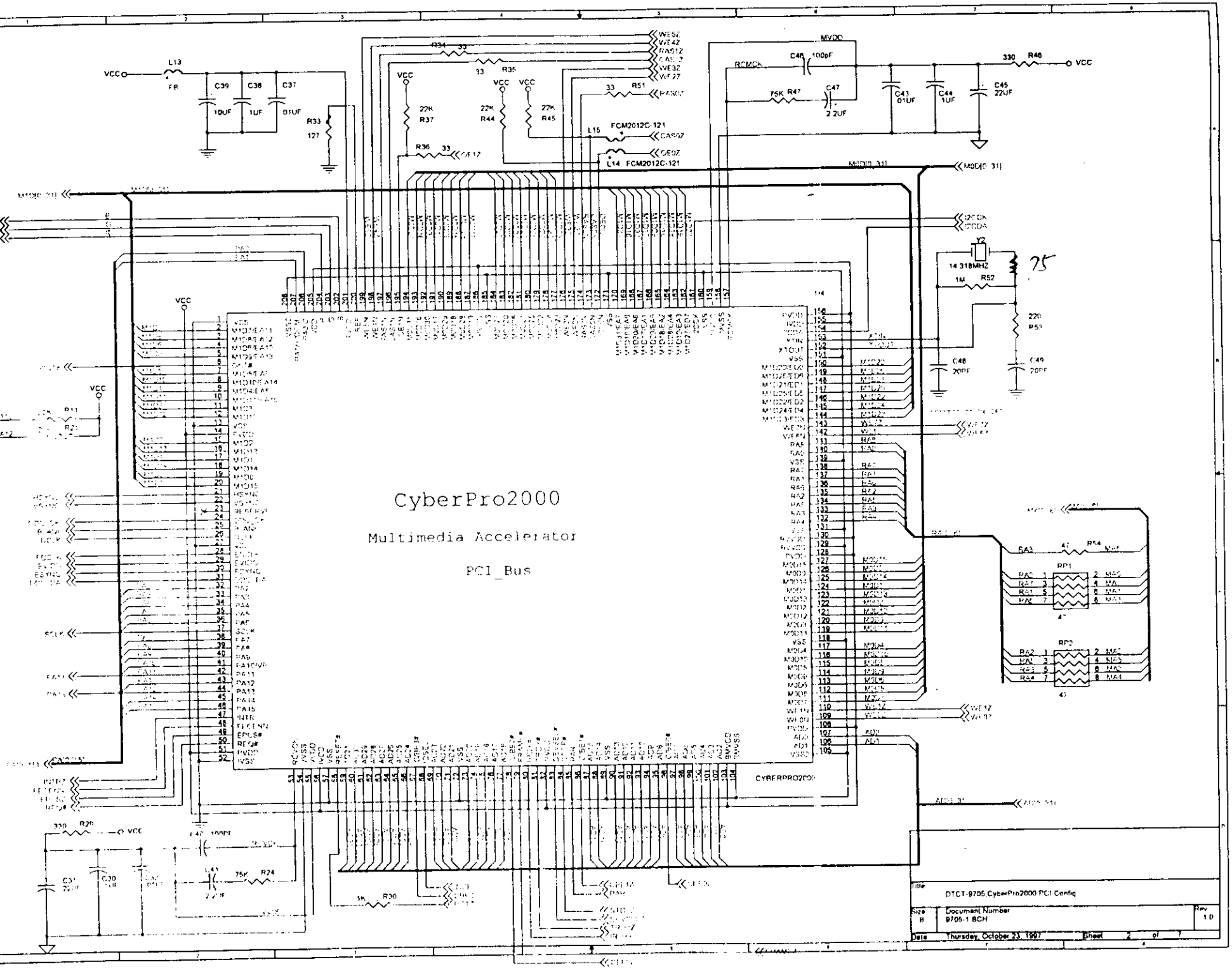






CRT Connector

Title	DTCT-9705, CRT & Feature Conn		
Size B	Document Number 9705-3 BCH		Rev * 0
Date	Thursday, October 23, 1997	Sheet 4 of 7	


CyberPro2000
Multimedia Accelerator
PCI_Bus
Title
DTCT-9705,CyberPro2000 PCI Config
Size
B
Document Number
9705-1 BCH
Rev
1.0
Date
Thursday, October 23, 1997
Sheet
2
of
7