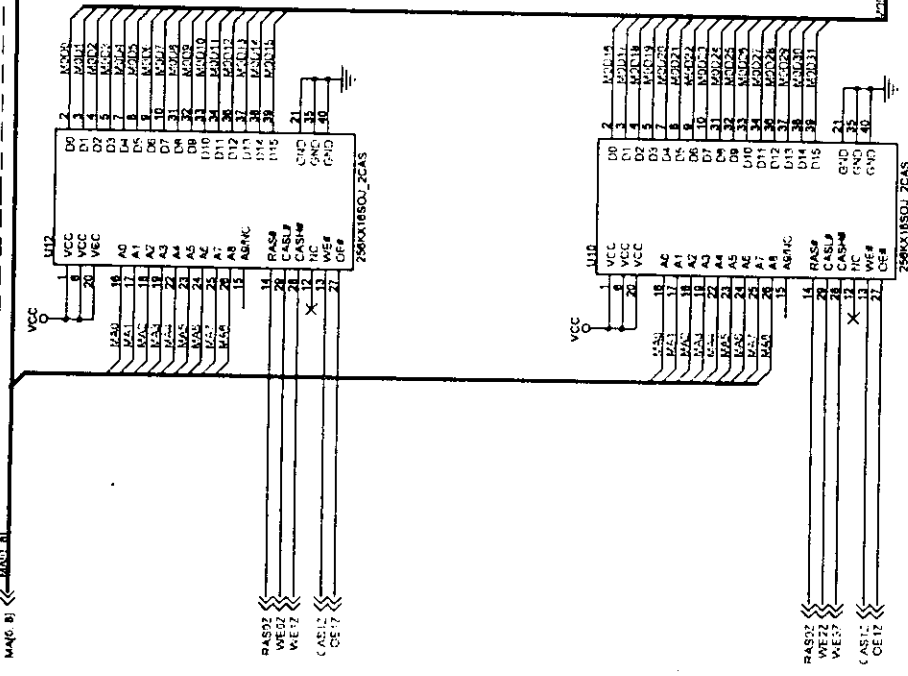
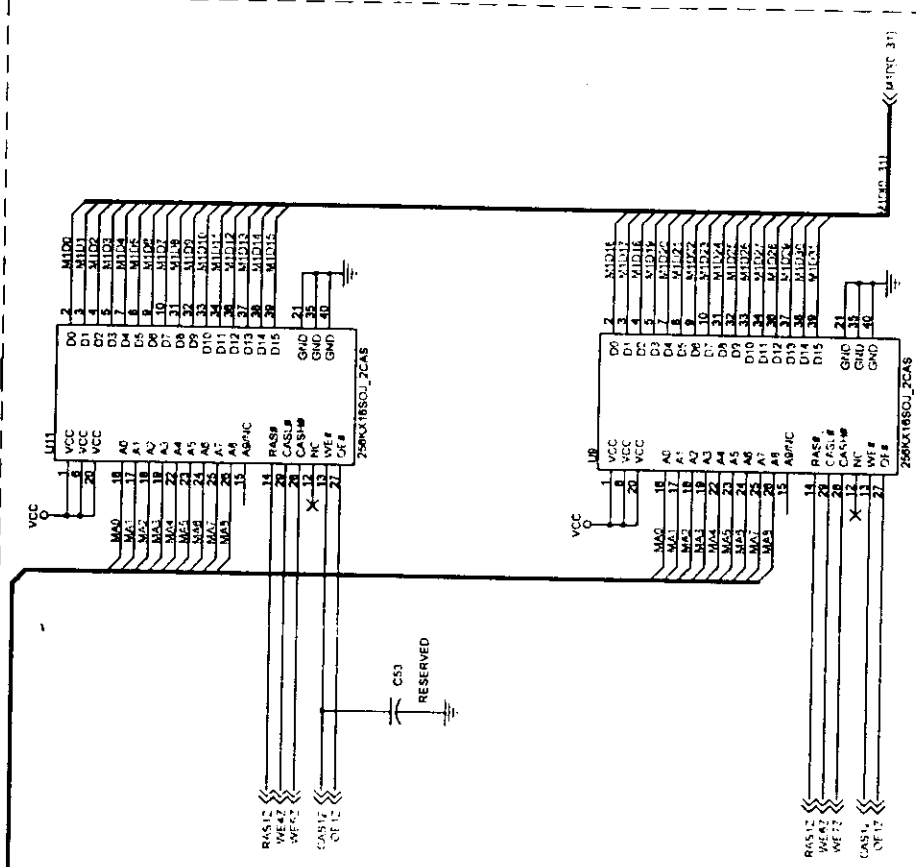


3rd MB DRAM (Dual CAS)



4th MB DRAM (Dual CAS)



DTCT-8706 DRAM Configuration

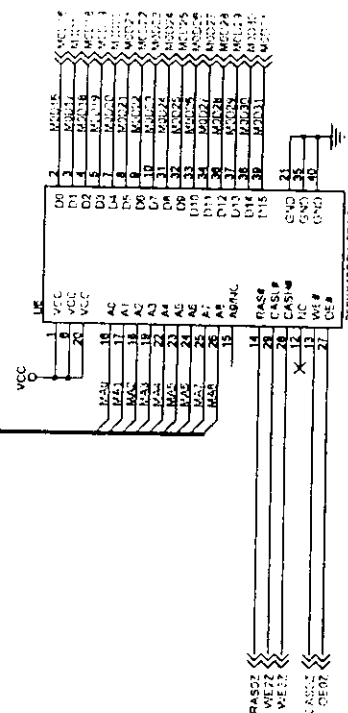
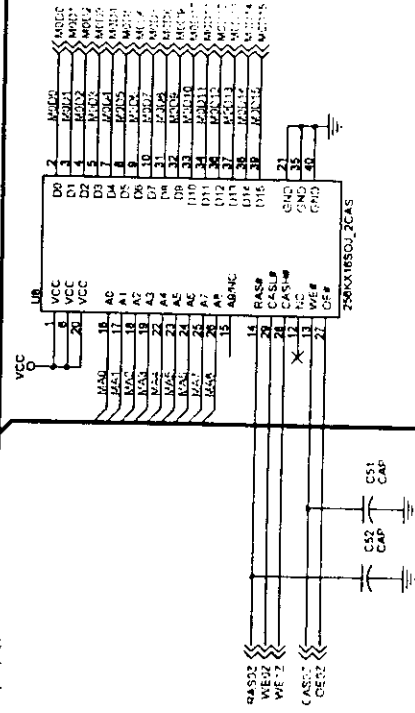
Document Number
8706-0 BOM

Date Thursday, October 23, 1987

Rev 1.0

1st MB DRAM (Dual CAS)

2nd MB DRAM (Dual CAS)



COMPONENT SIDE

CIRCUIT SIDE

PCIBUS_A

PCIBUS_B

PCIBUS_C

PCIBUS_D

PCIBUS_E

PCIBUS_F

PCIBUS_G

PCIBUS_H

PCIBUS_I

PCIBUS_J

PCIBUS_K

PCIBUS_L

PCIBUS_M

PCIBUS_N

PCIBUS_O

PCIBUS_P

PCIBUS_Q

PCIBUS_R

PCIBUS_S

PCIBUS_T

PCIBUS_U

PCIBUS_V

PCIBUS_W

PCIBUS_X

PCIBUS_Y

PCIBUS_Z

PCIBUS_1

PCIBUS_2

PCIBUS_3

PCIBUS_4

PCIBUS_5

PCIBUS_6

PCIBUS_7

PCIBUS_8

PCIBUS_9

PCIBUS_10

PCIBUS_11

PCIBUS_12

PCIBUS_13

PCIBUS_14

PCIBUS_15

PCIBUS_16

PCIBUS_17

PCIBUS_18

PCIBUS_19

PCIBUS_20

PCIBUS_21

PCIBUS_22

PCIBUS_23

PCIBUS_24

PCIBUS_25

PCIBUS_26

PCIBUS_27

PCIBUS_28

PCIBUS_29

PCIBUS_30

PCIBUS_31

PCIBUS_32

PCIBUS_33

PCIBUS_34

PCIBUS_35

PCIBUS_36

PCIBUS_37

PCIBUS_38

PCIBUS_39

PCIBUS_40

PCIBUS_41

PCIBUS_42

PCIBUS_43

PCIBUS_44

PCIBUS_45

PCIBUS_46

PCIBUS_47

PCIBUS_48

PCIBUS_49

PCIBUS_50

PCIBUS_51

PCIBUS_52

PCIBUS_53

PCIBUS_54

PCIBUS_55

PCIBUS_56

PCIBUS_57

PCIBUS_58

PCIBUS_59

PCIBUS_60

PCIBUS_61

PCIBUS_62

PCIBUS_63

PCIBUS_64

PCIBUS_65

PCIBUS_66

PCIBUS_67

PCIBUS_68

PCIBUS_69

PCIBUS_70

PCIBUS_71

PCIBUS_72

PCIBUS_73

PCIBUS_74

PCIBUS_75

PCIBUS_76

PCIBUS_77

PCIBUS_78

PCIBUS_79

PCIBUS_80

PCIBUS_81

PCIBUS_82

PCIBUS_83

PCIBUS_84

PCIBUS_85

PCIBUS_86

PCIBUS_87

PCIBUS_88

PCIBUS_89

PCIBUS_90

PCIBUS_91

PCIBUS_92

PCIBUS_93

PCIBUS_94

PCIBUS_95

PCIBUS_96

PCIBUS_97

PCIBUS_98

PCIBUS_99

PCIBUS_100

PCIBUS_101

PCIBUS_102

PCIBUS_103

PCIBUS_104

PCIBUS_105

PCIBUS_106

PCIBUS_107

PCIBUS_108

PCIBUS_109

PCIBUS_110

PCIBUS_111

PCIBUS_112

PCIBUS_113

PCIBUS_114

PCIBUS_115

PCIBUS_116

PCIBUS_117

PCIBUS_118

PCIBUS_119

PCIBUS_120

PCIBUS_121

PCIBUS_122

PCIBUS_123

PCIBUS_124

PCIBUS_125

PCIBUS_126

PCIBUS_127

PCIBUS_128

PCIBUS_129

PCIBUS_130

PCIBUS_131

PCIBUS_132

PCIBUS_133

PCIBUS_134

PCIBUS_135

PCIBUS_136

PCIBUS_137

PCIBUS_138

PCIBUS_139

PCIBUS_140

PCIBUS_141

PCIBUS_142

PCIBUS_143

PCIBUS_144

PCIBUS_145

PCIBUS_146

PCIBUS_147

PCIBUS_148

PCIBUS_149

PCIBUS_150

PCIBUS_151

PCIBUS_152

PCIBUS_153

PCIBUS_154

PCIBUS_155

PCIBUS_156

PCIBUS_157

PCIBUS_158

PCIBUS_159

PCIBUS_160

PCIBUS_161

PCIBUS_162

PCIBUS_163

PCIBUS_164

PCIBUS_165

PCIBUS_166

PCIBUS_167

PCIBUS_168

PCIBUS_169

PCIBUS_170

PCIBUS_171

PCIBUS_172

PCIBUS_173

PCIBUS_174

PCIBUS_175

PCIBUS_176

PCIBUS_177

PCIBUS_178

PCIBUS_179

PCIBUS_180

PCIBUS_181

PCIBUS_182

PCIBUS_183

PCIBUS_184

PCIBUS_185

PCIBUS_186

PCIBUS_187

PCIBUS_188

PCIBUS_189

PCIBUS_190

PCIBUS_191

PCIBUS_192

PCIBUS_193

PCIBUS_194

PCIBUS_195

PCIBUS_196

PCIBUS_197

PCIBUS_198

PCIBUS_199

PCIBUS_200

PCIBUS_201

PCIBUS_202

PCIBUS_203

PCIBUS_204

PCIBUS_205

PCIBUS_206

PCIBUS_207

PCIBUS_208

PCIBUS_209

PCIBUS_210

PCIBUS_211

PCIBUS_212

PCIBUS_213

PCIBUS_214

PCIBUS_215

PCIBUS_216

PCIBUS_217

PCIBUS_218

PCIBUS_219

PCIBUS_220

PCIBUS_221

PCIBUS_222

PCIBUS_223

PCIBUS_224

PCIBUS_225

PCIBUS_226

PCIBUS_227

PCIBUS_228

PCIBUS_229

PCIBUS_230

PCIBUS_231

PCIBUS_232

PCIBUS_233

PCIBUS_234

PCIBUS_235

PCIBUS_236

PCIBUS_237

PCIBUS_238

PCIBUS_239

PCIBUS_240

PCIBUS_241

PCIBUS_242

PCIBUS_243

PCIBUS_244

PCIBUS_245

PCIBUS_246

PCIBUS_247

PCIBUS_248

PCIBUS_249

PCIBUS_250

PCIBUS_251

PCIBUS_252

PCIBUS_253

PCIBUS_254

PCIBUS_255

PCIBUS_256

PCIBUS_257

PCIBUS_258

PCIBUS_259

PCIBUS_260

PCIBUS_261

PCIBUS_262

PCIBUS_263

PCIBUS_264

PCIBUS_265

PCIBUS_266

PCIBUS_267

PCIBUS_268

PCIBUS_269

PCIBUS_270

PCIBUS_271

PCIBUS_272

PCIBUS_273

PCIBUS_274

PCIBUS_275

PCIBUS_276

PCIBUS_277

PCIBUS_278

PCIBUS_279

PCIBUS_280

PCIBUS_281

PCIBUS_282

PCIBUS_283

PCIBUS_284

PCIBUS_285

PCIBUS_286

PCIBUS_287

PCIBUS_288

PCIBUS_289

PCIBUS_290

PCIBUS_291

PCIBUS_292

PCIBUS_293

PCIBUS_294

PCIBUS_295

PCIBUS_296

PCIBUS_297

PCIBUS_298

PCIBUS_299

PCIBUS_300

PCIBUS_301

PCIBUS_302

PCIBUS_303

PCIBUS_304

PCIBUS_305

PCIBUS_306

PCIBUS_307

PCIBUS_308

PCIBUS_309

PCIBUS_310

PCIBUS_311

PCIBUS_312

PCIBUS_313

PCIBUS_314

PCIBUS_315

PCIBUS_316

PCIBUS_317

PCIBUS_318

PCIBUS_319

PCIBUS_320

PCIBUS_321

PCIBUS_322

PCIBUS_323

PCIBUS_324

PCIBUS_325

PCIBUS_326

PCIBUS_327

PCIBUS_328

PCIBUS_329

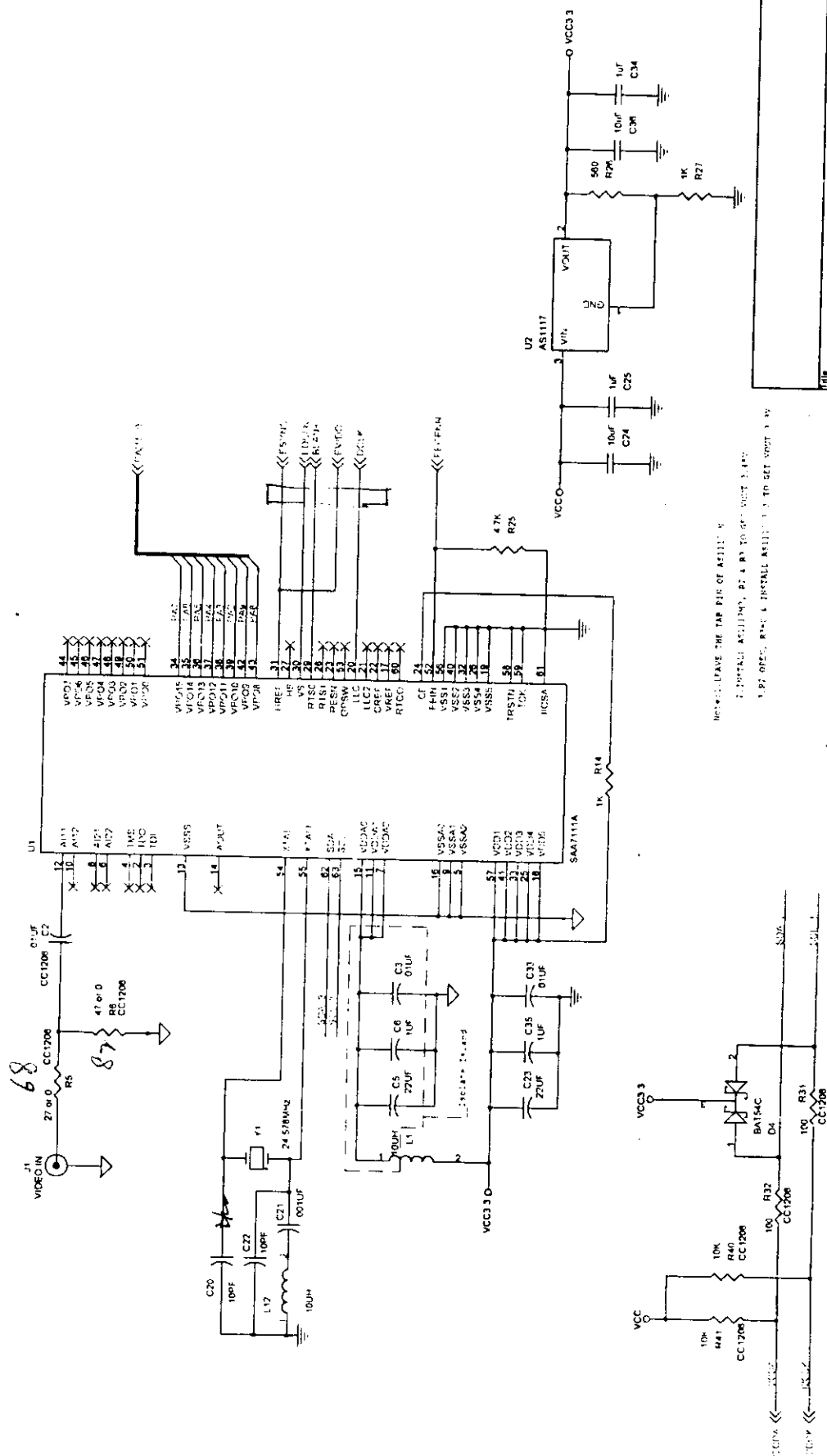
PCIBUS

Figure 1 is a block diagram of the 2716 EPROM. It shows a 16x8 array of memory cells. The vertical axis is labeled with addresses 00 to 0F. The horizontal axis is labeled with data bits D7 to D0. The array is divided into four 4x4 quadrants. The top-left quadrant is labeled '00' to '0F' on the left and 'D7' to 'D0' on the top. The top-right quadrant is labeled '00' to '0F' on the left and 'D7' to 'D0' on the top. The bottom-left quadrant is labeled '00' to '0F' on the left and 'D7' to 'D0' on the top. The bottom-right quadrant is labeled '00' to '0F' on the left and 'D7' to 'D0' on the top. The array is connected to a VCC pin and a GND pin. The array is also connected to a 2716 EPROM chip.

NOTES:
If the unit is not connected to TV,
please do not connect to TV.

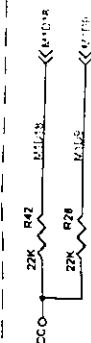
Title DTCT-9705, PCI BUS & RIOs

Use	Document Number	Rev
B	8705-2 SCH	10

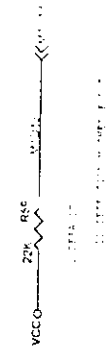
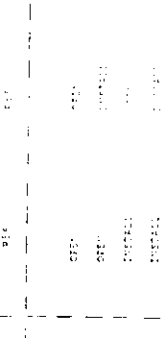
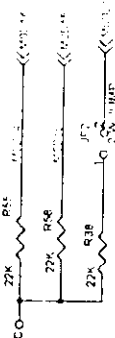
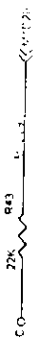


NOTE: LEAVE THE TAP PIP OF ASHLEY W
 1. INSTALL ASHLEY'S 2" & 3" TO GET YOUR 1.400
 1.97 OPEN, RACE & INSTALL ASHLEY'S 1.5 TO GET YOUR 1.30

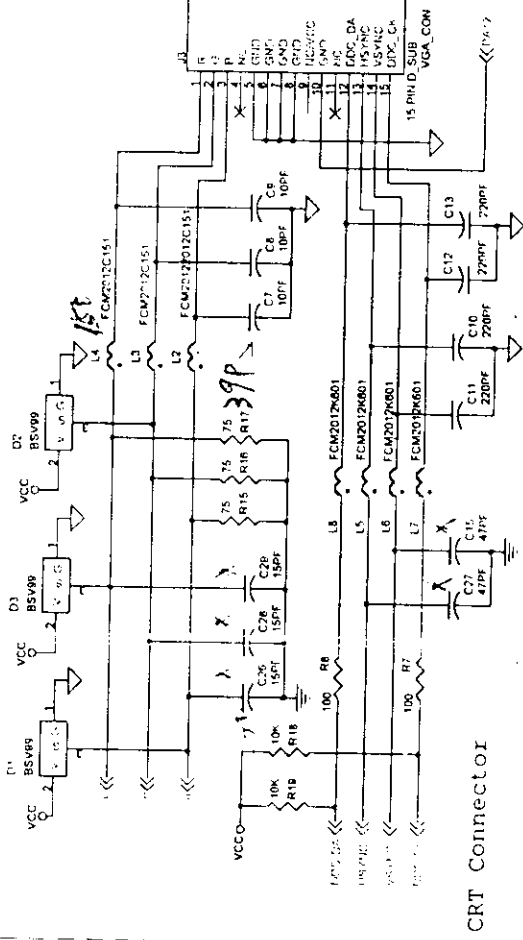
Configuration Resistors



1. 22K 1/4W 5% RESISTOR
2. 22K 1/4W 5% RESISTOR



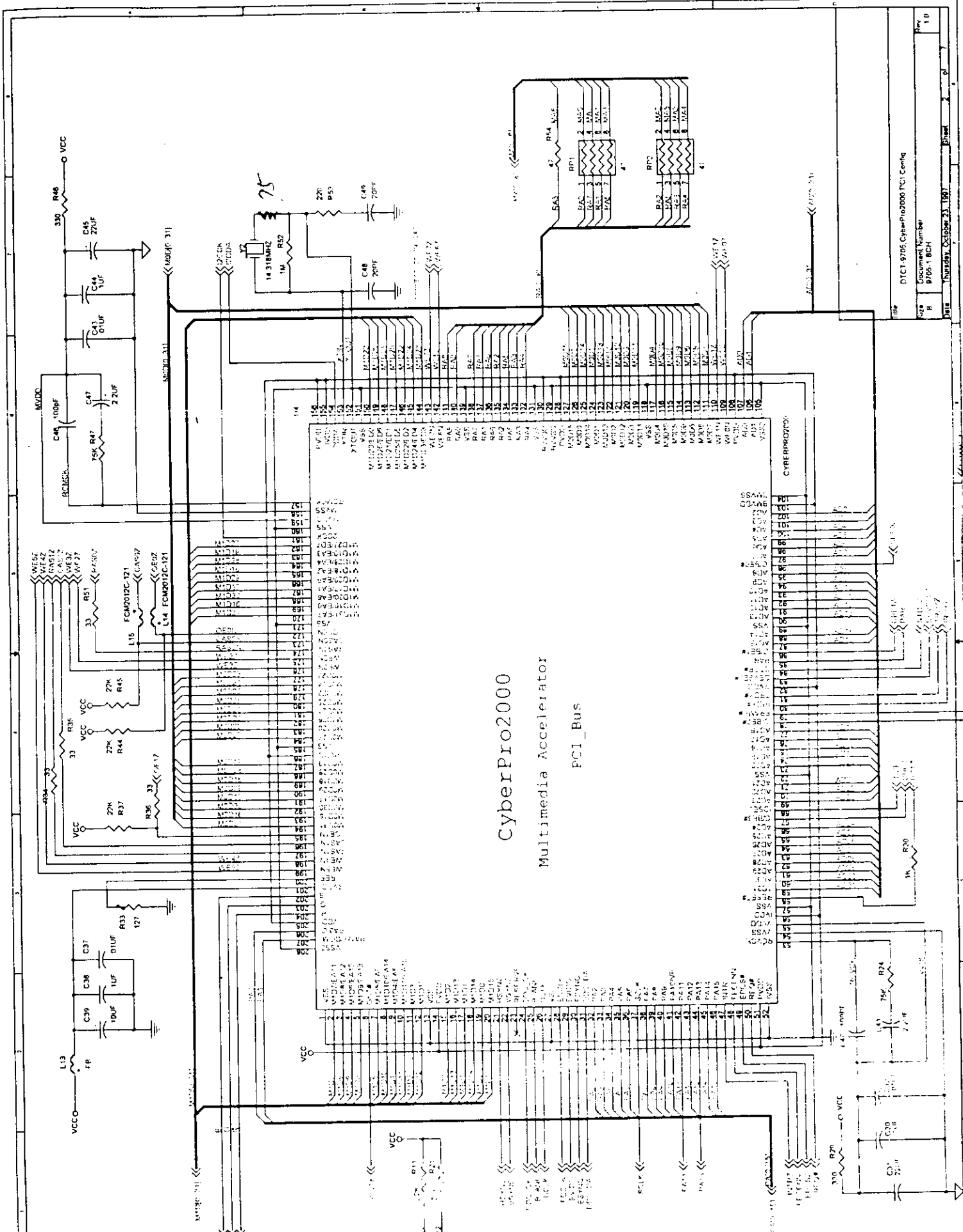
1. 22K 1/4W 5% RESISTOR



CRT Connector

Rev	1.0
Doc	8705-3 BCH
Proj	8705-3 BCH
File	Thursday, October 23, 1987
Page	4 of 7

DTCT-8705 CRT & Feature Conn



Doc# DTCT-9705 CyberPro2000 PCI Contig

Size 8
Document Number 9705-1 BCH
Date Thursday, October 23, 1997
Page 2 of 10