

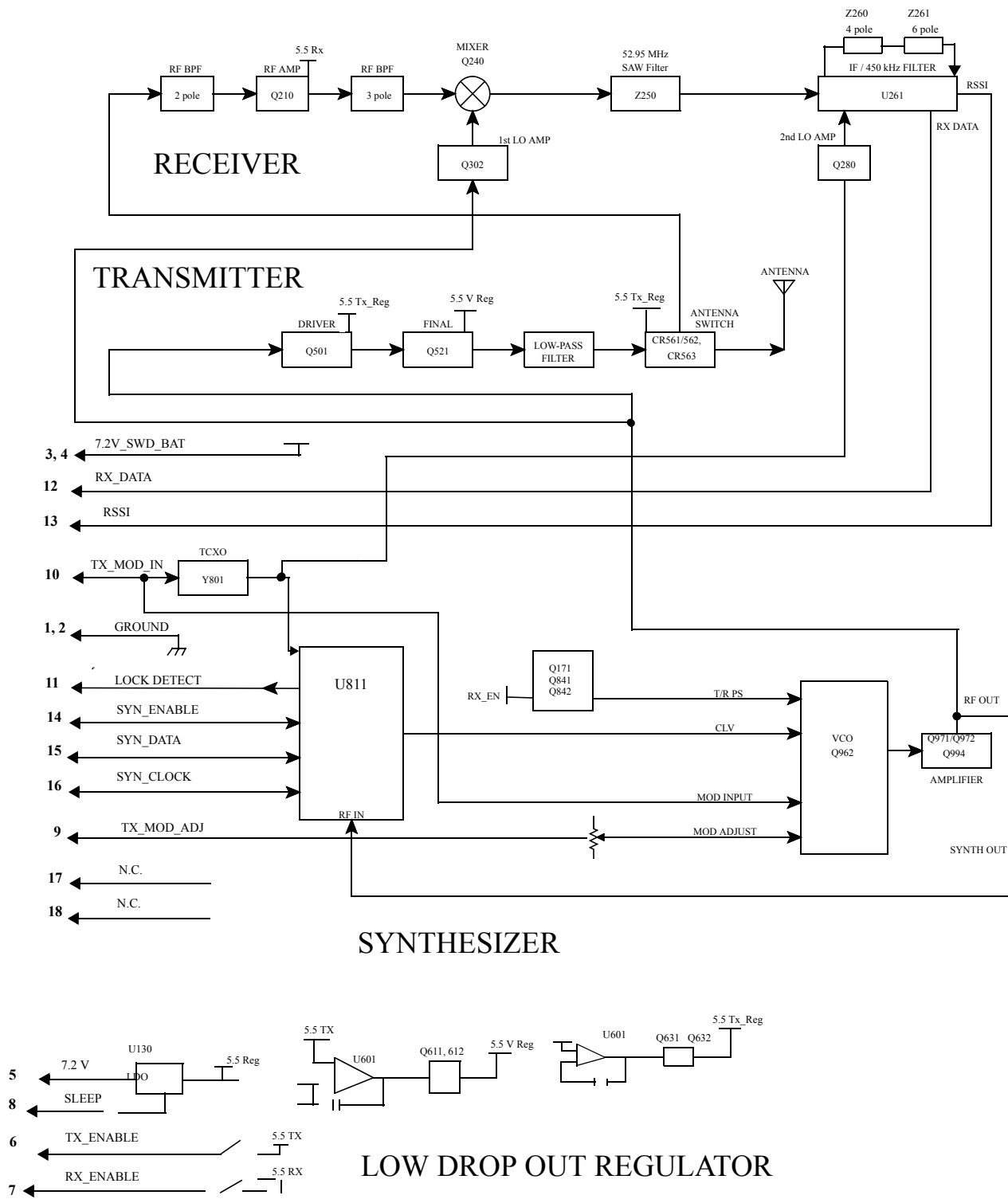


Figure 4-1 DATA TRANSCEIVER BLOCK DIAGRAM