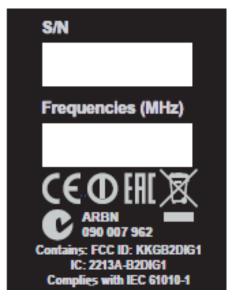
DR2 Label and Location



PCB level







Device Level