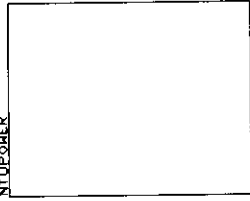


EXHIBIT D

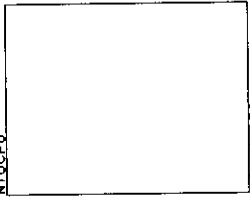
Circuit Diagram

NTUPOMER



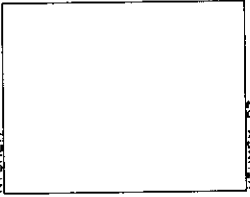
NTUPOMER.P1

NTUCPU



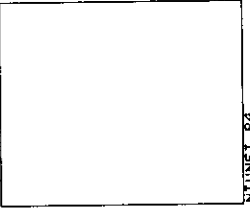
NTUCPU.P2

NTUMEM



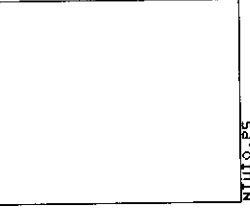
NTUMEM.P3

NTUNET



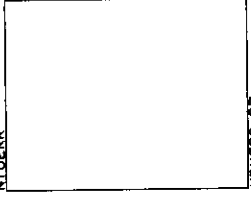
NTUNET.P4

NTUIO



NTUIO.P5

NTUERR



NTUERR.P7

NTURACK

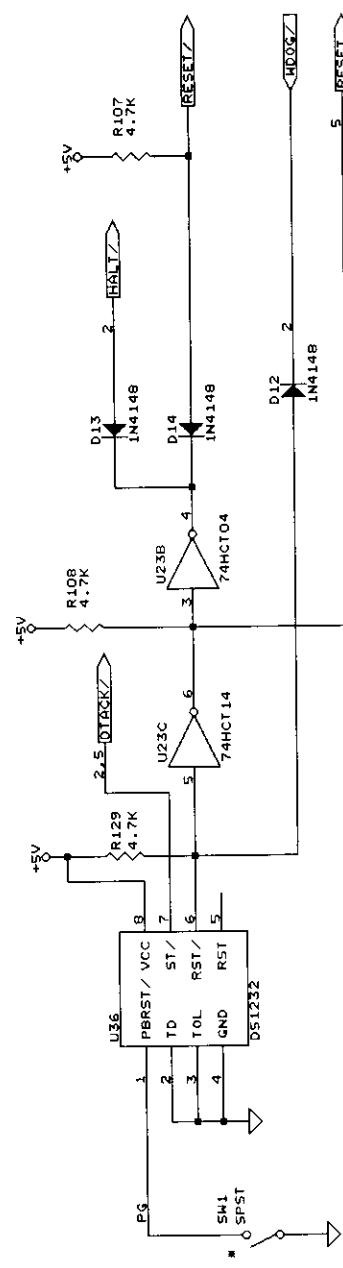
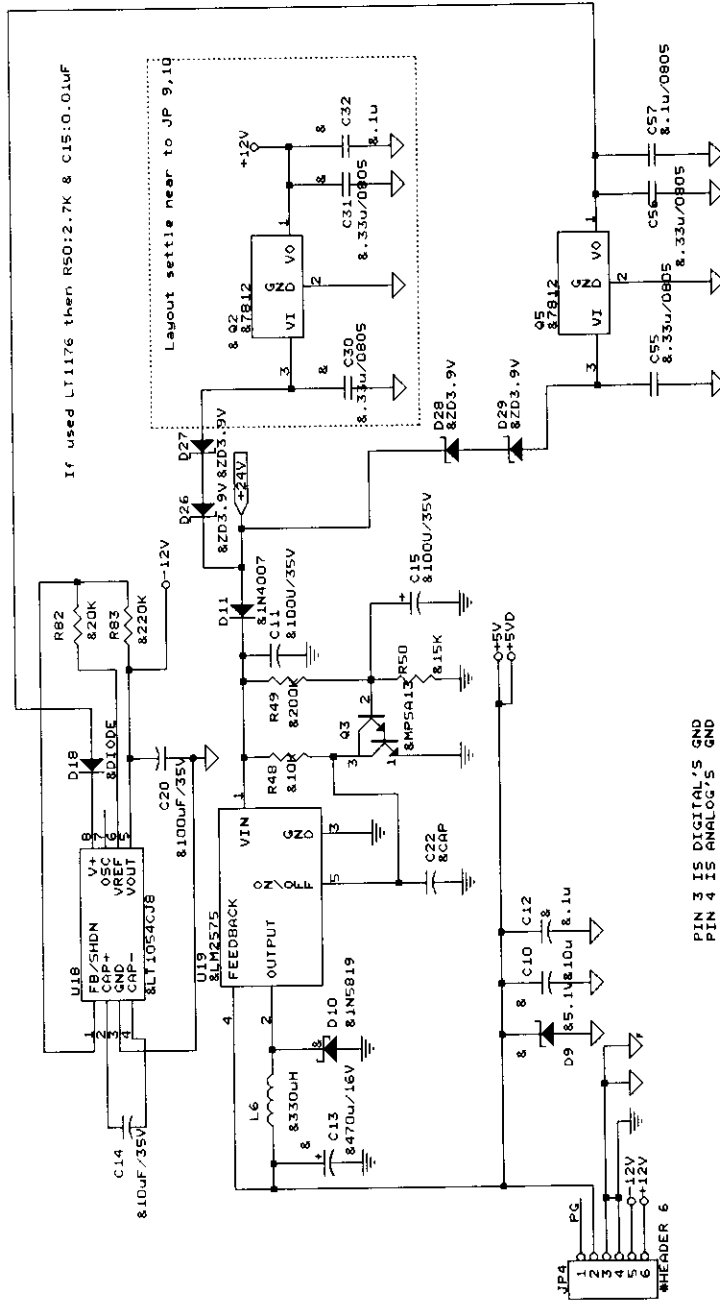


NTURACK.P8

NTUCLK



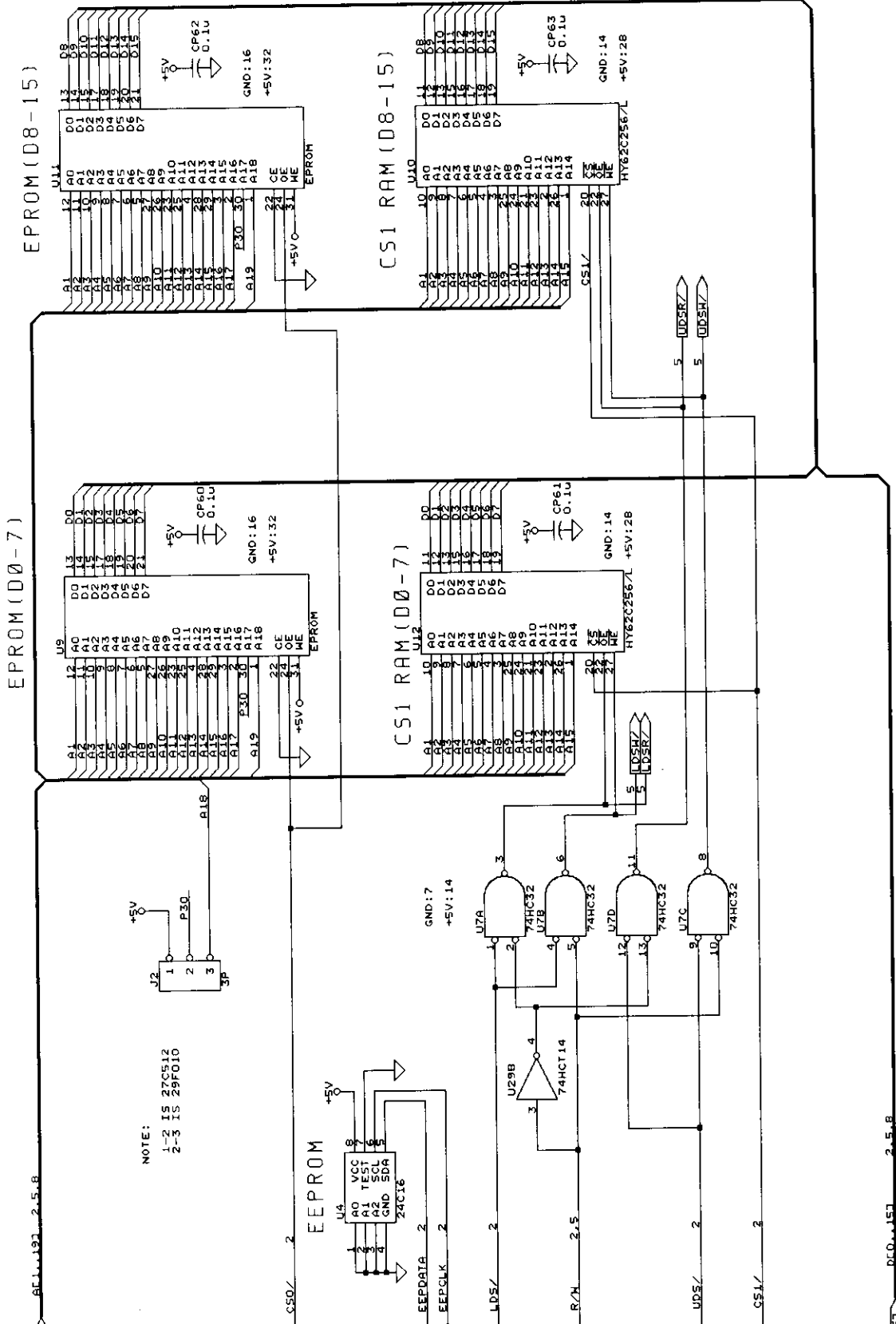
NTUCLK.P9



NTUPOWER.SCH

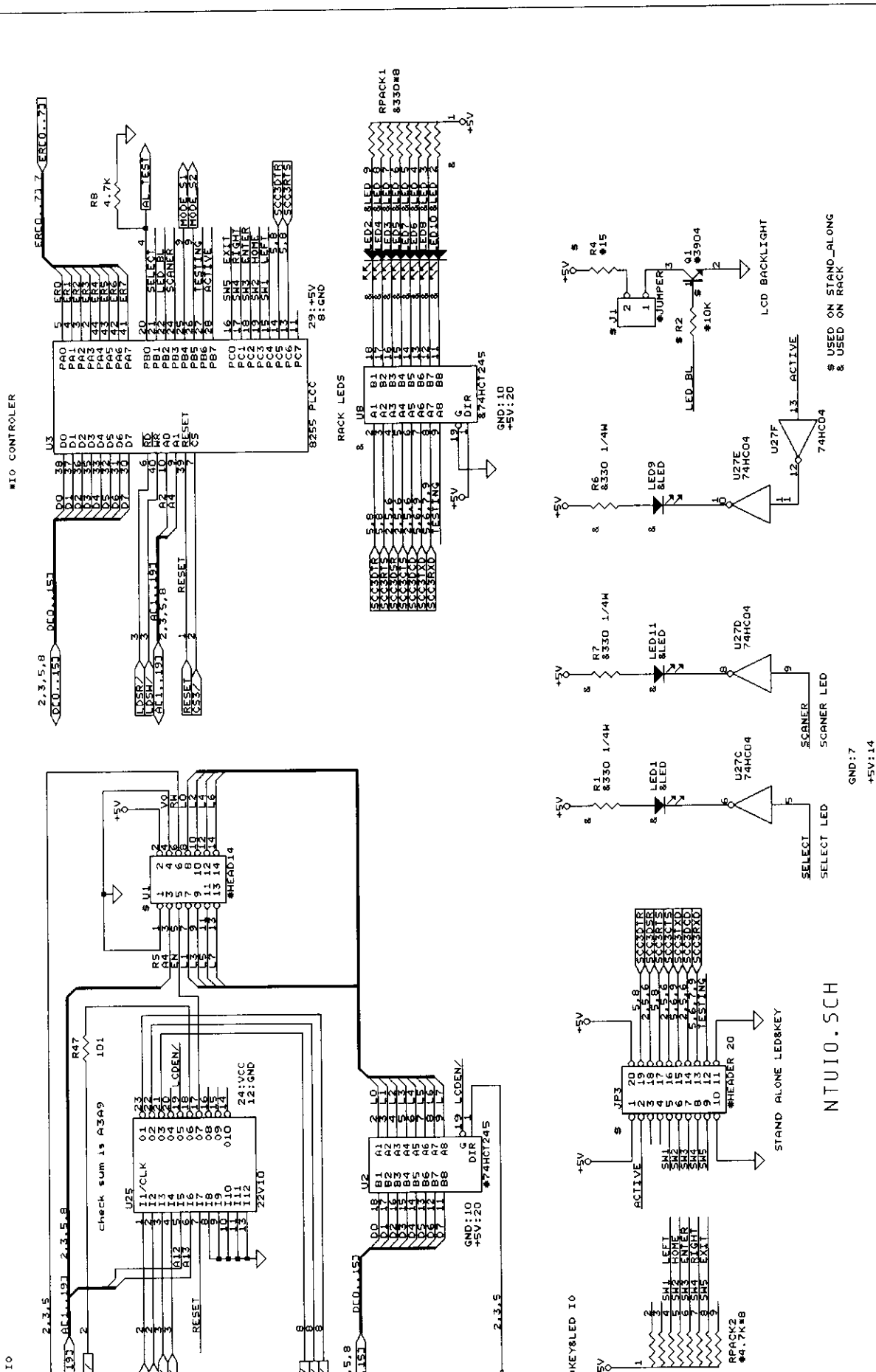
SW1 : POWER RESET
* USED ON TESTING
& USED RACK

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NOTE:
1-2 IS 27C512
2-3 IS 29F010

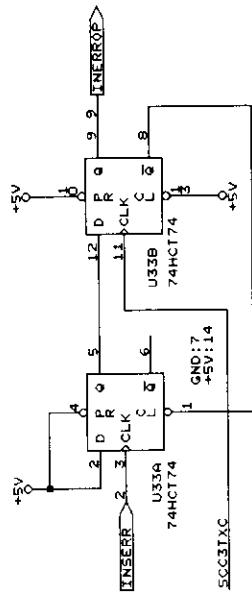
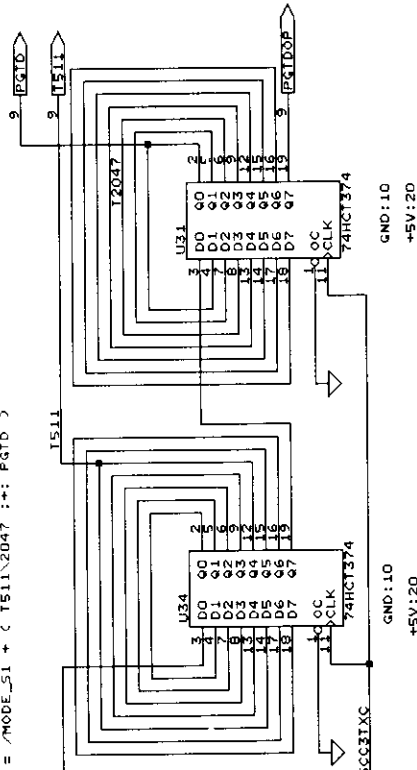
Title	5058-0016
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Date:	April 17, 1998
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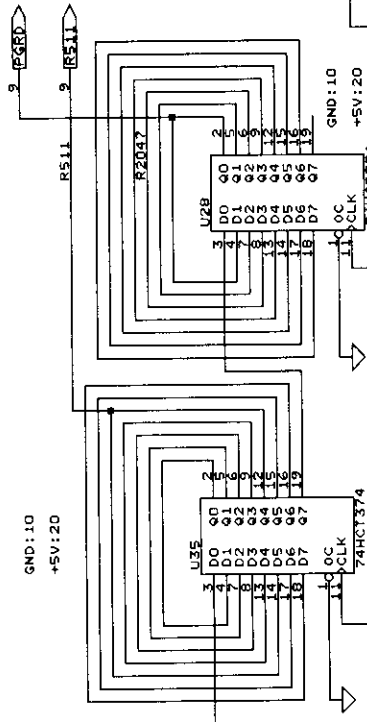
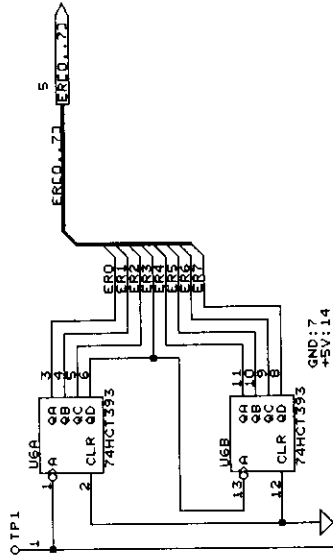
NTU10.SCH

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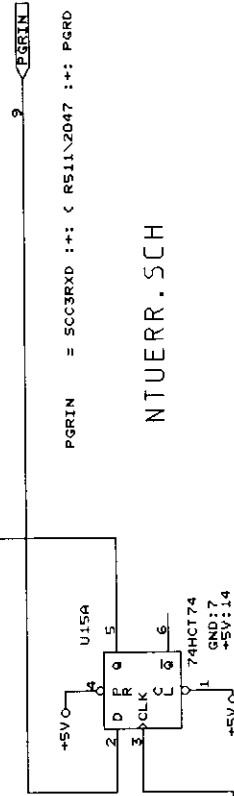
= /MODE_S1 + (T511\2047 :+ PGTD)



RANDOMOP = PGTDOP :+ INERROP

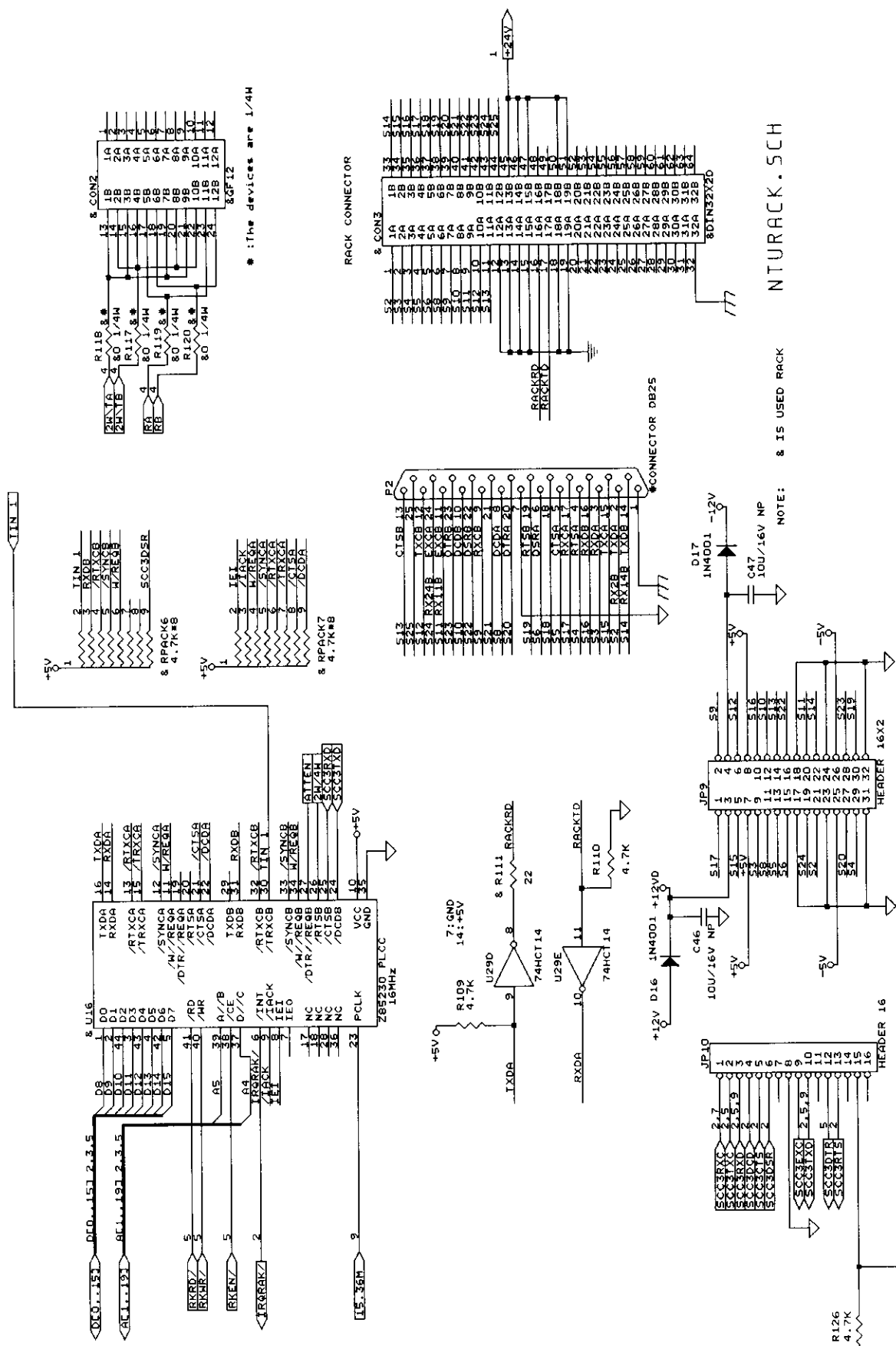


PGIN = SCC3RXD :+ (R511\2047 :+ PGRO)

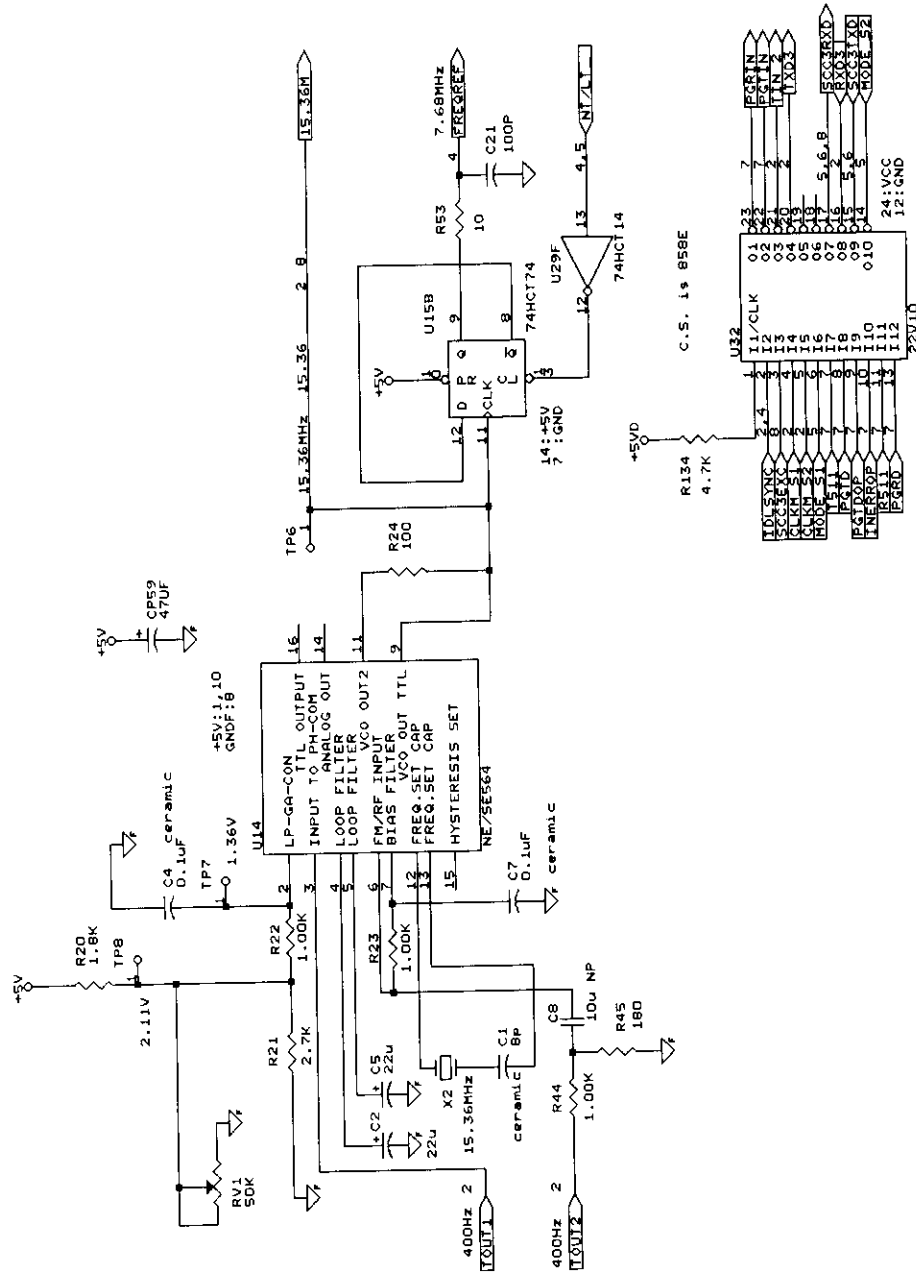


NTUERR.SCH

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NOTE:

CLKM_S2	CLKM_S1	CLK	MODE
0	1	INTERNAL	INTERNAL
1	0	LOOPBACK	LOOPBACK
1	1	EXTERNAL	EXTERNAL

PS: * is "DON'T CARE"

NTUCLK.SCH