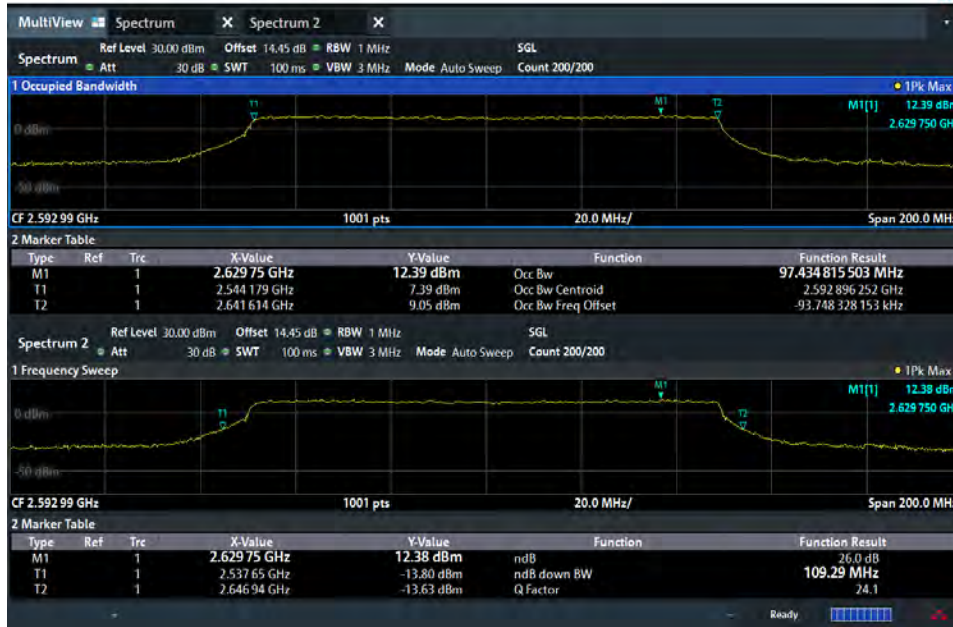


Appendix B. Test Results_Band n41

26dB and 99% Bandwidth

OB_n41_CP_10M_DL518598_Outer Full RB_16QAM



OB_n41_CP_10M_DL518598_Outer Full RB_64QAM



OB_n41_CP_10M_DL518598_Outer Full RB_256QAM



OB_n41_CP_10M_DL518598_Outer Full RB_QPSK



OB_n41_CP_15M_DL518598_Outer Full RB_16QAM



OB_n41_CP_15M_DL518598_Outer Full RB_64QAM



OB_n41_CP_15M_DL518598_Outer Full RB_256QAM



OB_n41_CP_15M_DL518598_Outer Full RB_QPSK



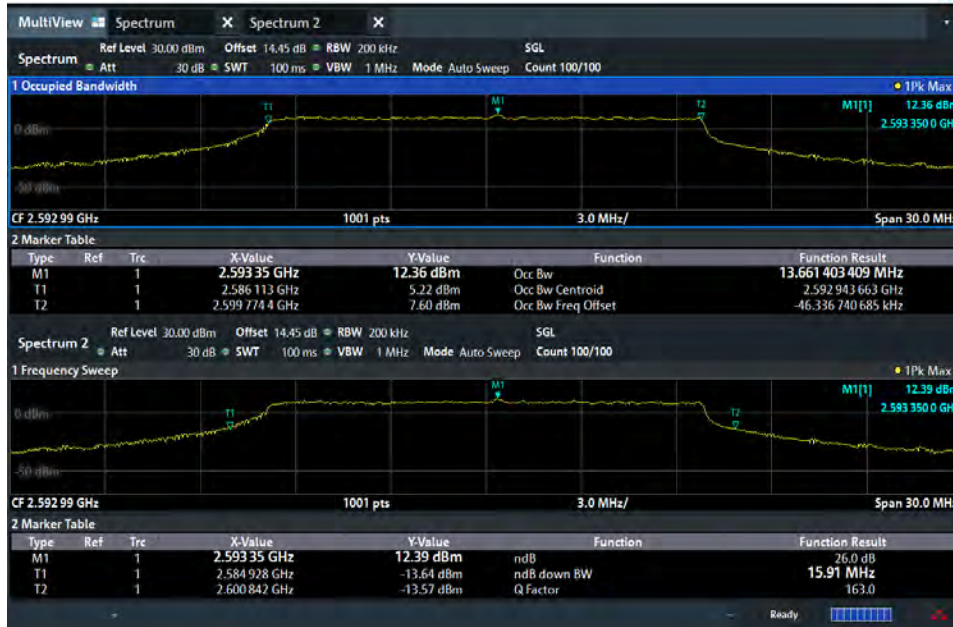
OB_n41_CP_20M_DL518598_Outer Full RB_16QAM



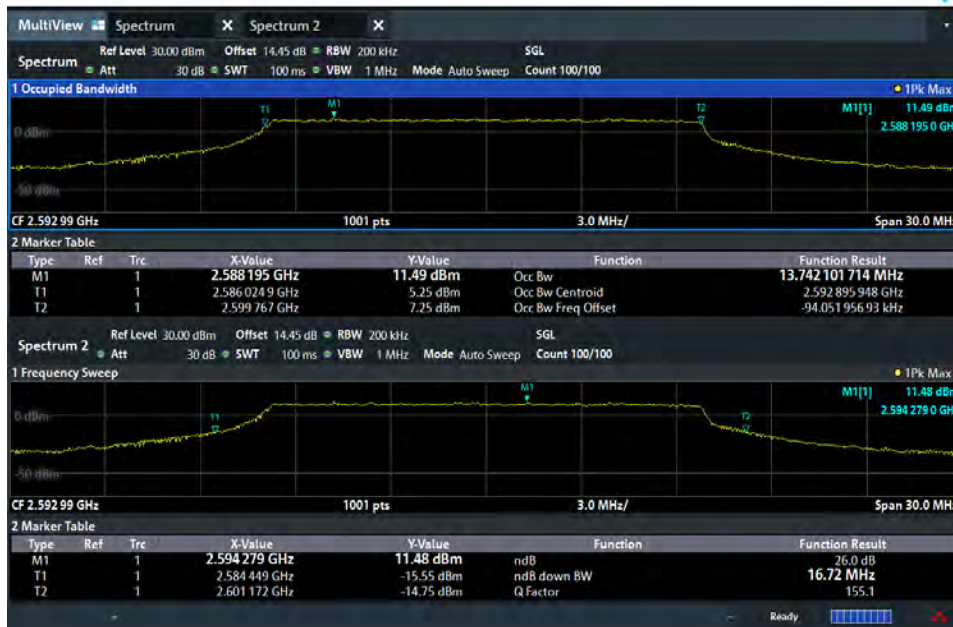
OB_n41_CP_20M_DL518598_Outer Full RB_64QAM



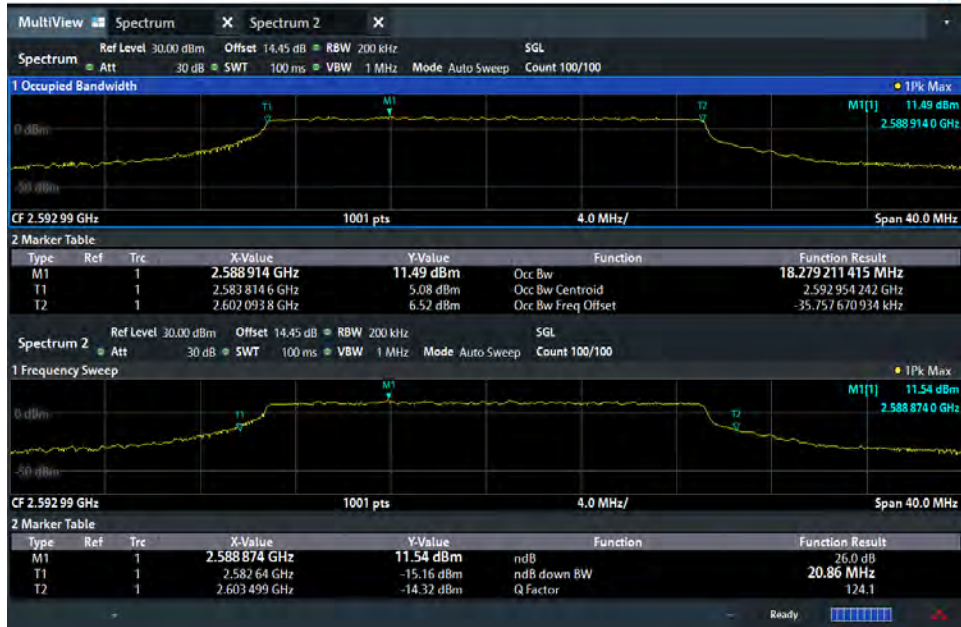
OB_n41_CP_20M_DL518598_Outer Full RB_256QAM



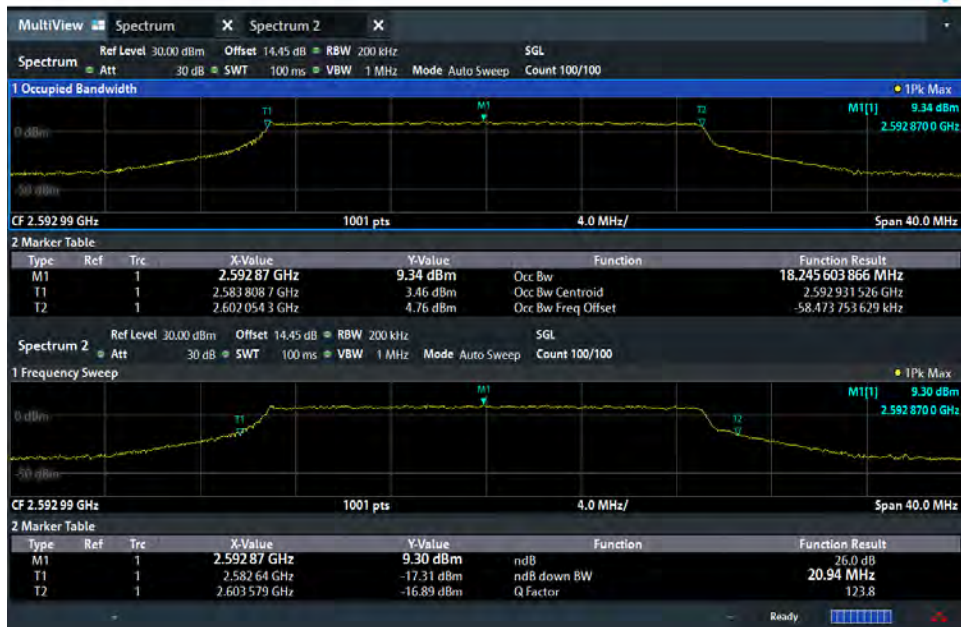
OB_n41_CP_20M_DL518598_Outer Full RB_QPSK



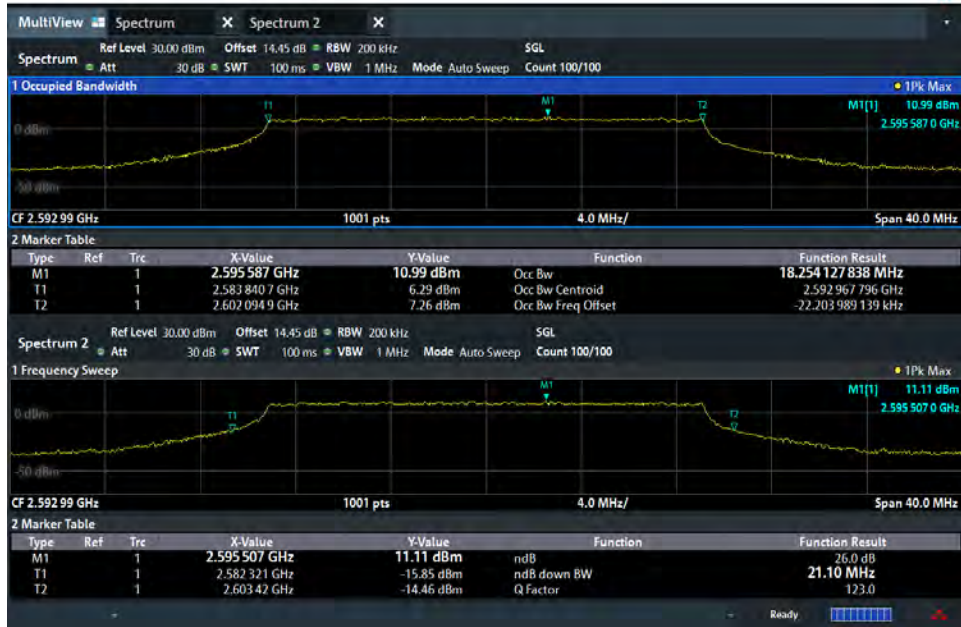
OB_n41_CP_40M_DL518598_Outer Full RB_16QAM



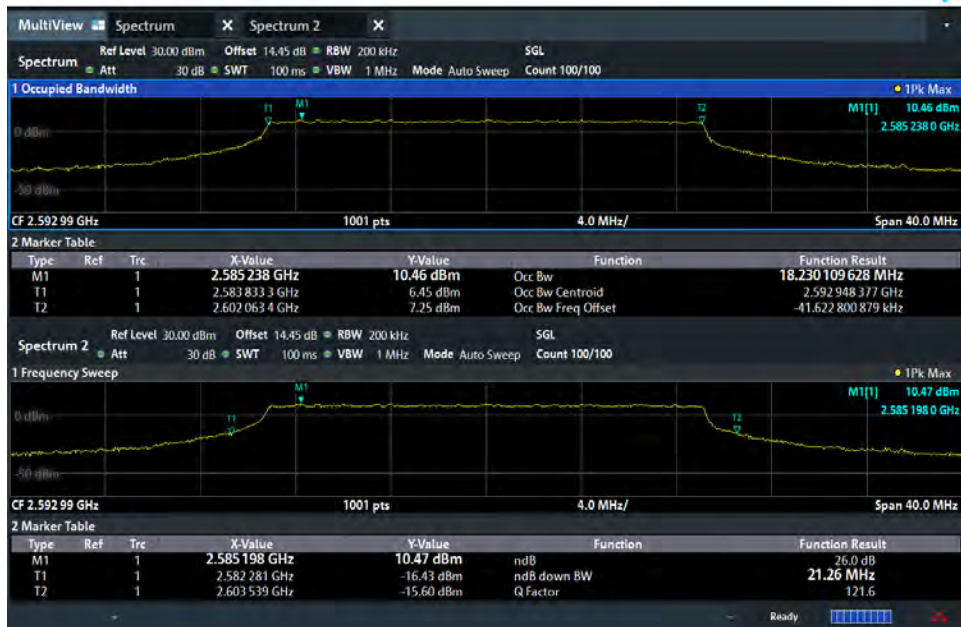
OB_n41_CP_40M_DL518598_Outer Full RB_64QAM



OB_n41_CP_40M_DL518598_Outer Full RB_256QAM



OB_n41_CP_40M_DL518598_Outer Full RB_QPSK



OB_n41_CP_50M_DL518598_Outer Full RB_16QAM



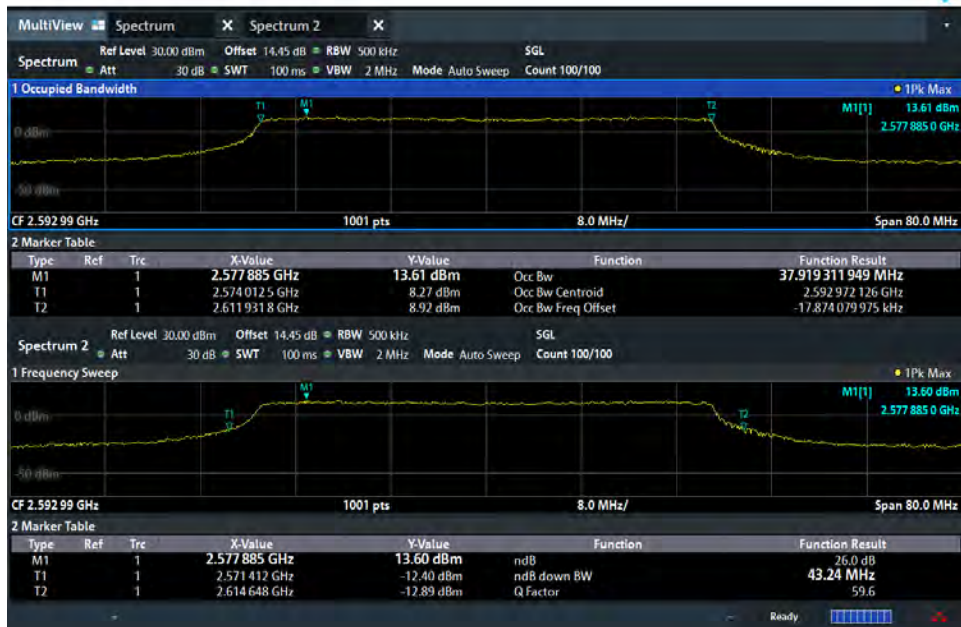
OB_n41_CP_50M_DL518598_Outer Full RB_64QAM



OB_n41_CP_50M_DL518598_Outer Full RB_256QAM



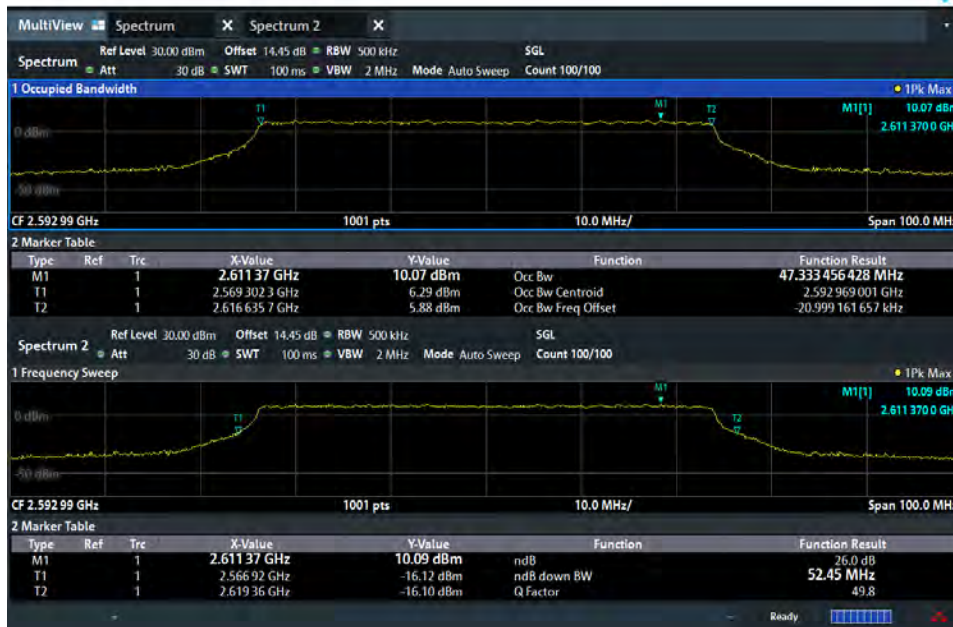
OB_n41_CP_50M_DL518598_Outer Full RB_QPSK



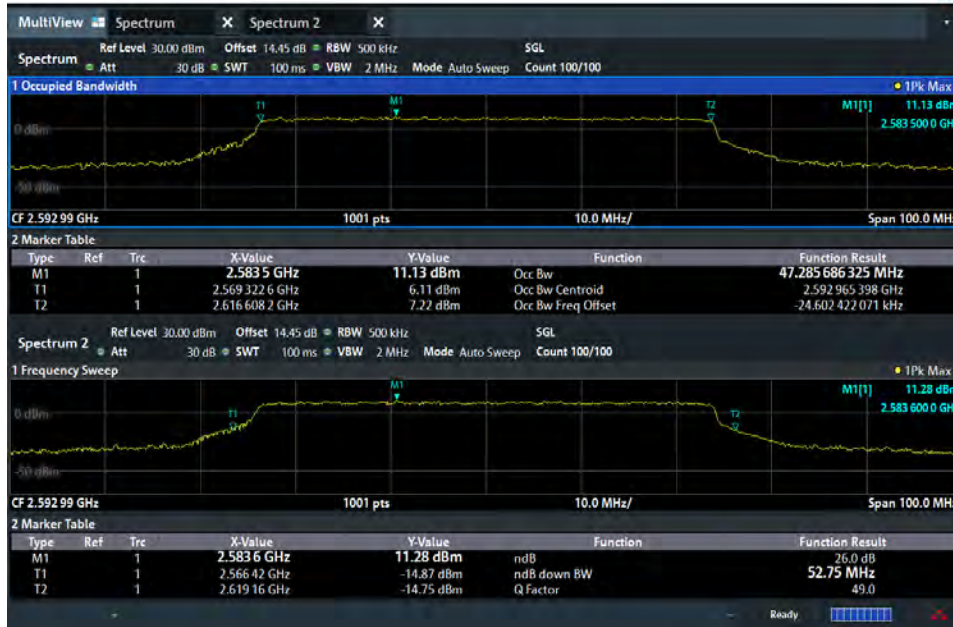
OB_n41_CP_60M_DL518598_Outer Full RB_16QAM



OB_n41_CP_60M_DL518598_Outer Full RB_64QAM



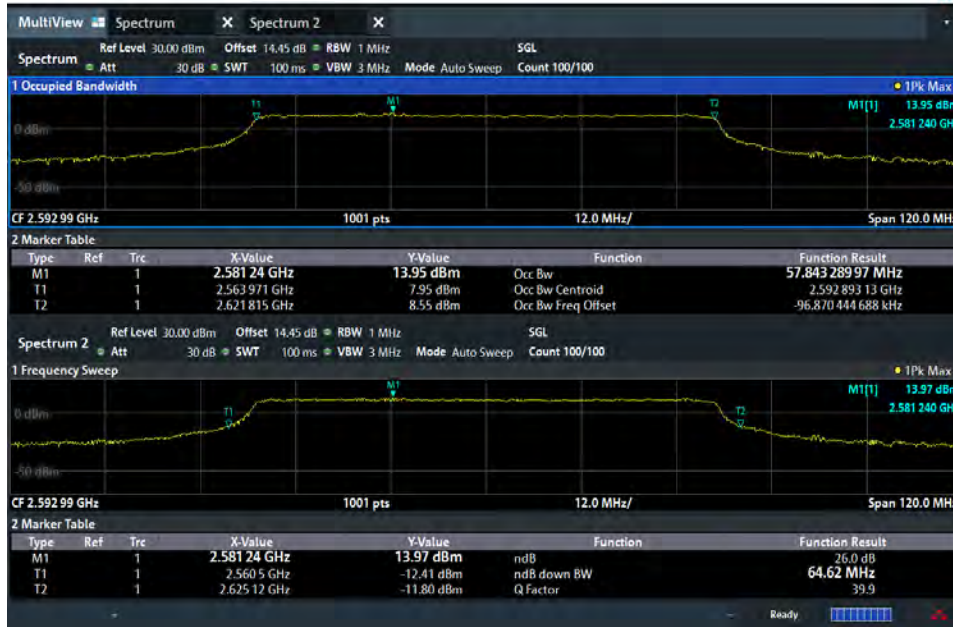
OB_n41_CP_60M_DL518598_Outer Full RB_256QAM



OB_n41_CP_60M_DL518598_Outer Full RB_QPSK



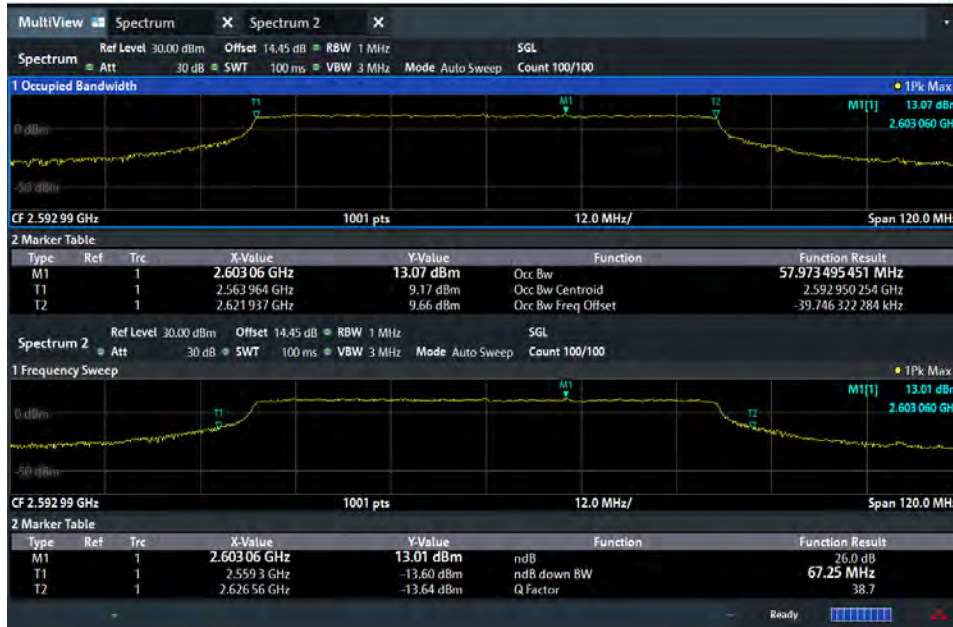
OB_n41_CP_80M_DL518598_Outer Full RB_16QAM



OB_n41_CP_80M_DL518598_Outer Full RB_64QAM



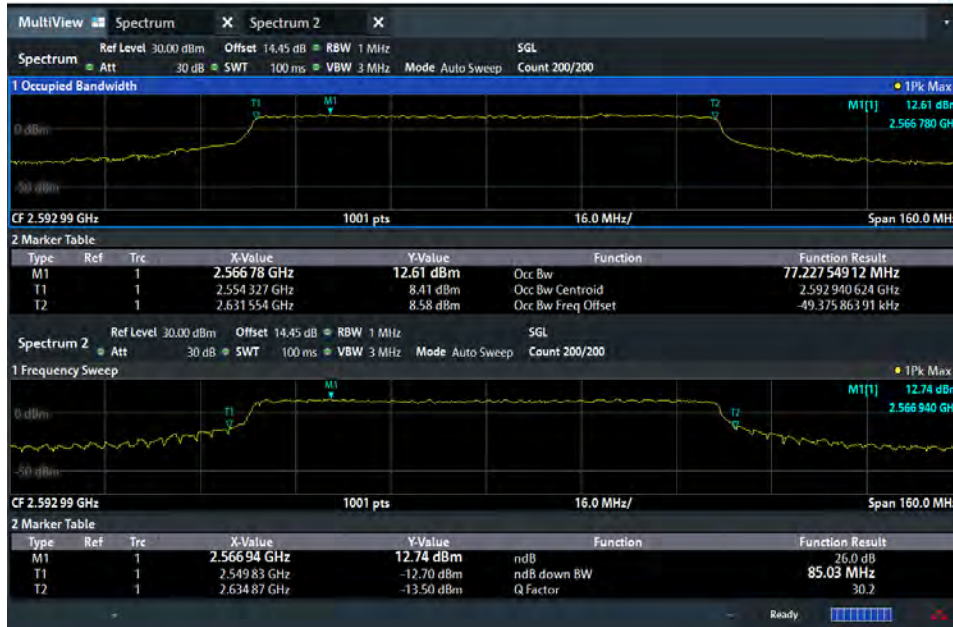
OB_n41_CP_80M_DL518598_Outer Full RB_256QAM



OB_n41_CP_80M_DL518598_Outer Full RB_QPSK



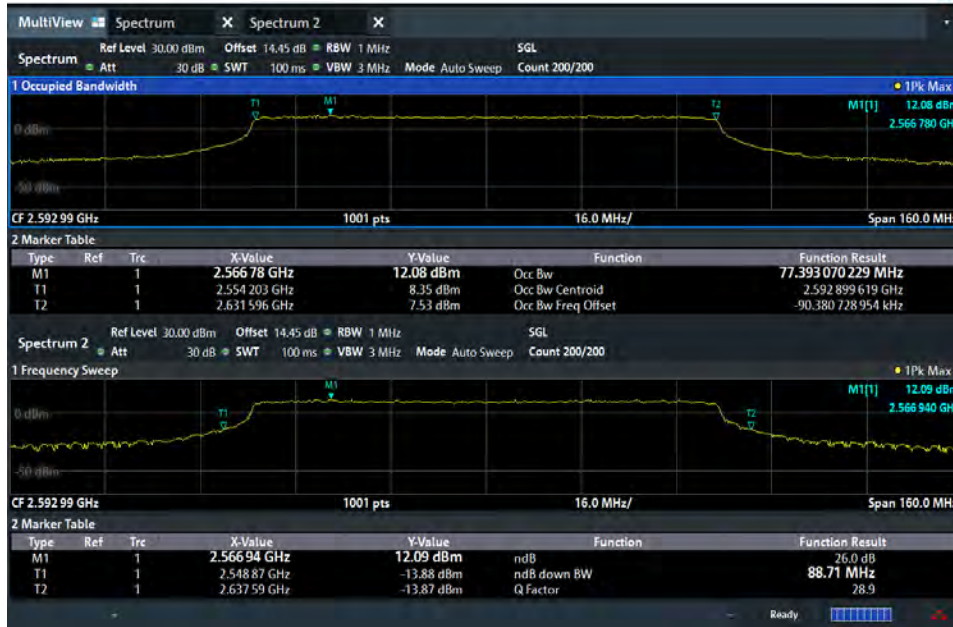
OB_n41_CP_90M_DL518598_Outer Full RB_16QAM



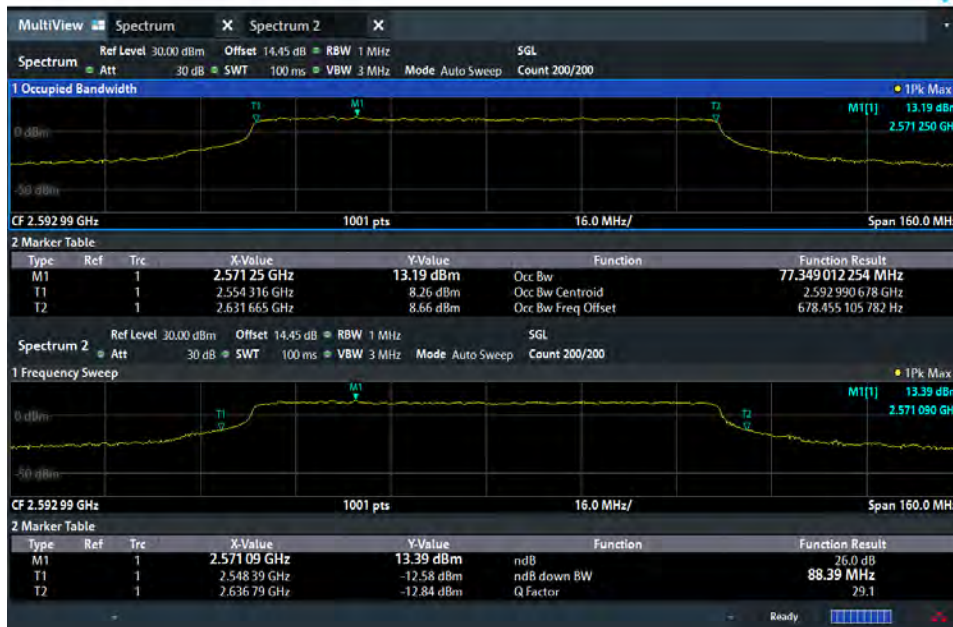
OB_n41_CP_90M_DL518598_Outer Full RB_64QAM



OB_n41_CP_90M_DL518598_Outer Full RB_256QAM



OB_n41_CP_90M_DL518598_Outer Full RB_QPSK



OB_n41_CP_100M_DL518598_Outer Full RB_16QAM



OB_n41_CP_100M_DL518598_Outer Full RB_64QAM



OB_n41_CP_100M_DL518598_Outer Full RB_256QAM



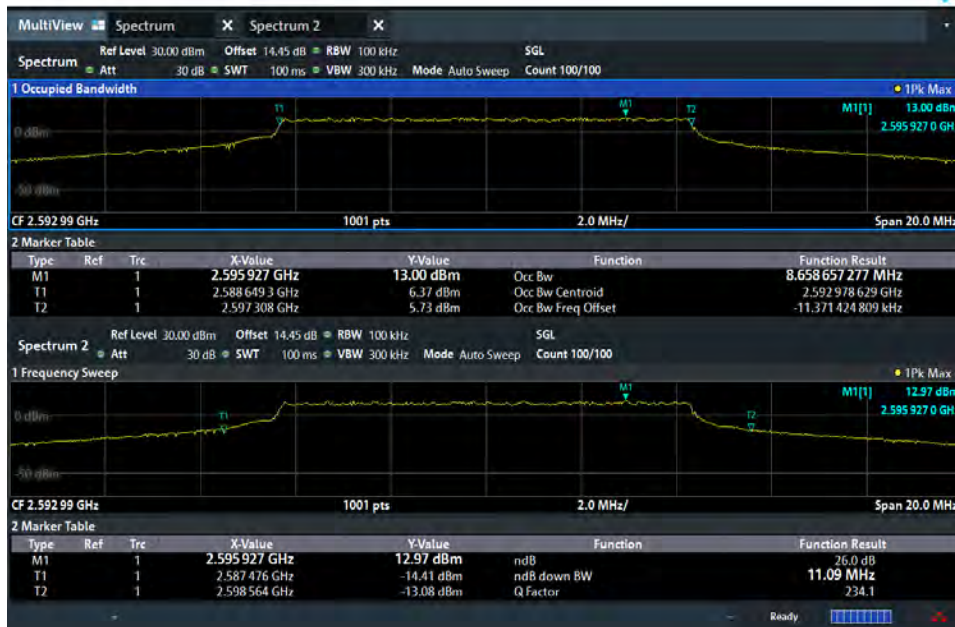
OB_n41_CP_100M_DL518598_Outer Full RB_QPSK



OB_n41_DFT_10M_DL518598_Outer Full RB_BPSK



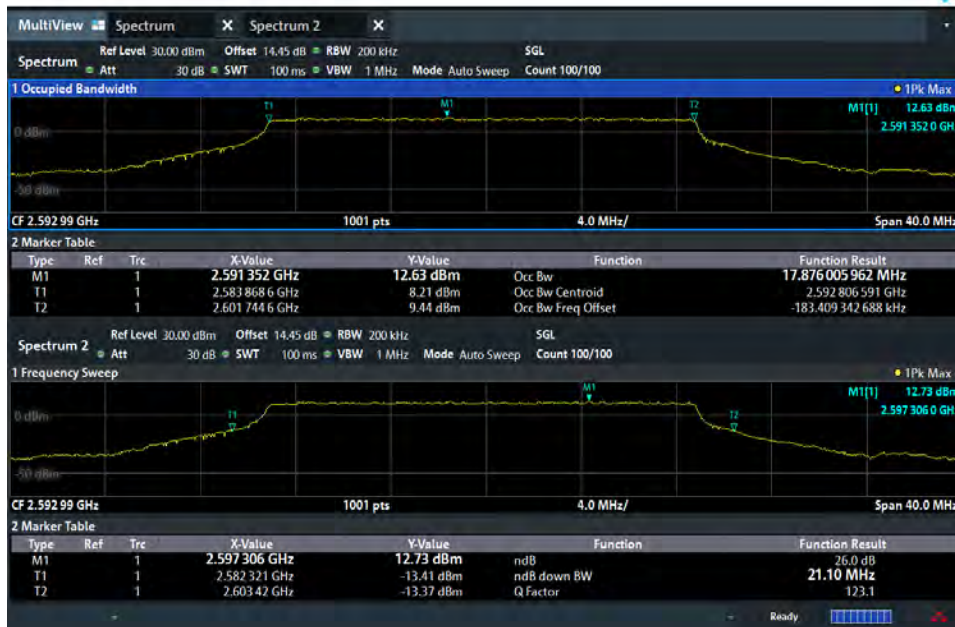
OB_n41_DFT_15M_DL518598_Outer Full RB_BPSK



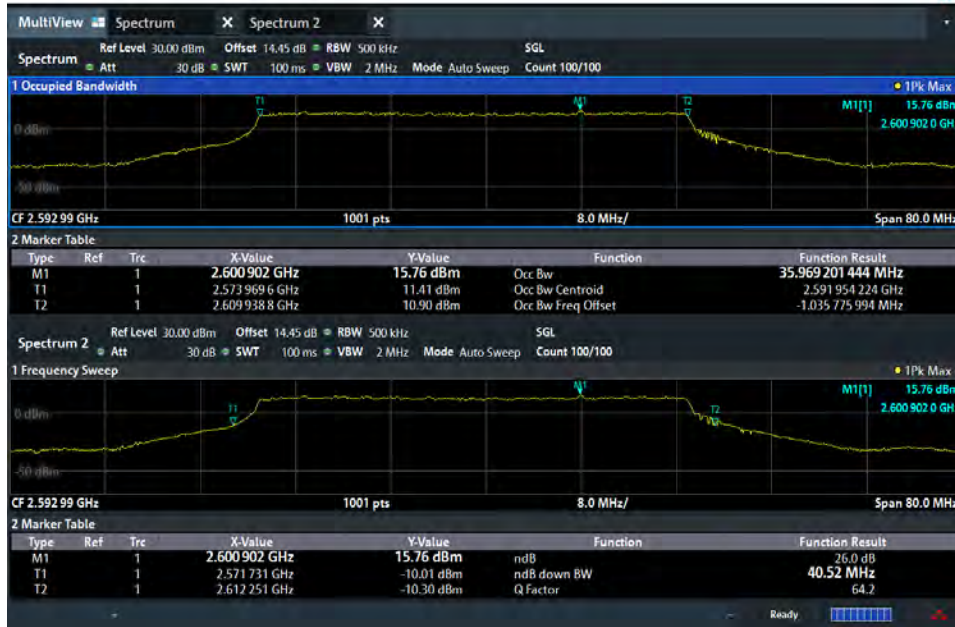
OB_n41_DFT_20M_DL518598_Outer Full RB_BPSK



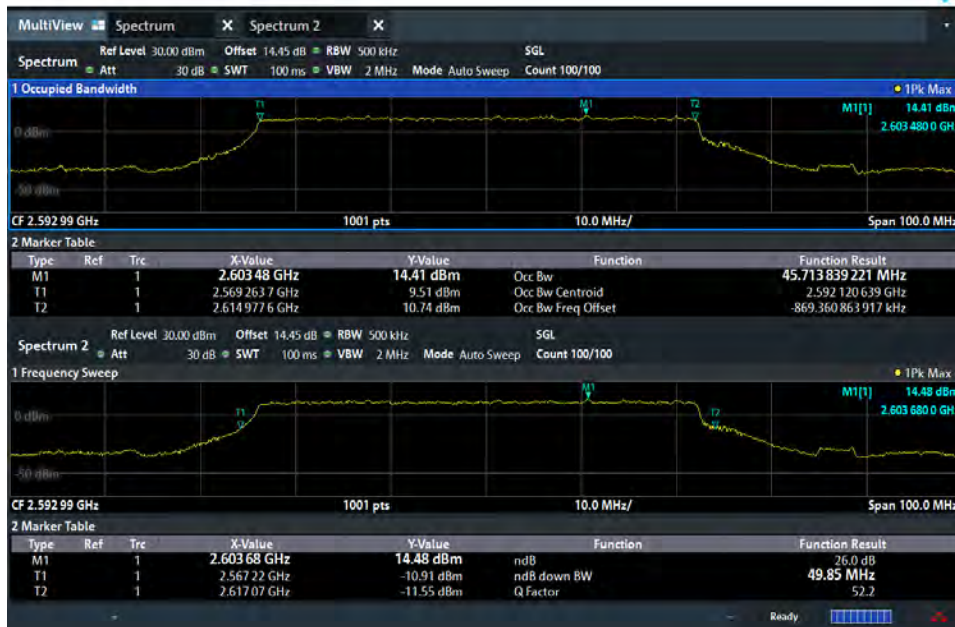
OB_n41_DFT_40M_DL518598_Outer Full RB_BPSK



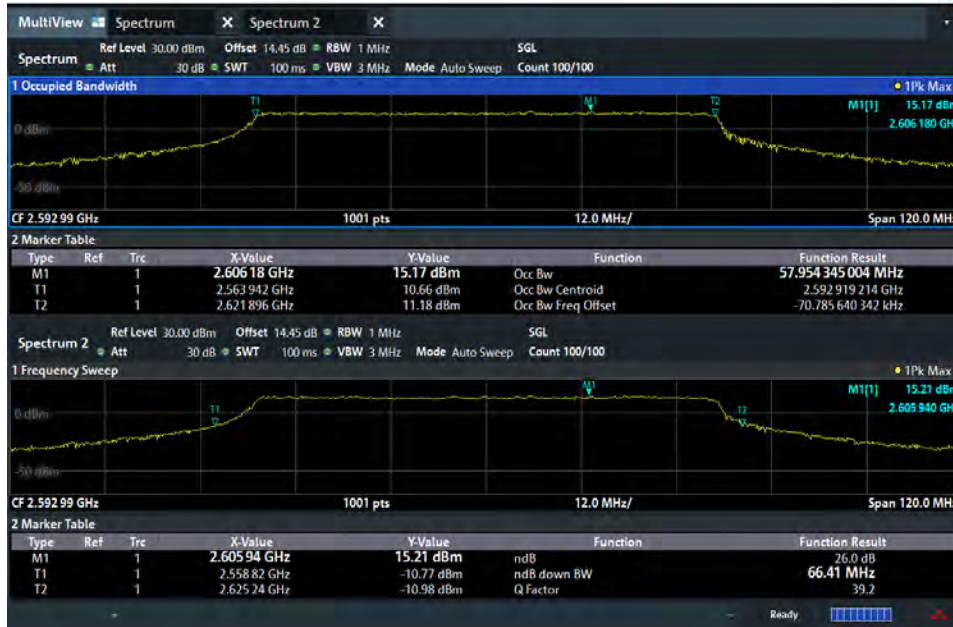
OB_n41_DFT_50M_DL518598_Outer Full RB_BPSK



OB_n41_DFT_60M_DL518598_Outer Full RB_BPSK



OB_n41_DFT_80M_DL518598_Outer Full RB_BPSK



OB_n41_DFT_90M_DL518598_Outer Full RB_BPSK

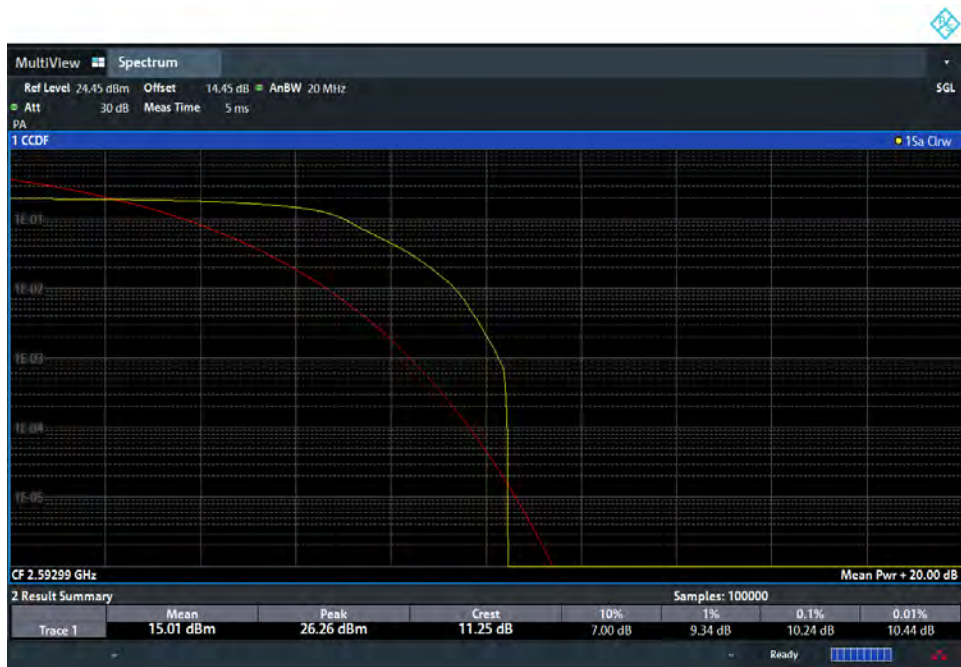


OB_n41_DFT_100M_DL518598_Outer Full RB_BPSK



Peak to Average Power Ratio

PAR_n41_20M_DL518598_Outer Full RB_BPSK



PAR_n41_20M_DL518598_Outer Full RB_QPSK



Band Edge

BE_n41_10M_High CH_DL537000_2685MHz_CP_QPSK_1RB Right



BE_n41_10M_High CH_DL537000_2685MHz_CP_QPSK_Full RB



BE_n41_10M_High CH_DL537000_2685MHz_DFT_BPSK_1RB Right



BE_n41_10M_High CH_DL537000_2685MHz_DFT_BPSK_Full RB



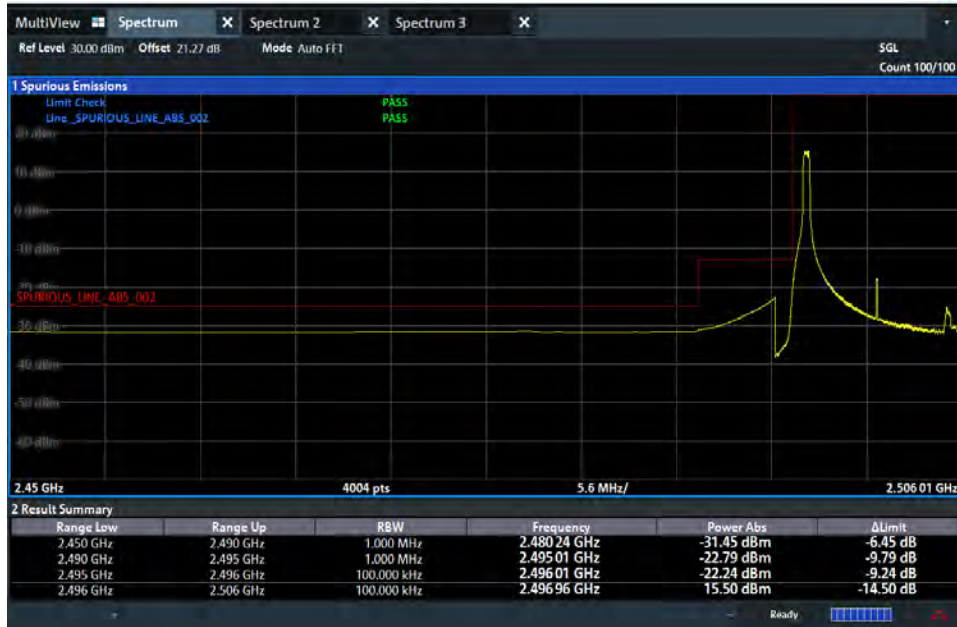
BE_n41_10M_High CH_DL537000_2685MHz_DFT_QPSK_1RB Right



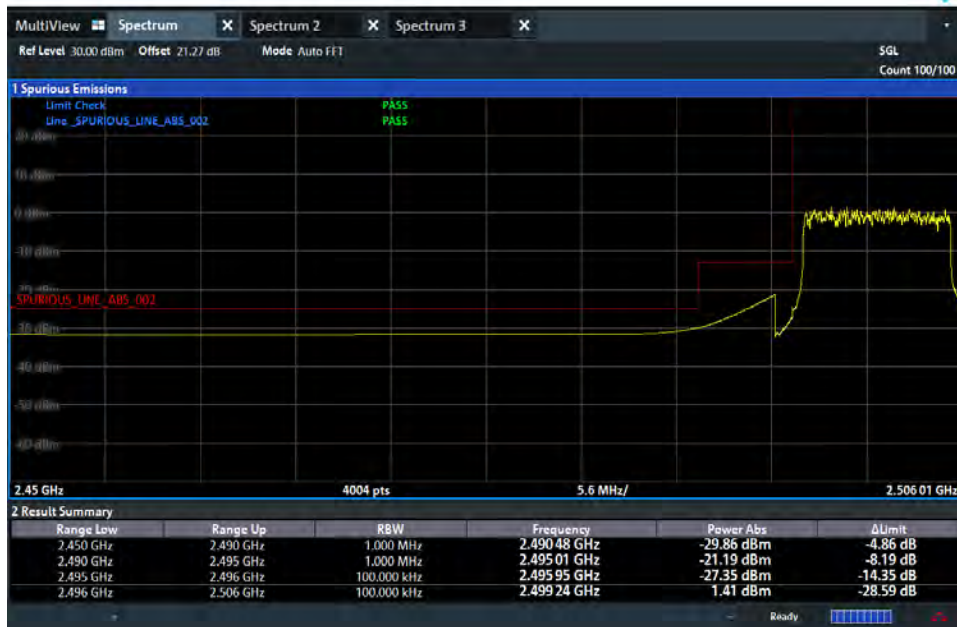
BE_n41_10M_High CH_DL537000_2685MHz_DFT_QPSK_Full RB



BE_n41_10M_Low_CH_DL500202_2501.01MHz_CP_QPSK_1RB Left



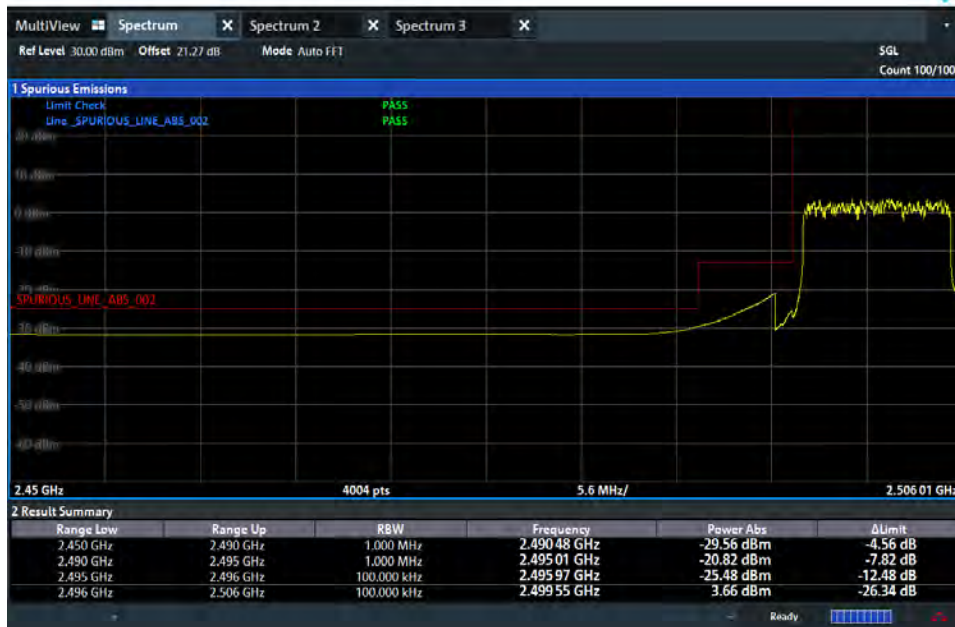
BE_n41_10M_Low_CH_DL500202_2501.01MHz_CP_QPSK_Full RB



BE_n41_10M_Low_CH_DL500202_2501.01MHz_DFT_BPSK_1RB Left



BE_n41_10M_Low_CH_DL500202_2501.01MHz_DFT_BPSK_Full RB



BE_n41_10M_Low CH_DL500202_2501.01MHz_DFT_QPSK_1RB Left



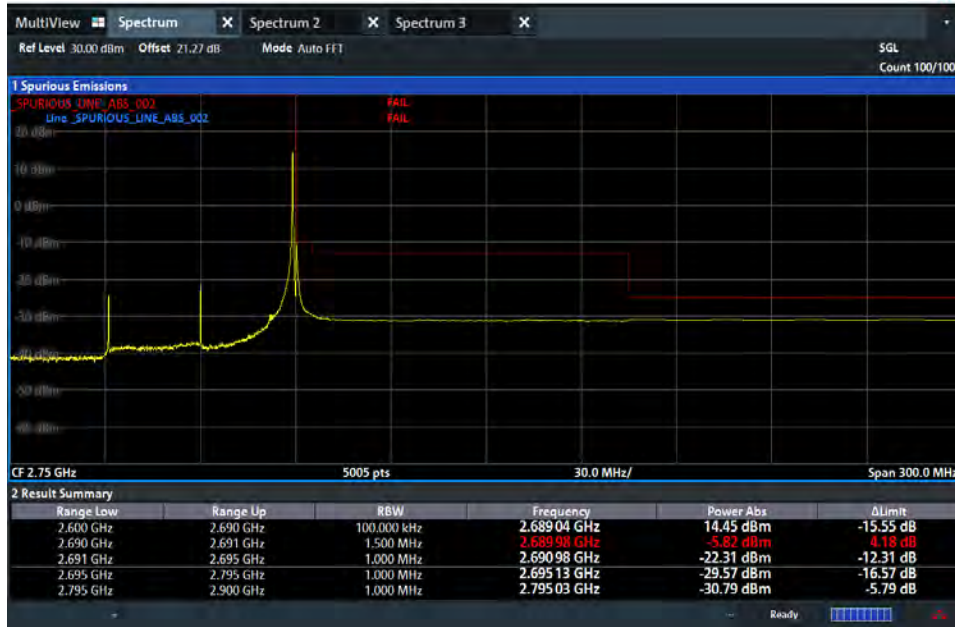
BE_n41_10M_Low CH_DL500202_2501.01MHz_DFT_QPSK_1RB Left_Duty cycle



BE_n41_10M_Low CH_DL500202_2501.01MHz_DFT_QPSK_Full RB



BE_n41_60M_High CH_DL531996_2659.98MHz_CP_QPSK_1RB Right



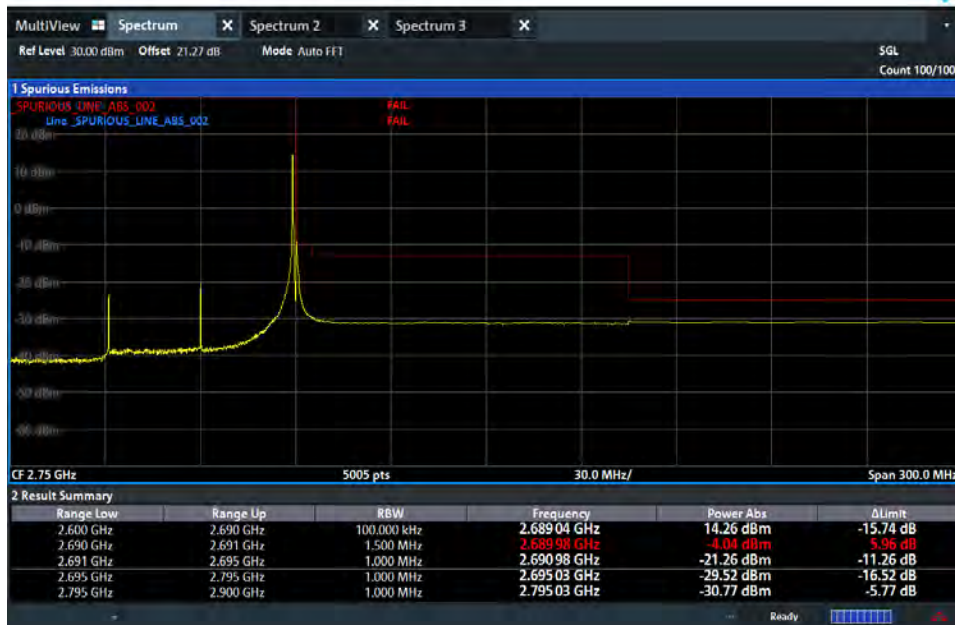
BE_n41_60M_High CH_DL531996_2659.98MHz_CP_QPSK_1RB Right_verify



BE_n41_60M_High CH_DL531996_2659.98MHz_CP_QPSK_Full RB



BE_n41_60M_High CH_DL531996_2659.98MHz_DFT_BPSK_1RB Right



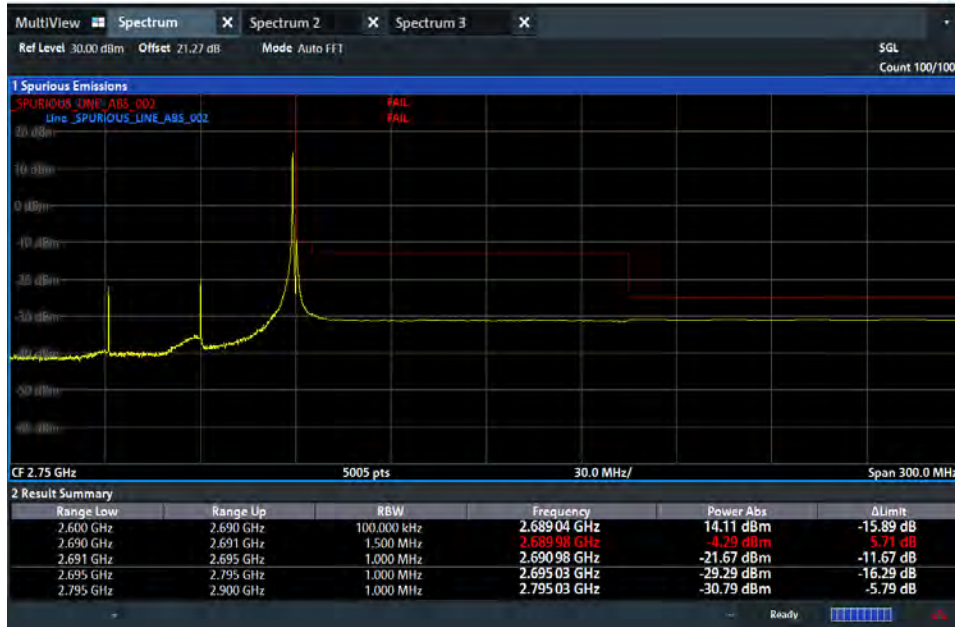
BE_n41_60M_High CH_DL531996_2659.98MHz_DFT_BPSK_1RB Right_verify



BE_n41_60M_High CH_DL531996_2659.98MHz_DFT_BPSK_Full RB



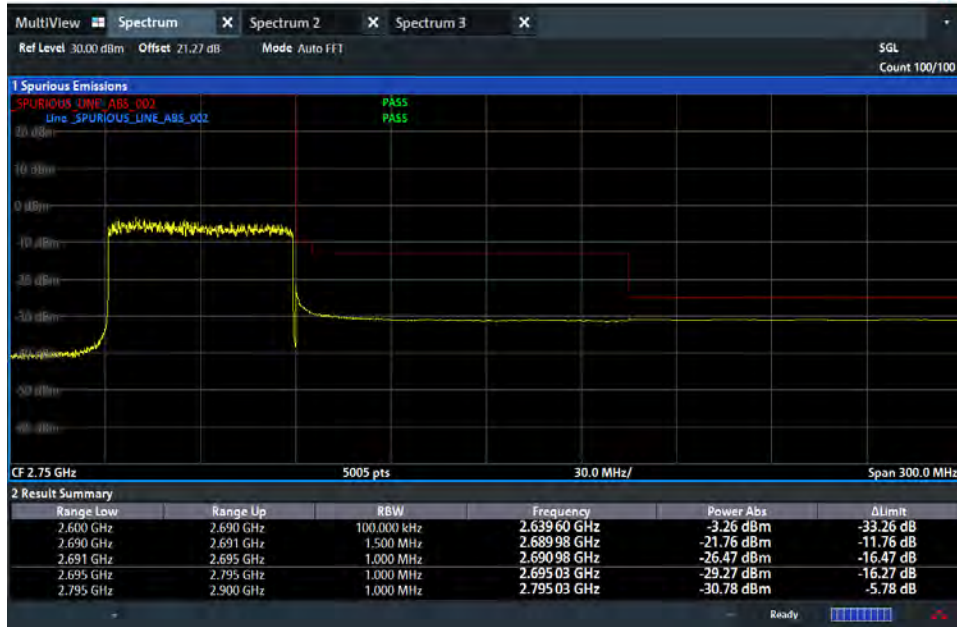
BE_n41_60M_High CH_DL531996_2659.98MHz_DFT_QPSK_1RB Right



BE_n41_60M_High CH_DL531996_2659.98MHz_DFT_QPSK_1RB Right_verify



BE_n41_60M_High CH_DL531996_2659.98MHz_DFT_QPSK_Full RB



BE_n41_60M_Low CH_DL505200_2526MHz_CP_QPSK_1RB Left



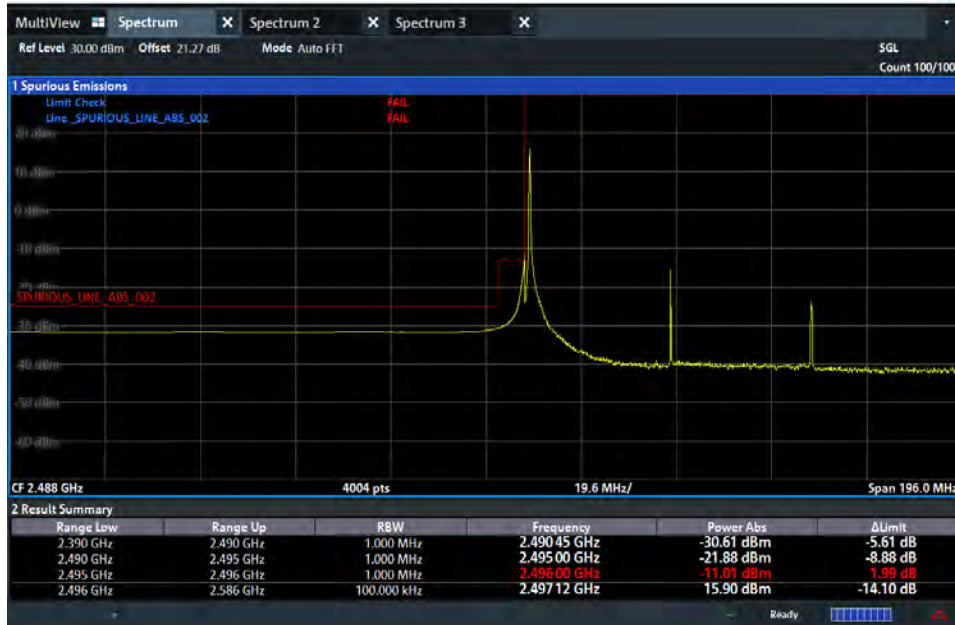
BE_n41_60M_Low CH_DL505200_2526MHz_CP_QPSK_1RB Left_verify



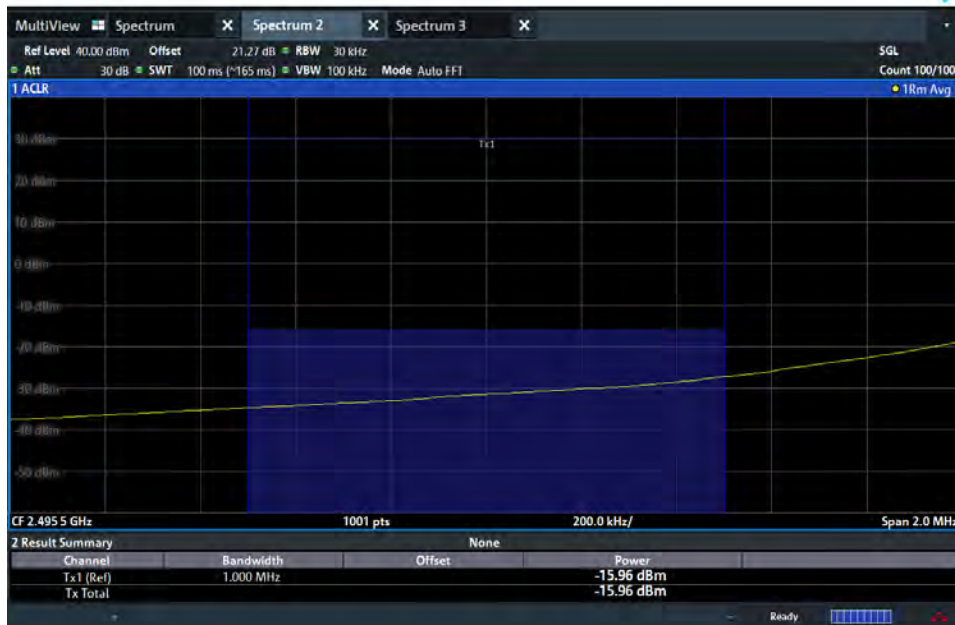
BE_n41_60M_Low CH_DL505200_2526MHz_CP_QPSK_Full RB



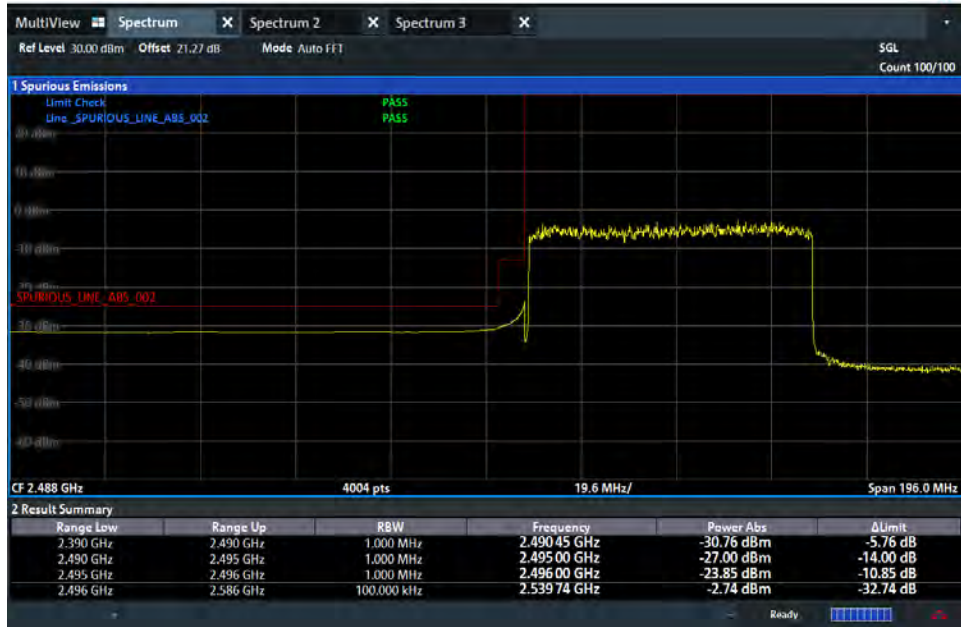
BE_n41_60M_Low CH_DL505200_2526MHz_DFT_BPSK_1RB Left



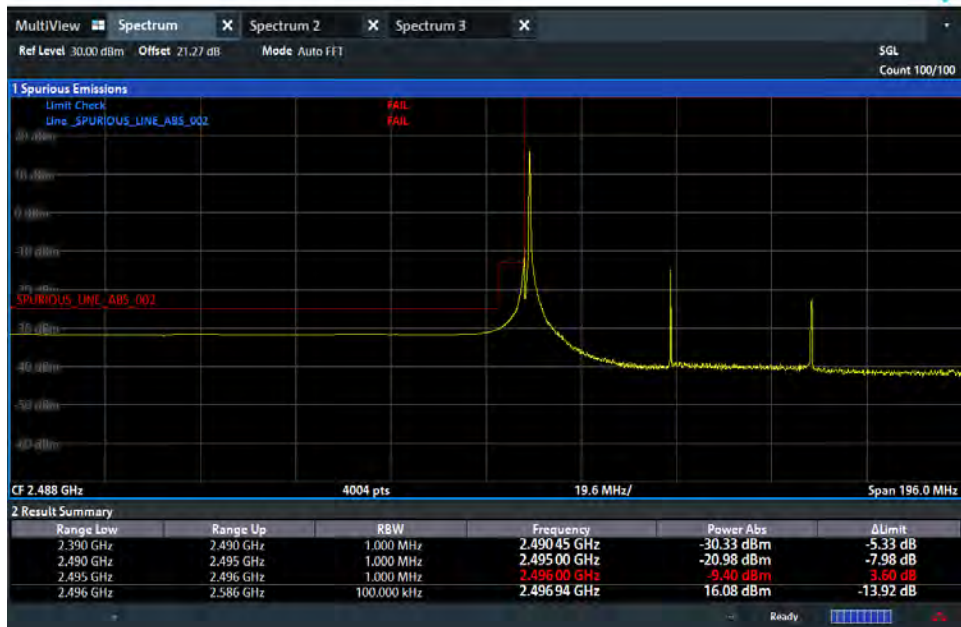
BE_n41_60M_Low CH_DL505200_2526MHz_DFT_BPSK_1RB Left_verify



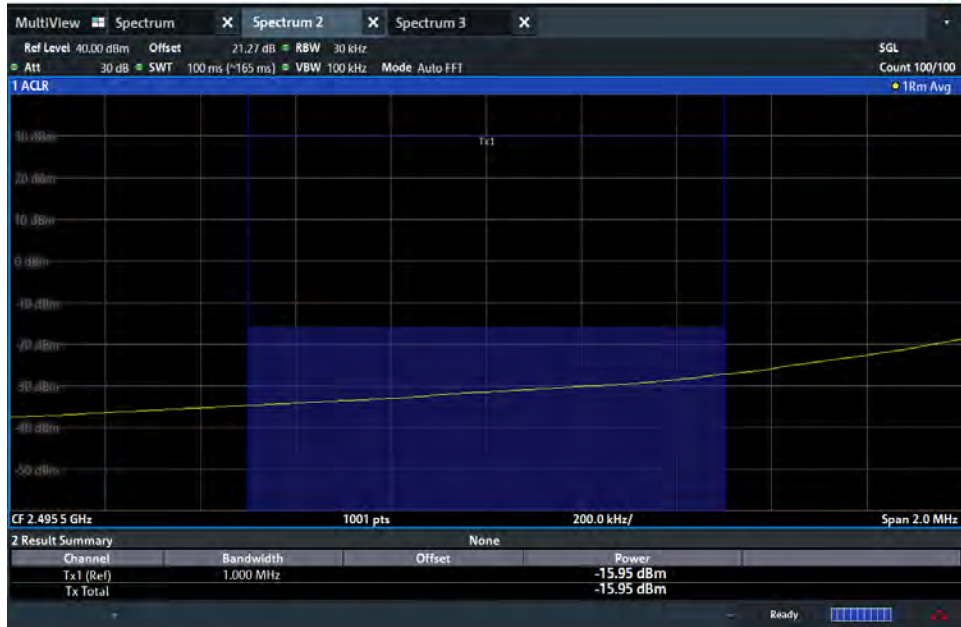
BE_n41_60M_Low CH_DL505200_2526MHz_DFT_BPSK_Full RB



BE_n41_60M_Low CH_DL505200_2526MHz_DFT_QPSK_1RB Left



BE_n41_60M_Low CH_DL505200_2526MHz_DFT_QPSK_1RB Left_verify



BE_n41_60M_Low CH_DL505200_2526MHz_DFT_QPSK_Full RB



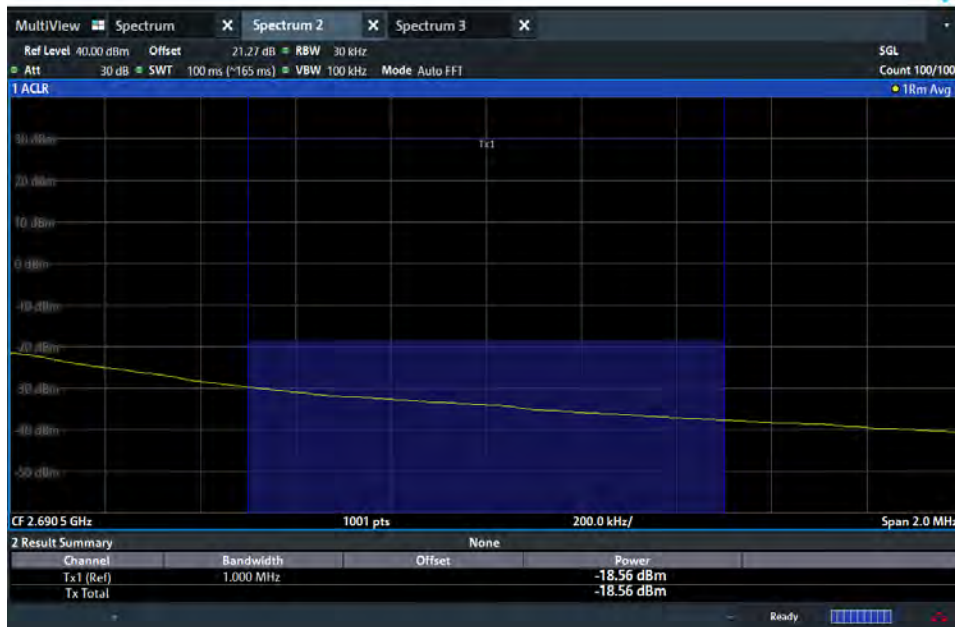
BE_n41_60M_Low CH_DL505200_2526MHz_DFT_QPSK_Full RB_Duty cycle



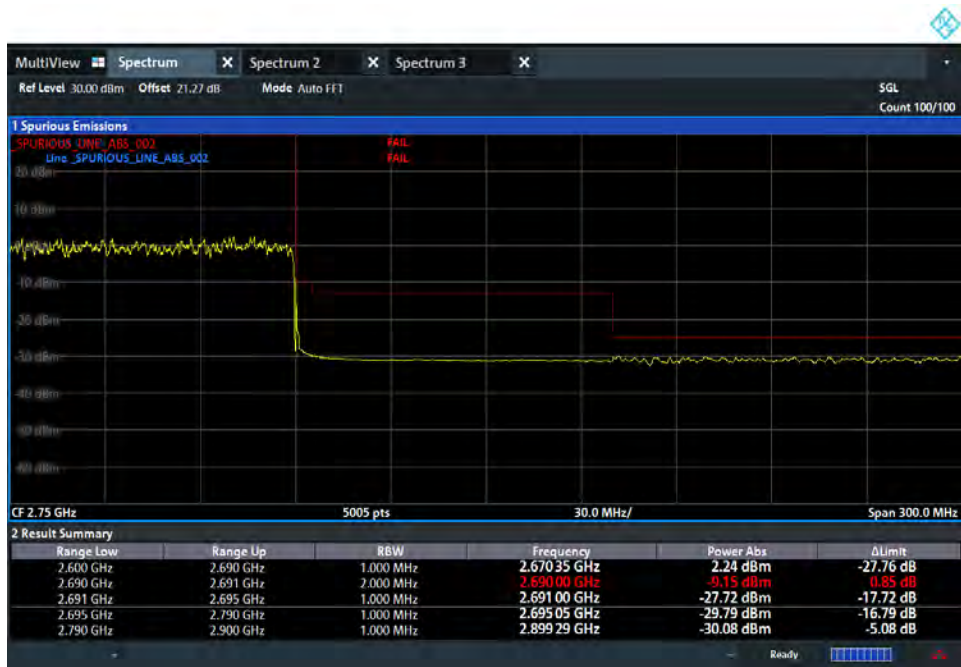
BE_n41_100M_High CH_DL528000_2640MHz_CP_QPSK_1RB Right



BE_n41_100M_High CH_DL528000_2640MHz_CP_QPSK_1RB Right_verify



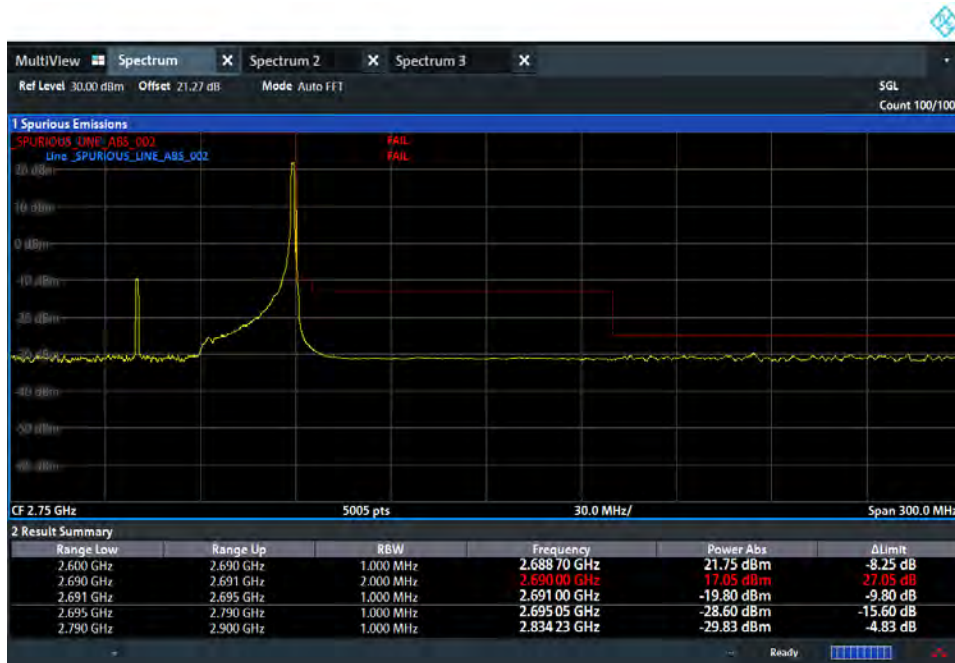
BE_n41_100M_High CH_DL528000_2640MHz_CP_QPSK_Full RB



BE_n41_100M_High CH_DL528000_2640MHz_CP_QPSK_Full RB_verify



BE_n41_100M_High CH_DL528000_2640MHz_DFT_BPSK_1RB Right



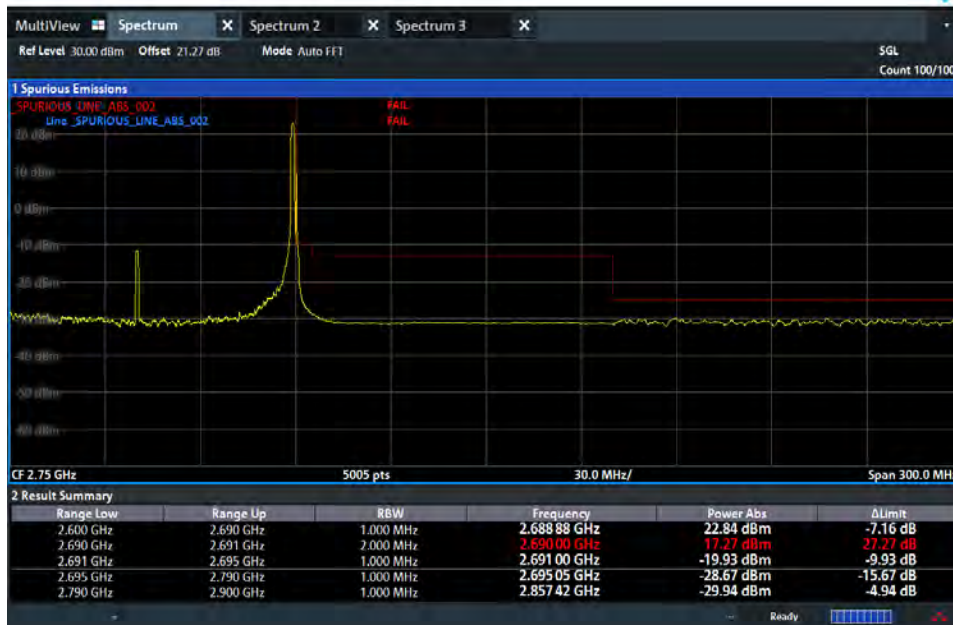
BE_n41_100M_High CH_DL528000_2640MHz_DFT_BPSK_1RB Right_verify



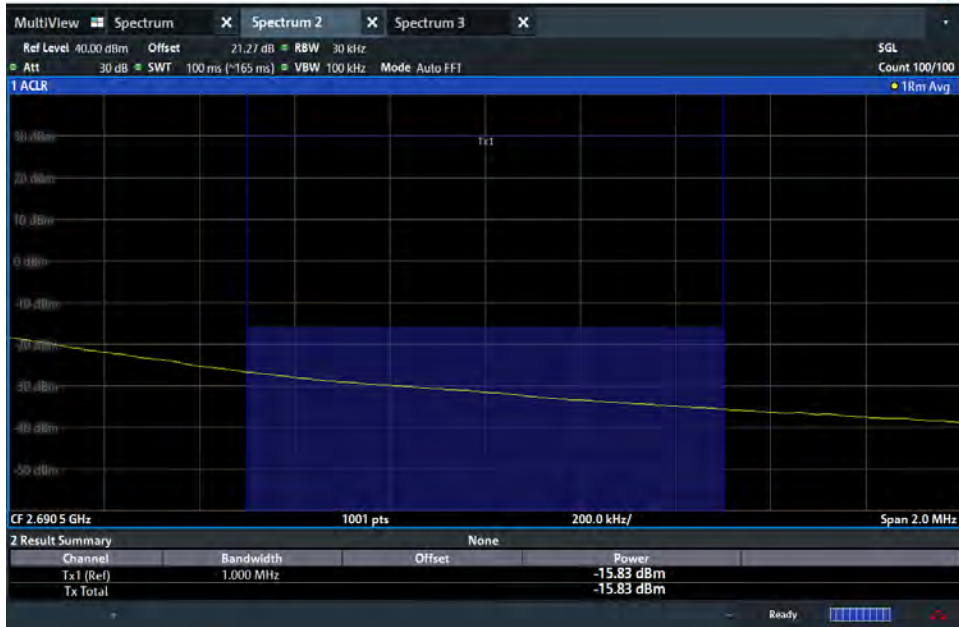
BE_n41_100M_High CH_DL528000_2640MHz_DFT_BPSK_Full RB



BE_n41_100M_High CH_DL528000_2640MHz_DFT_QPSK_1RB Right



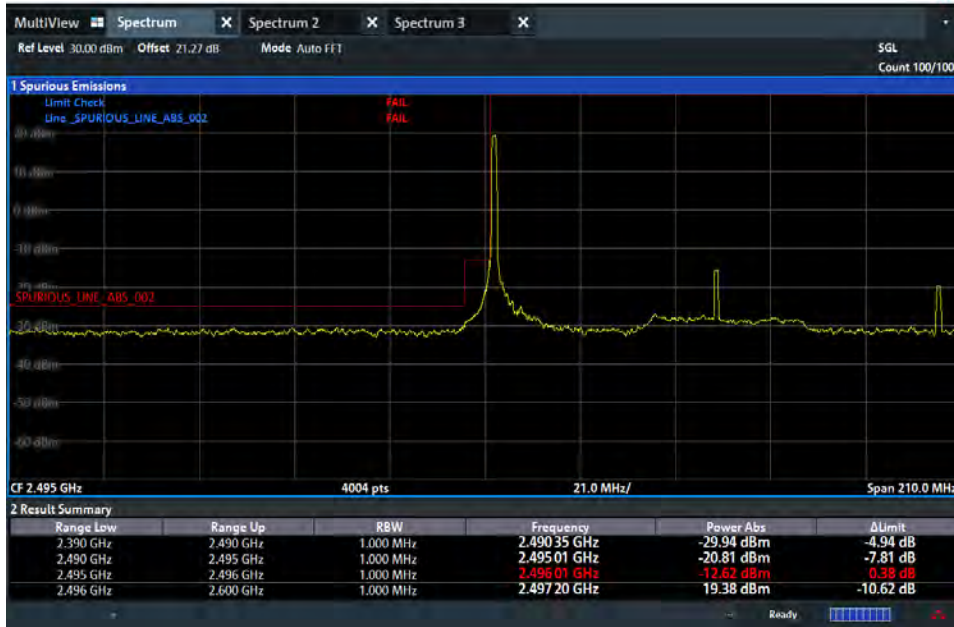
BE_n41_100M_High CH_DL528000_2640MHz_DFT_QPSK_1RB Right_verify



BE_n41_100M_High CH_DL528000_2640MHz_DFT_QPSK_Full RB



BE_n41_100M_Low CH_DL509202_2546.01MHz_CP_QPSK_1RB Left



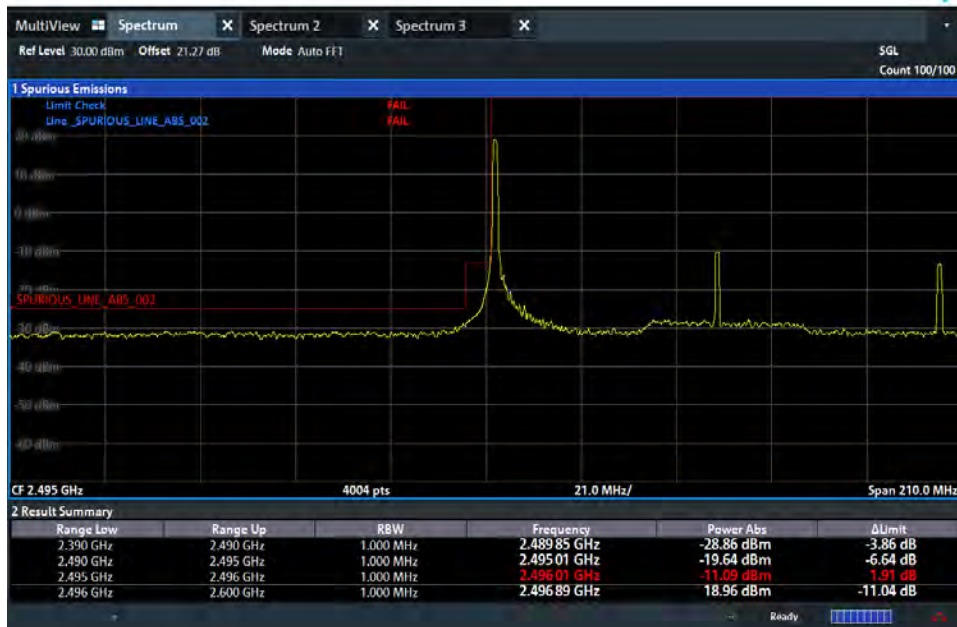
BE_n41_100M_Low CH_DL509202_2546.01MHz_CP_QPSK_1RB Left_verify



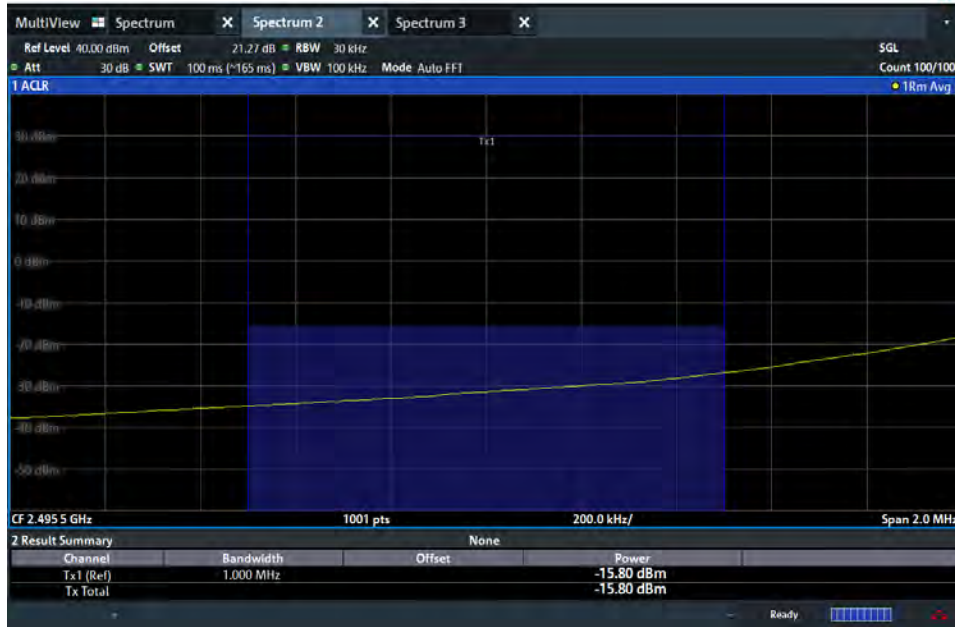
BE_n41_100M_Low CH_DL509202_2546.01MHz_CP_QPSK_Full RB



BE_n41_100M_Low CH_DL509202_2546.01MHz_DFT_BPSK_1RB Left



BE_n41_100M_Low CH_DL509202_2546.01MHz_DFT_BPSK_1RB Left_verify



BE_n41_100M_Low CH_DL509202_2546.01MHz_DFT_QPSK_1RB Left



BE_n41_100M_Low CH_DL509202_2546.01MHz_DFT_QPSK_1RB Left_verify



BE_n41_100M_Low CH_DL509202_2546.01MHz_DFT_QPSK_Full RB



BE_n41_100M_Low CH_DL509202_2546.01MHz_DFT_QPSK_Full RB_Duty cycle



Conducted Spurious Emissions

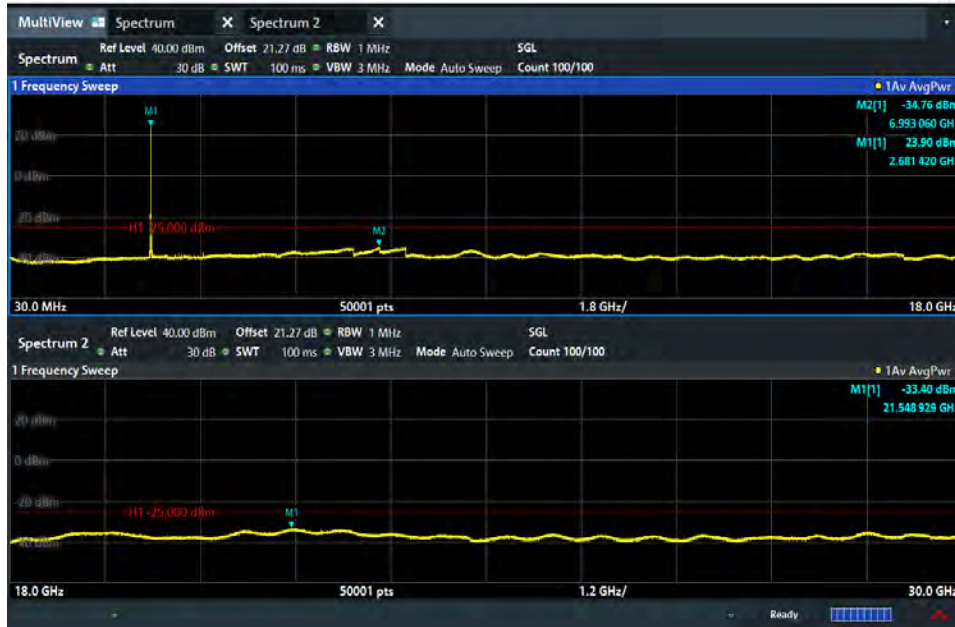
CSE_n41_10M_High CH_DL537000_2685MHz_DFT_BPSK_1@1



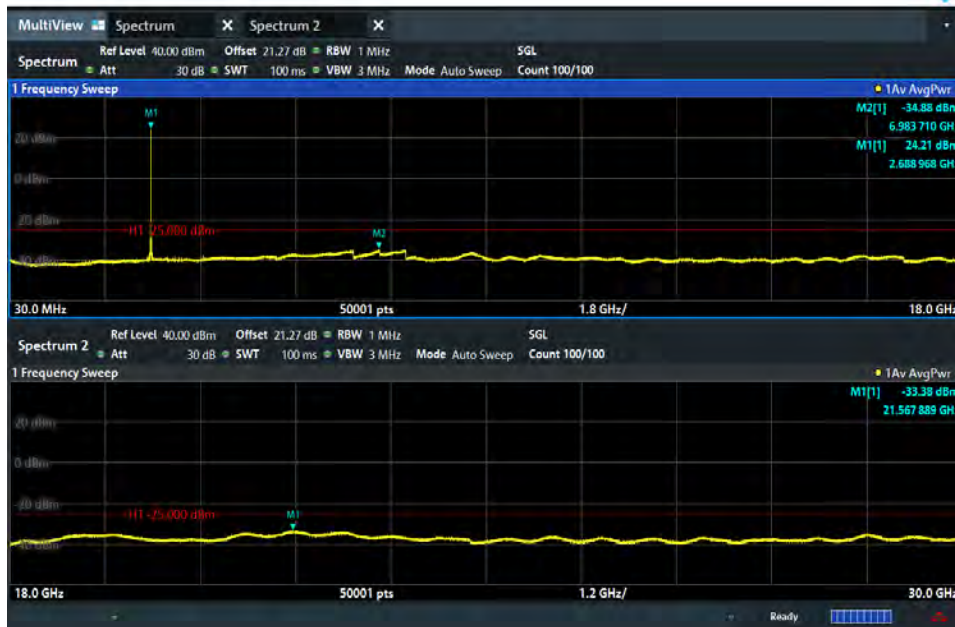
CSE_n41_10M_High CH_DL537000_2685MHz_DFT_BPSK_1@22



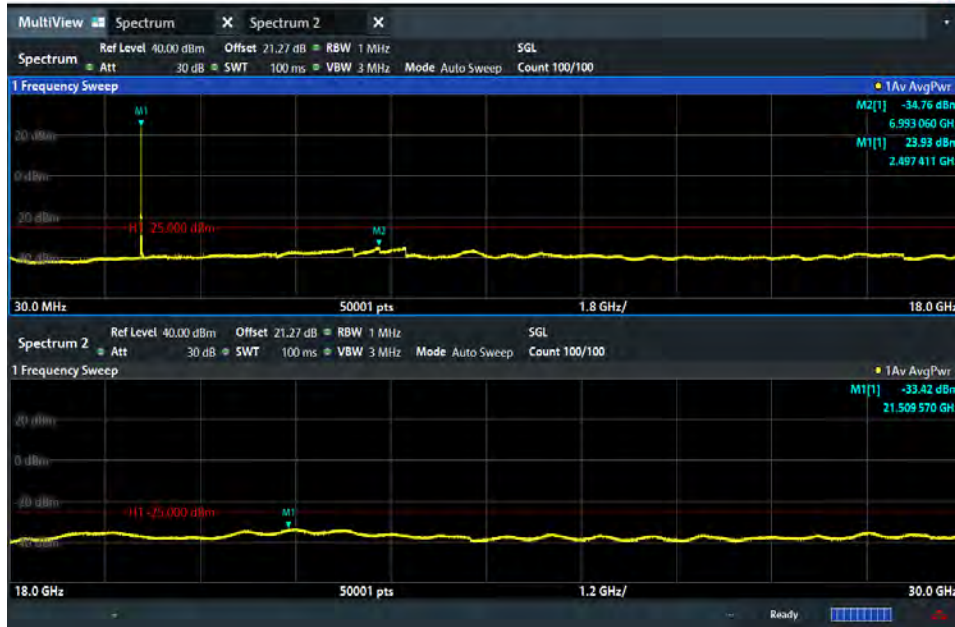
CSE_n41_10M_High CH_DL537000_2685MHz_DFT_QPSK_1@1



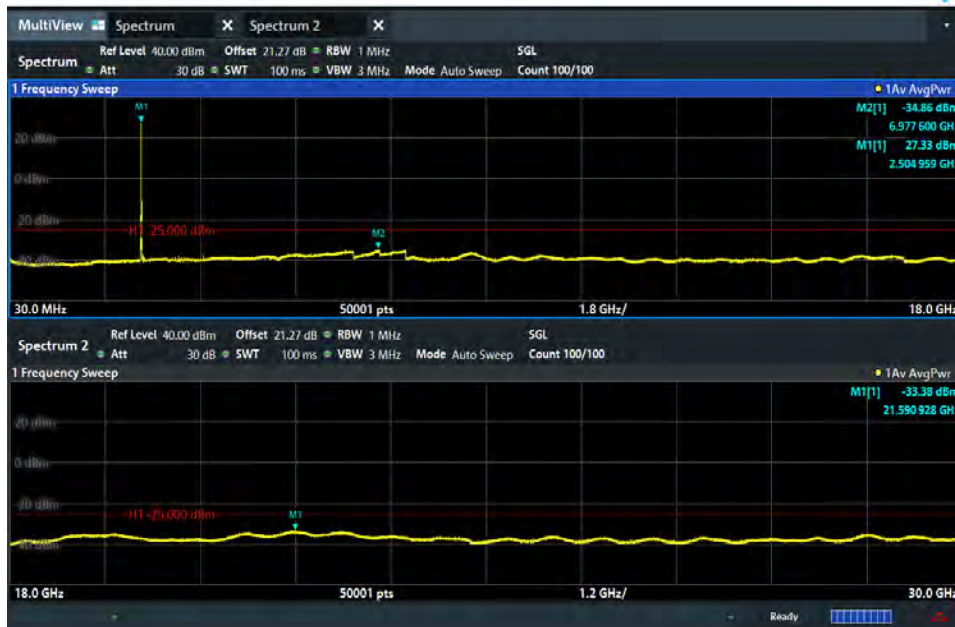
CSE_n41_10M_High CH_DL537000_2685MHz_DFT_QPSK_1@22



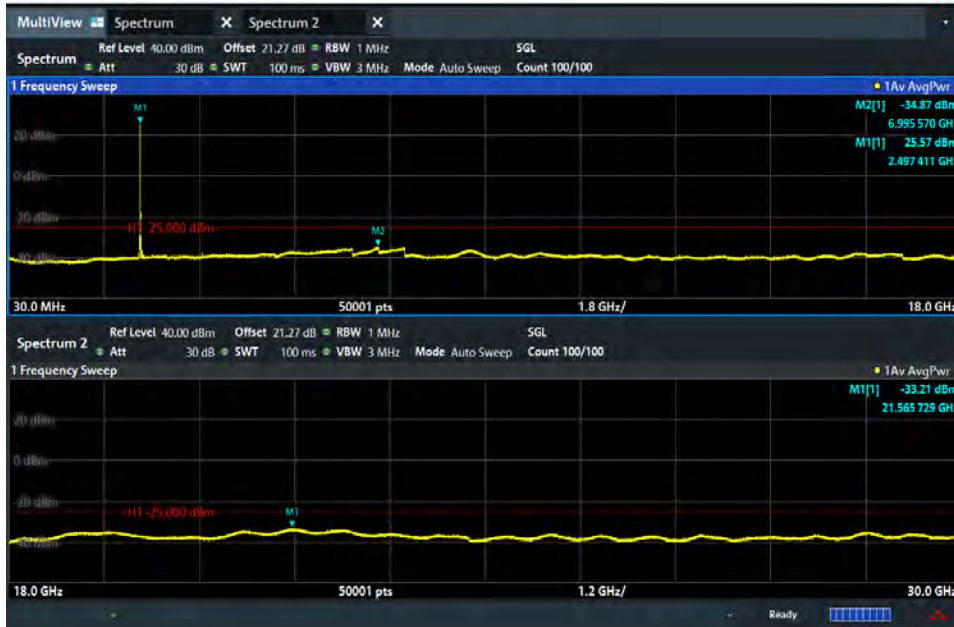
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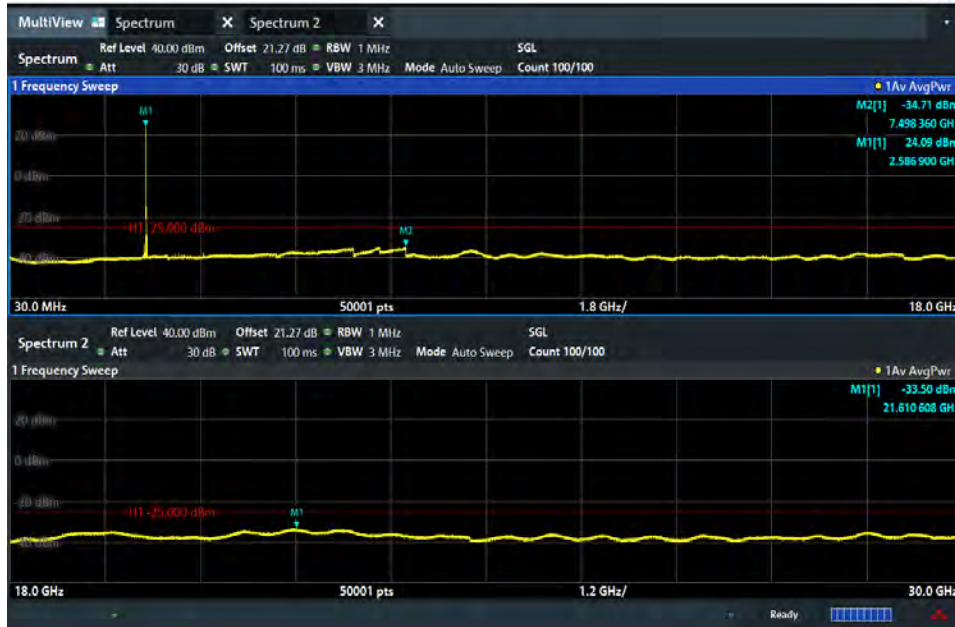
CSE_n41_10M_Low CH_DL500202_2501.01MHz_DFT_BPSK_1@22



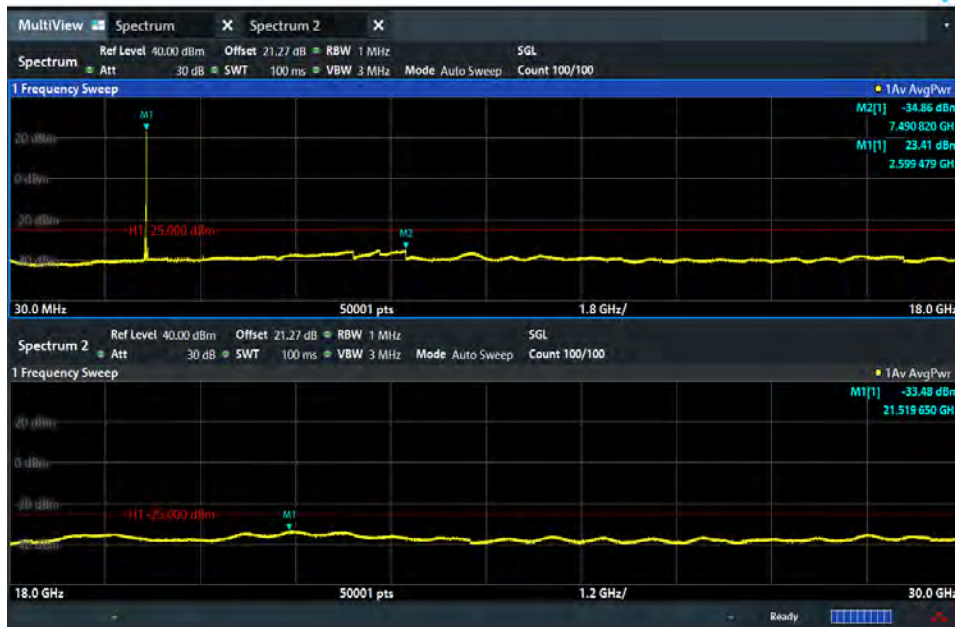
CSE_n41_10M_Low CH_DL500202_2501.01MHz_DFT_QPSK_1@1



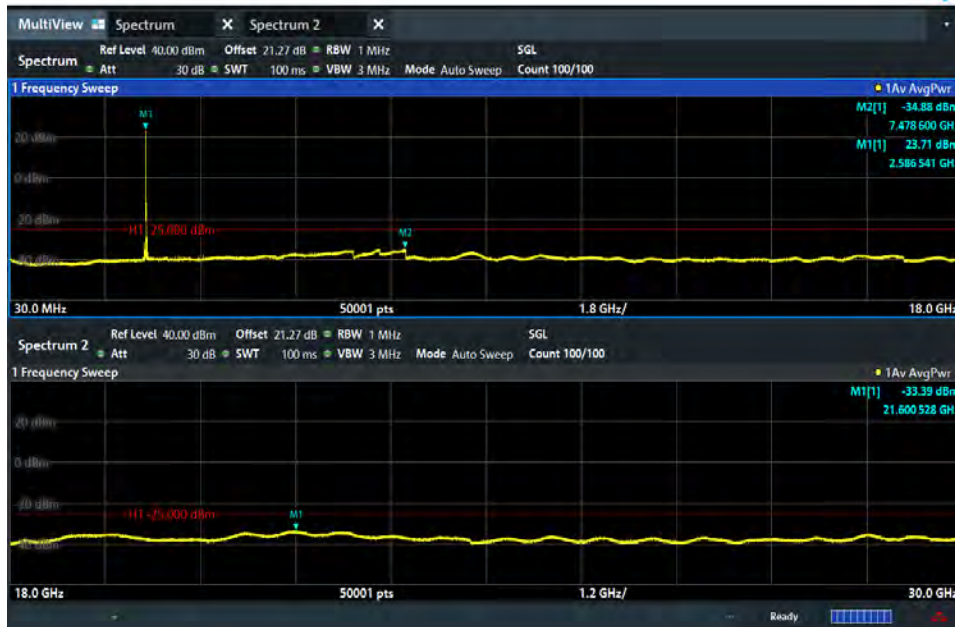
CSE_n41_10M_Mid CH_DL518598_2592.99MHz_DFT_BPSK_1@1



CSE_n41_10M_Mid CH_DL518598_2592.99MHz_DFT_BPSK_1@22



CSE_n41_10M_Mid CH_DL518598_2592.99MHz_DFT_QPSK_1@1



CSE_n41_10M_Mid CH_DL518598_2592.99MHz_DFT_QPSK_1@22



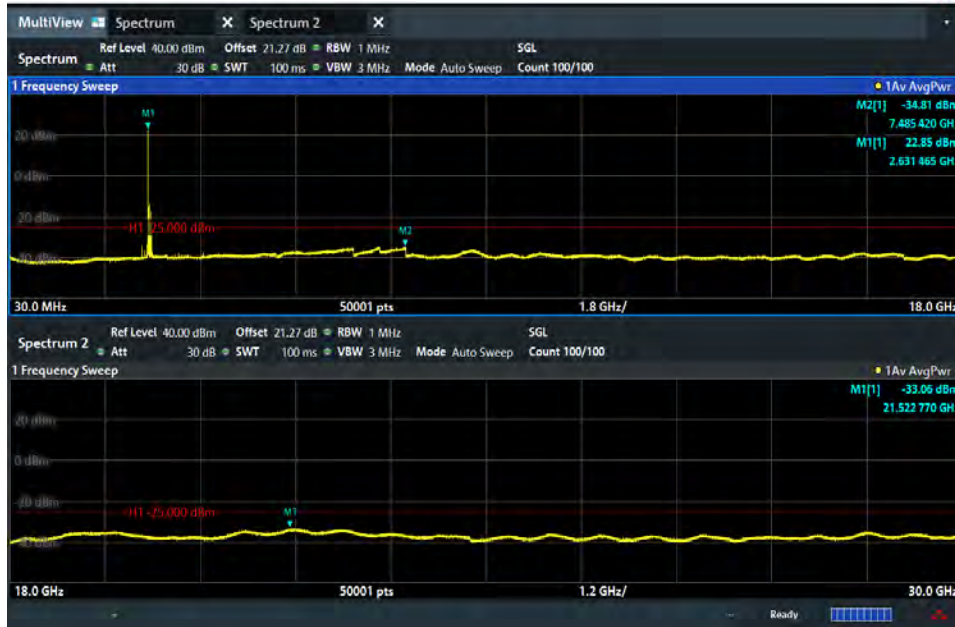
CSE_n41_60M_High CH_DL531996_2659.98MHz_DFT_BPSK_1@1



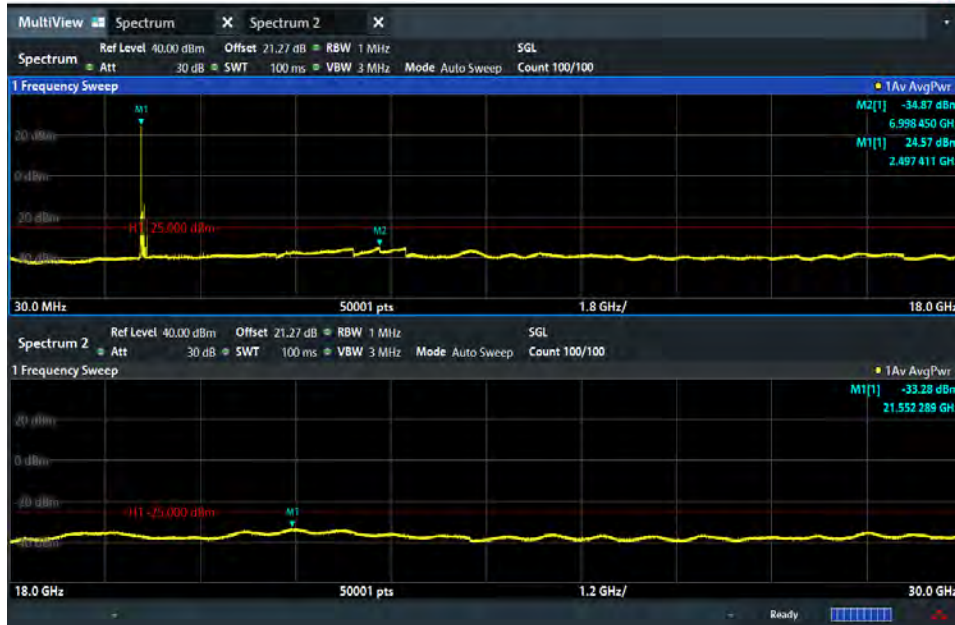
CSE_n41_60M_High CH_DL531996_2659.98MHz_DFT_BPSK_1@160



CSE_n41_60M_High CH_DL531996_2659.98MHz_DFT_QPSK_1@1



CSE_n41_60M_Low CH_DL505200_2526MHz_DFT_BPSK_1@1



CSE_n41_60M_Low CH_DL505200_2526MHz_DFT_BPSK_1@160



CSE_n41_60M_Low CH_DL505200_2526MHz_DFT_QPSK_1@1



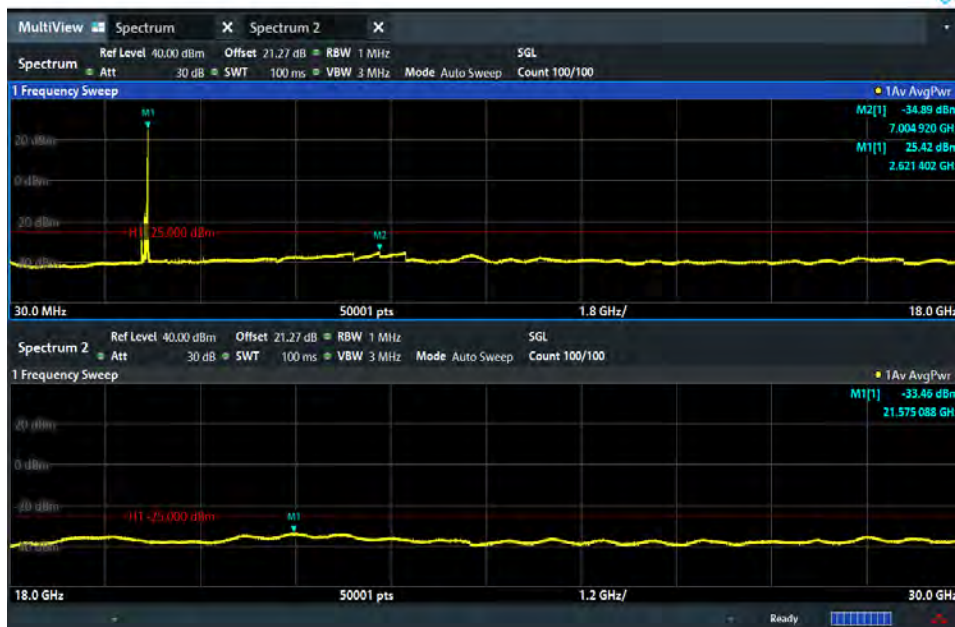
CSE_n41_60M_Low CH_DL505200_2526MHz_DFT_QPSK_1@160



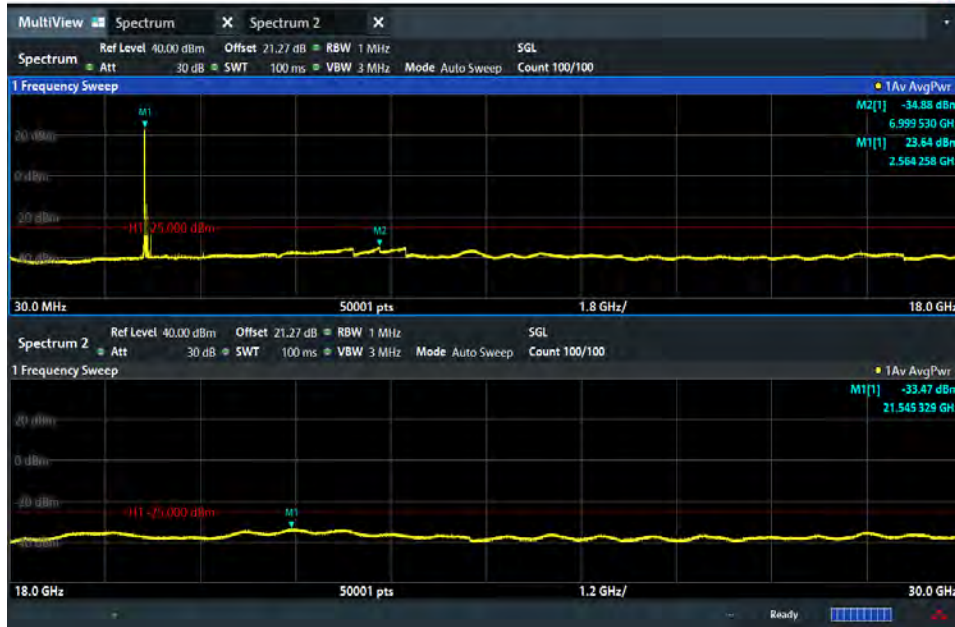
CSE_n41_60M_Mid CH_DL518598_2592.99MHz_DFT_BPSK_1@1



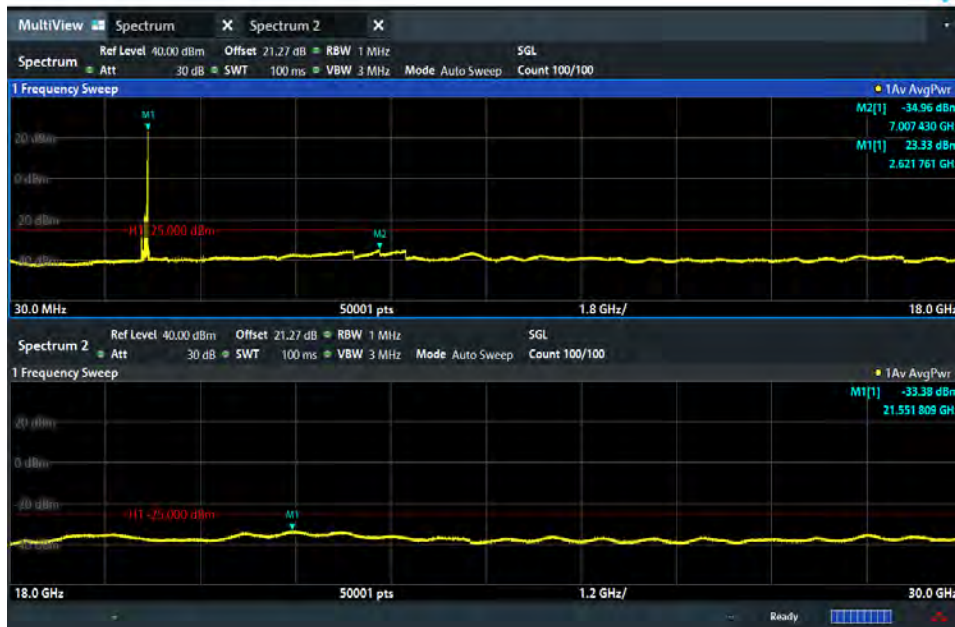
CSE_n41_60M_Mid CH_DL518598_2592.99MHz_DFT_BPSK_1@160



CSE_n41_60M_Mid CH_DL518598_2592.99MHz_DFT_QPSK_1@1



CSE_n41_60M_Mid CH_DL518598_2592.99MHz_DFT_QPSK_1@160



CSE_n41_100M_High CH_DL528000_2640MHz_DFT_BPSK_1@1



CSE_n41_100M_High CH_DL528000_2640MHz_DFT_BPSK_1@271



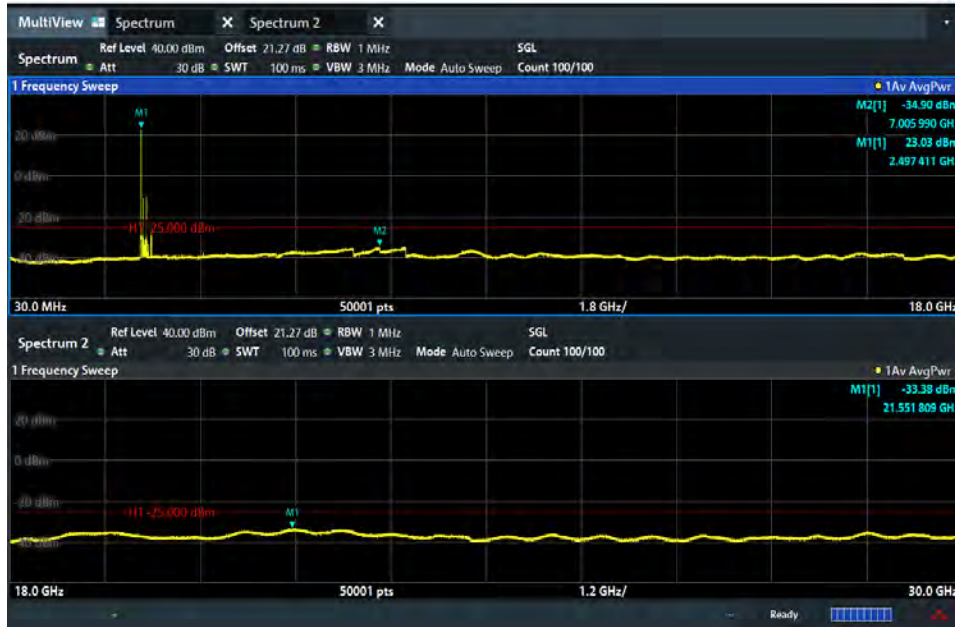
CSE_n41_100M_High CH_DL528000_2640MHz_DFT_QPSK_1@1



CSE_n41_100M_High CH_DL528000_2640MHz_DFT_QPSK_1@271



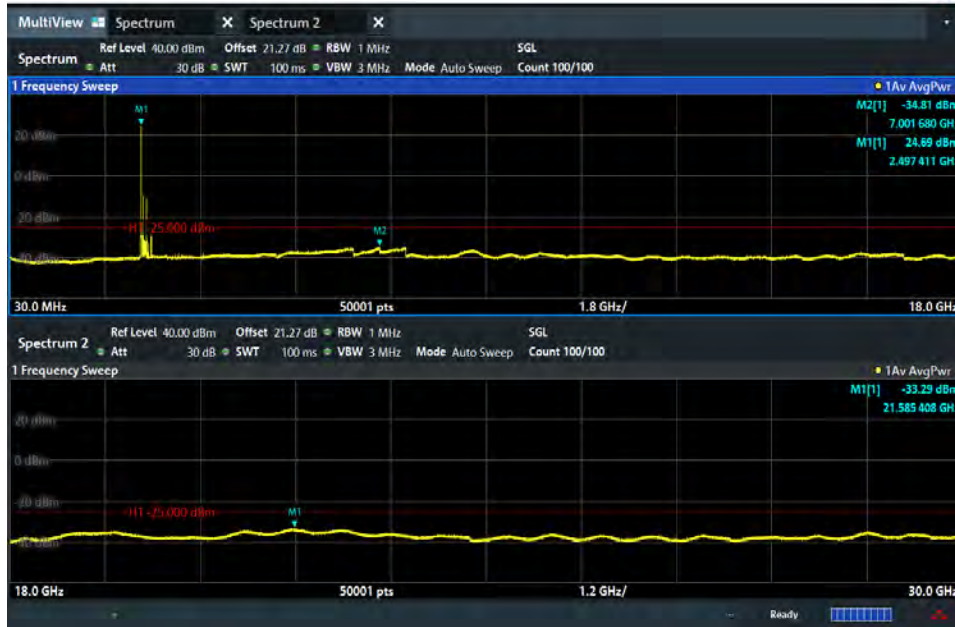
CSE_n41_100M_Low CH_DL509202_2546.01MHz_DFT_BPSK_1@1



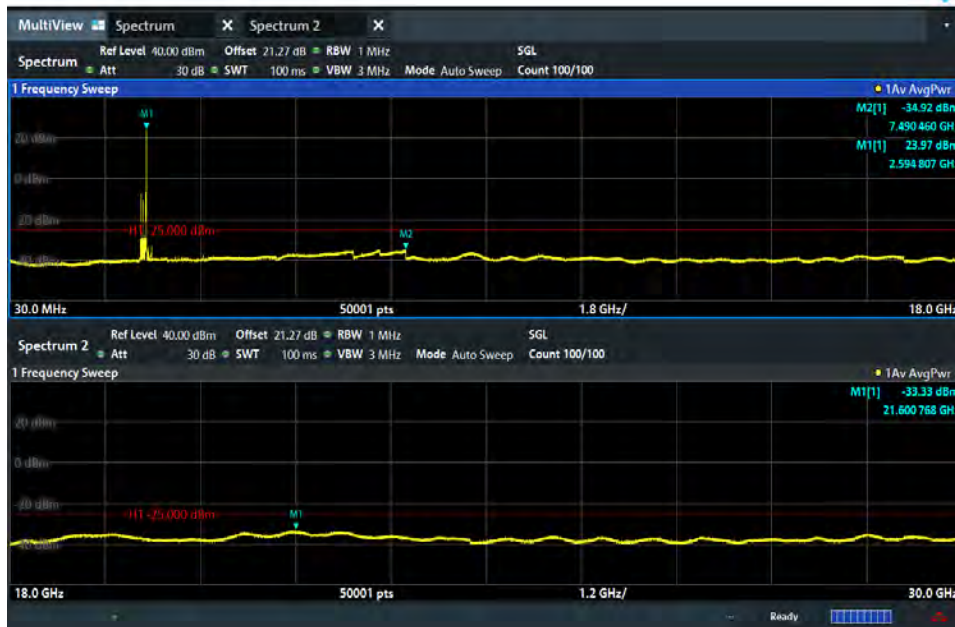
CSE_n41_100M_Low CH_DL509202_2546.01MHz_DFT_BPSK_1@271



CSE_n41_100M_Low CH_DL509202_2546.01MHz_DFT_QPSK_1@1



CSE_n41_100M_Low CH_DL509202_2546.01MHz_DFT_QPSK_1@271



CSE_n41_100M_Mid CH_DL518598_2592.99MHz_DFT_QPSK_1@1



CSE_n41_100M_Mid CH_DL518598_2592.99MHz_DFT_QPSK_1@271

