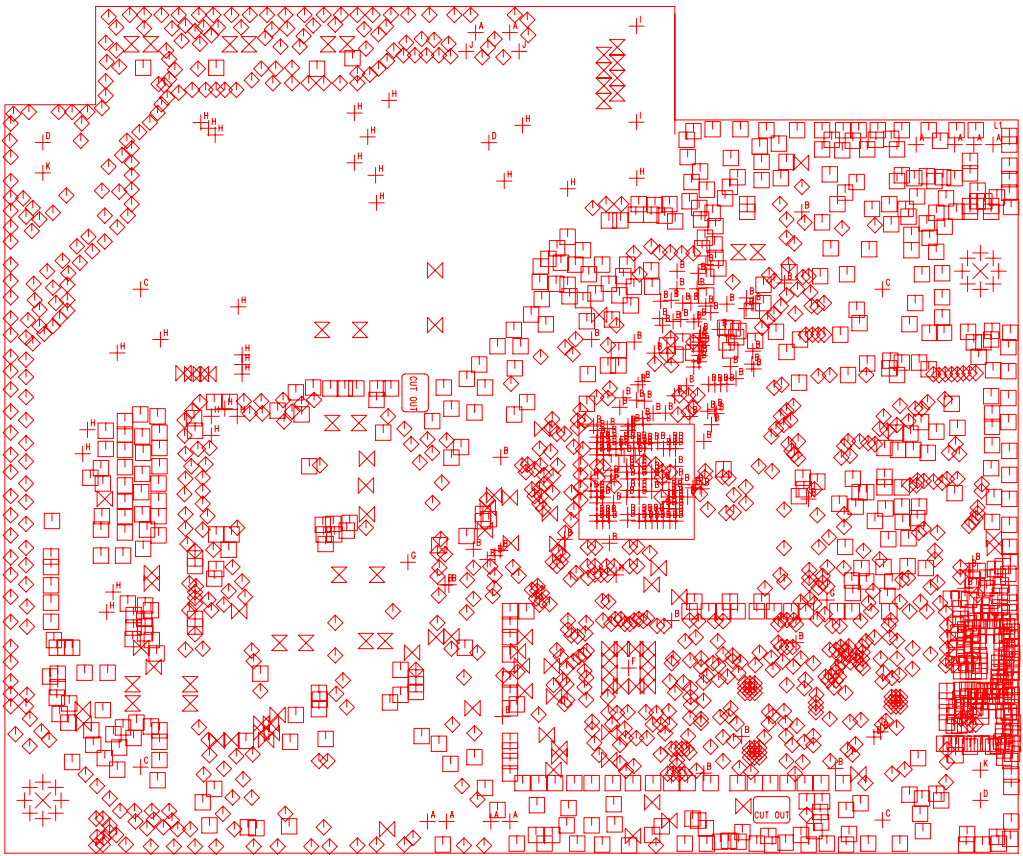
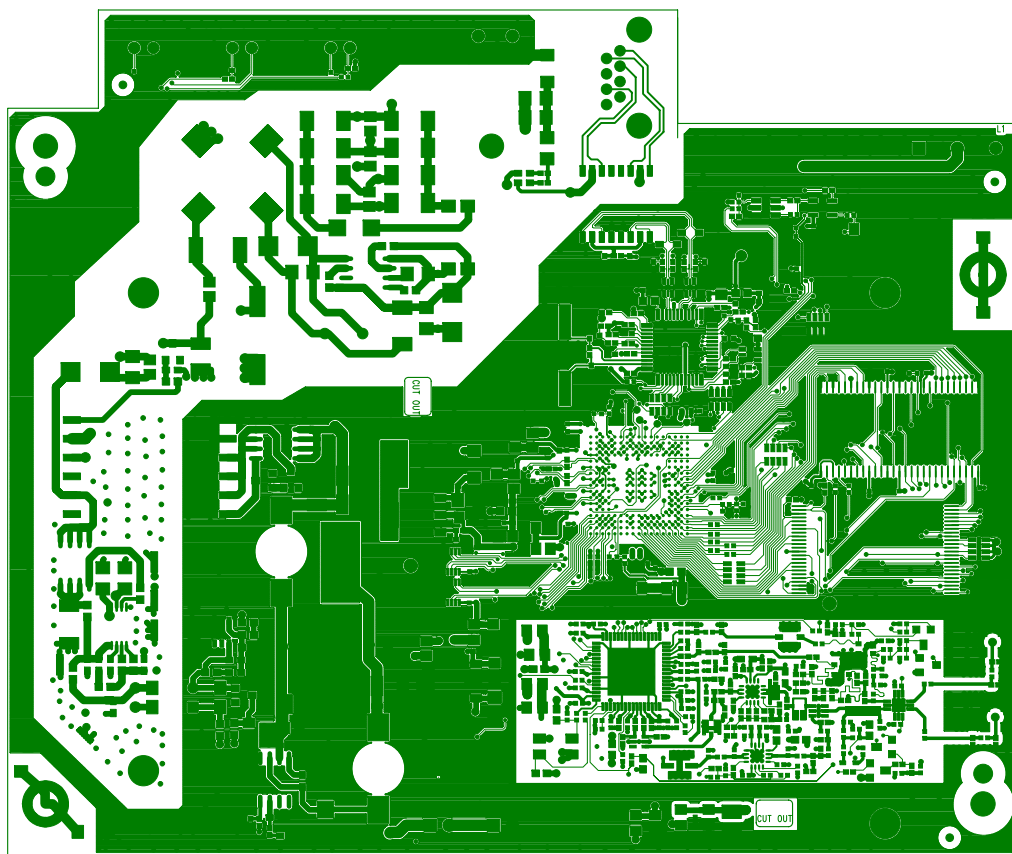


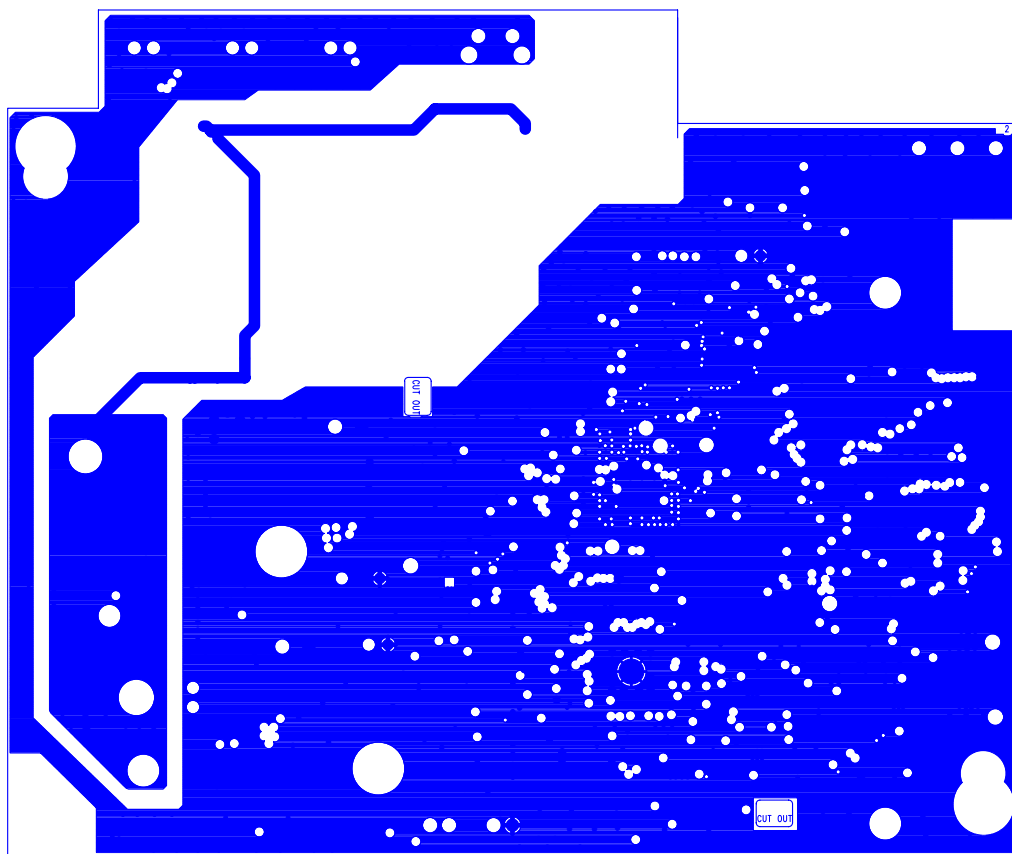
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126	2	×	NO	+/-3
12	659	□	YES	+/-3
10	866	◇	YES	+/-3
35	30	⊗	YES	+/-3
18	74	⊗	YES	+/-3
43	10	+ ^A	YES	+/-3
8	178	+ ^B	YES	+/-3
157.48	4	+ ^C	NO	+/-3
125.98	3	+ ^D	NO	+/-3
100	1	+ ^E	YES	+/-3
71	2	+ ^F	NO	+/-3
28	28	+ ^H	YES	+/-3
132	2	+ ^I	NO	+/-3
60	2	+ ^J	YES	+/-3
98.42	2	+ ^K	NO	+/-0.0



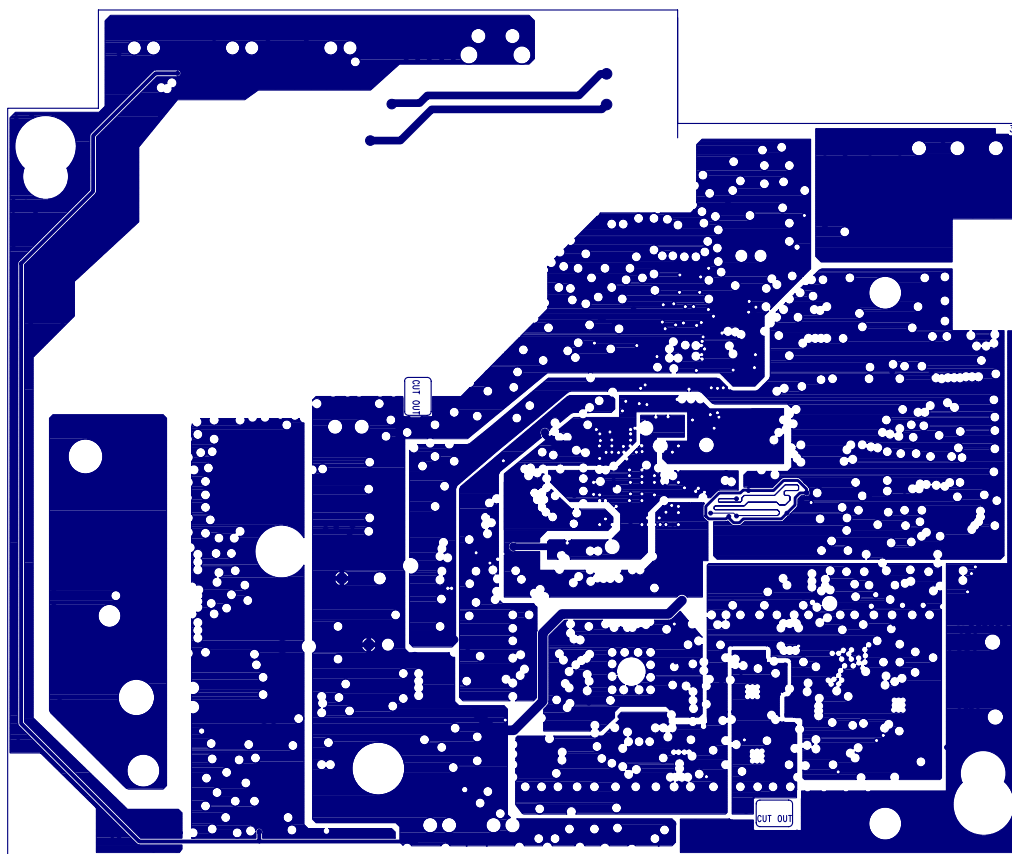
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PCB Name	WAP-D16
PCB Rev.	1A1G
Date	2007/04/23
Layer Name	Drill Drawing
PCB P/N	8WAPD16..1A1G
PCB W/G	5/5
PCB Layer	4
PLD Engineer	Janis Huang
IDD Approved	
PLD Approved	
R&D Engineer	Aaron Lee
R&D Approved	Name
LDS No.	LDS2007040077



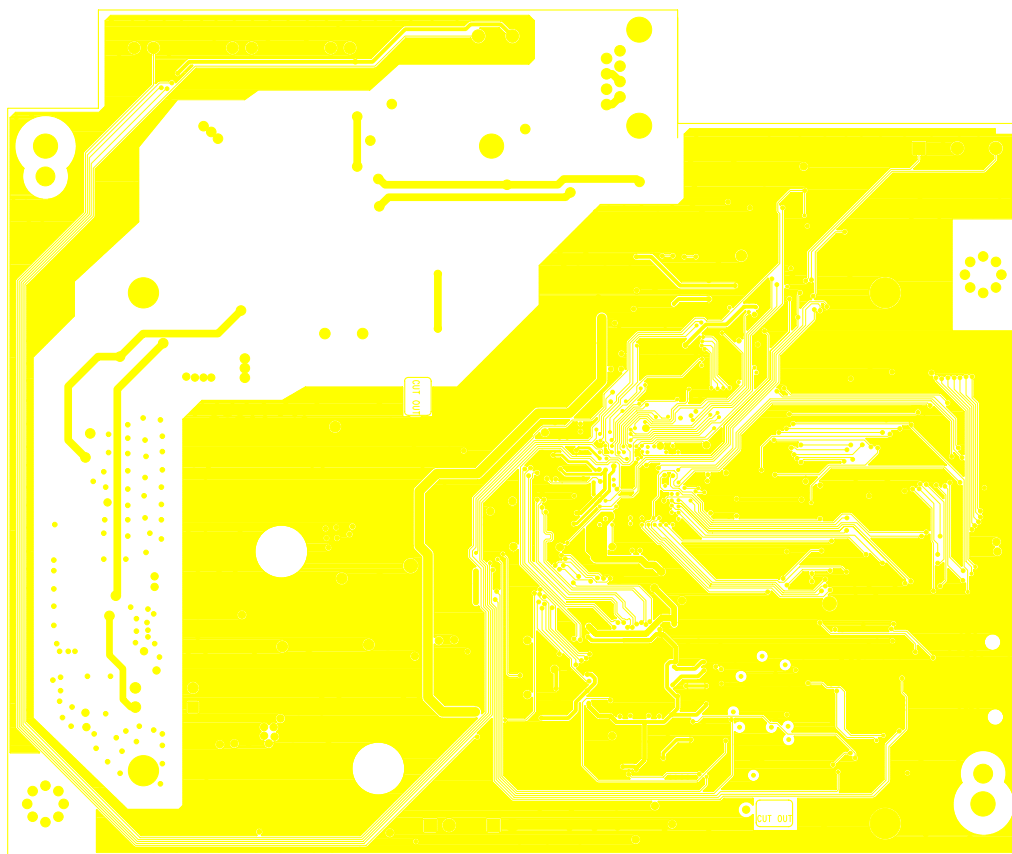
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PCB Name	WAP-D16
PCB Rev.	1A1G
Date	2007/04/23
Layer Name	Top Route



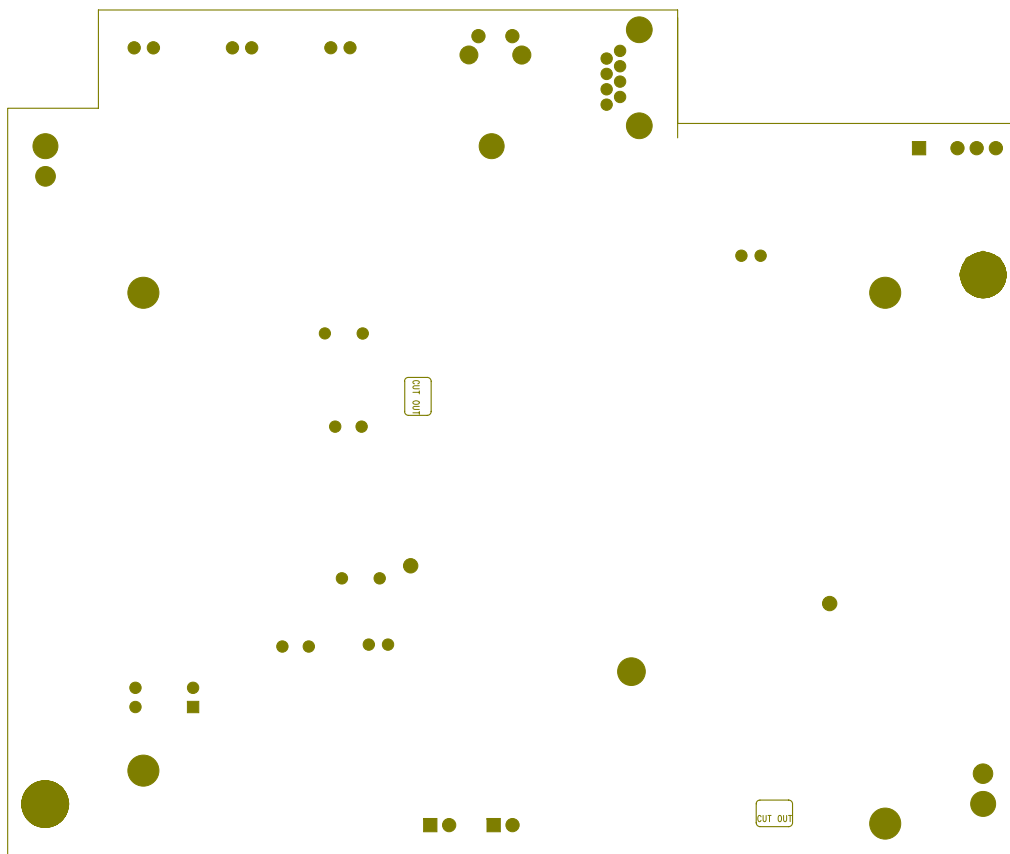
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PCB Name	WAP-D16
PCB Rev.	1A1G
Date	2007/04/23
Layer Name	Layer 2



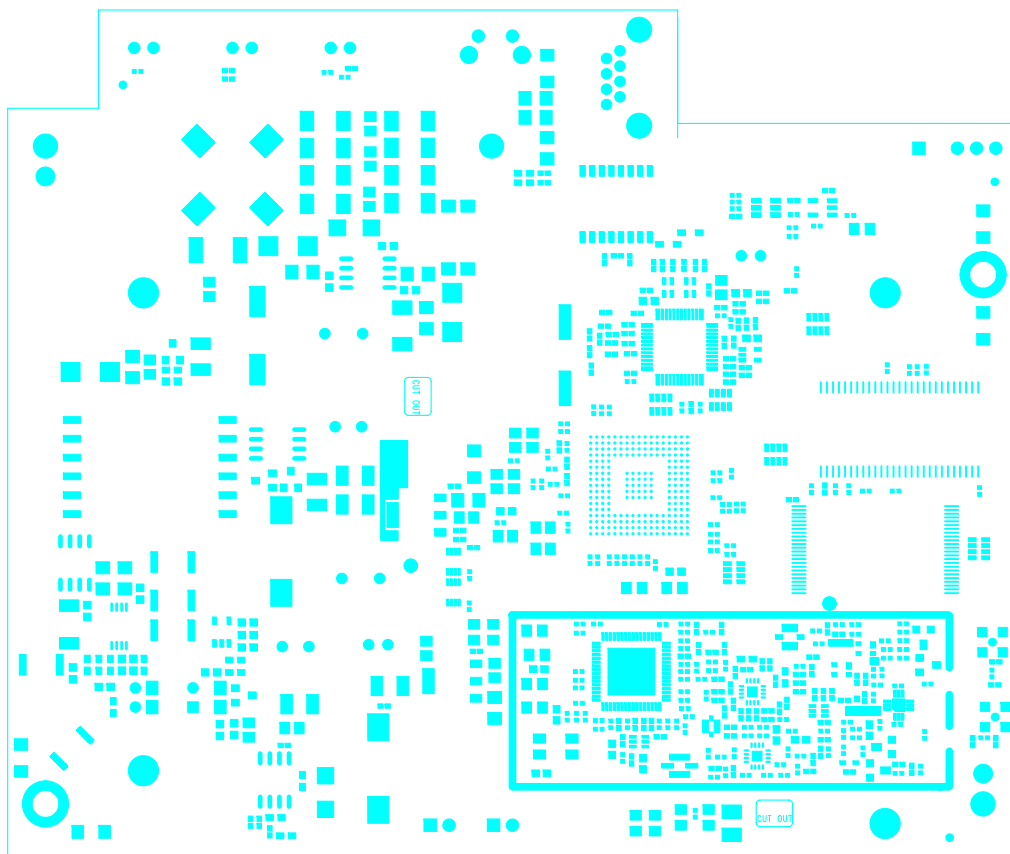
ALPHA Networks Inc.	
PCB Name	WAP-D16
PCB Rev.	1A1G
Date	2007/04/23
Layer Name	Layer 3



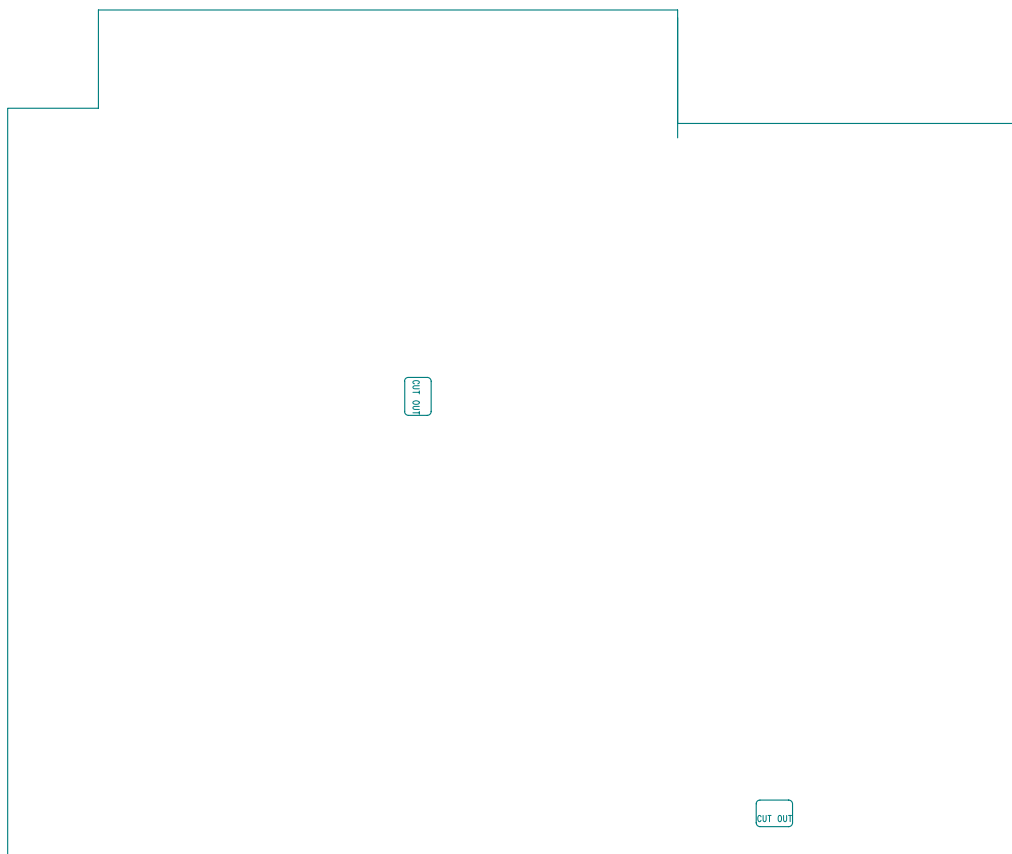
ALPHA Networks Inc.	
PCB Name	WAP-D16
PCB Rev.	1A1G
Date	2007/04/23
Layer Name	Bottom Route



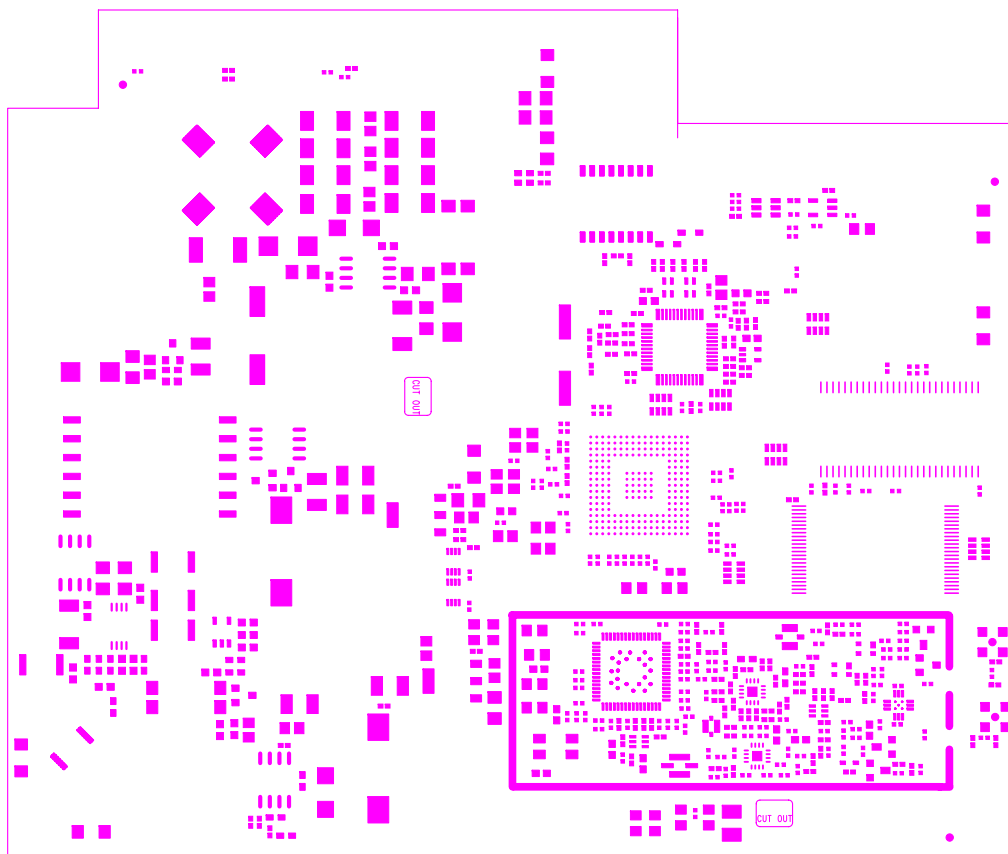
ALPHA Networks Inc.	
PCB Name	WAP-D16
PCB Rev.	1A1G
Date	2007/04/23
Layer Name	Solder Mask Bottom



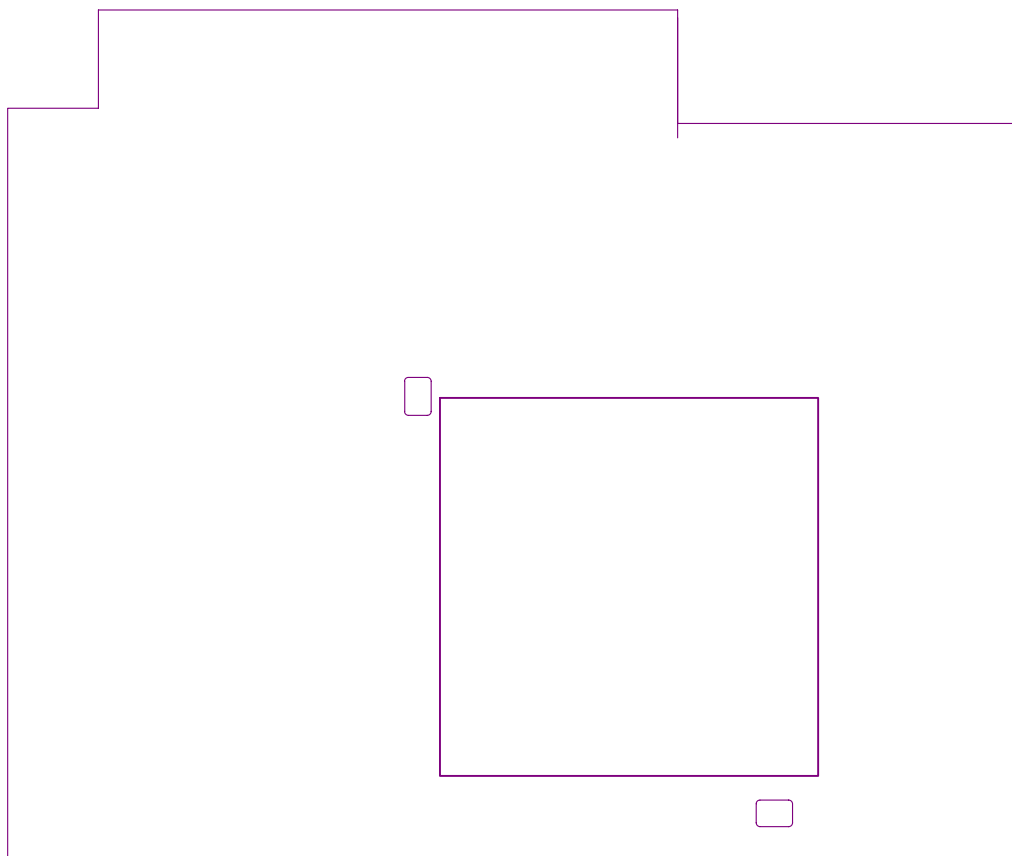
ALPHA Networks Inc.	
PCB Name	WAP-D16
PCB Rev.	1A1G
Date	2007/04/23
Layer Name	Solder Mask Top



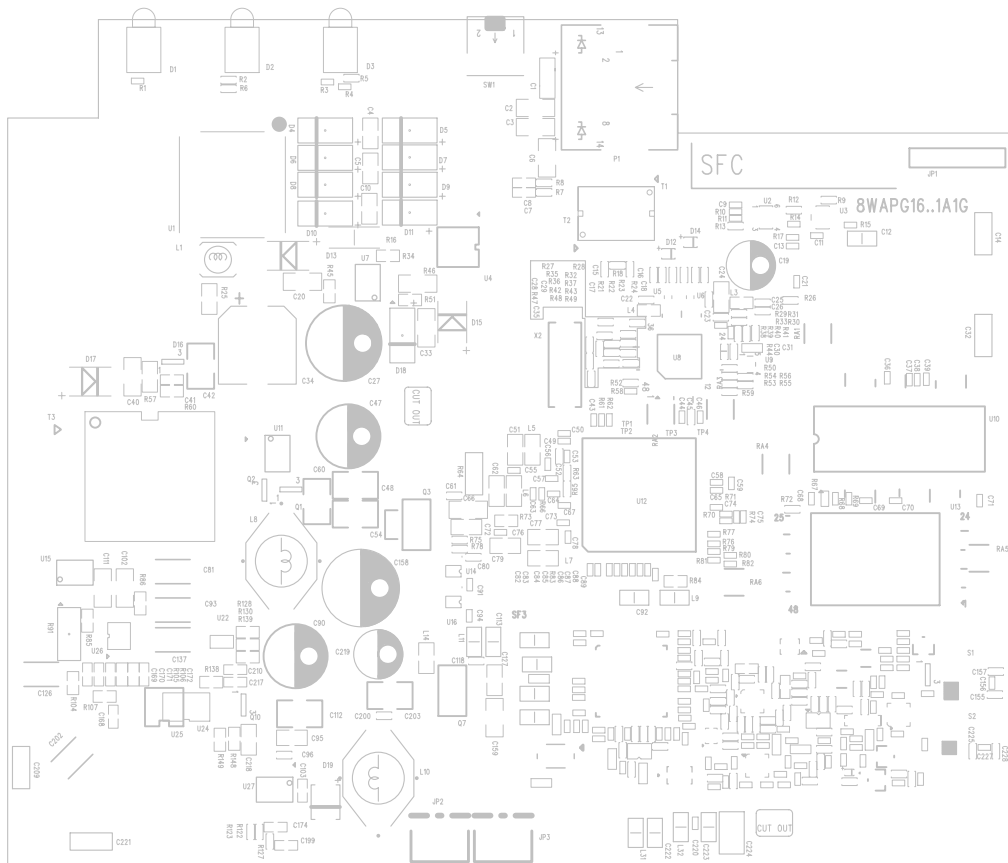
ALPHA Networks Inc.	
PCB Name	WAP-D16
PCB Rev.	1A1G
Date	2007/04/23
Layer Name	Paste Mask Bottom



ALPHA Networks Inc.	
PCB Name	WAP-D16
PCB Rev.	1A1G
Date	2007/04/23
Layer Name	Paste Mask Top



ALPHA Networks Inc.	
PCB Name	WAP-D16
PCB Rev.	1A1G
Date	2007/04/23
Layer Name	Silkscreen Bottom



ALPHA Networks Inc.	
PCB Name	WAP-D16
PCB Rev.	1A1G
Date	2007/04/23
Layer Name	Silkscreen Top