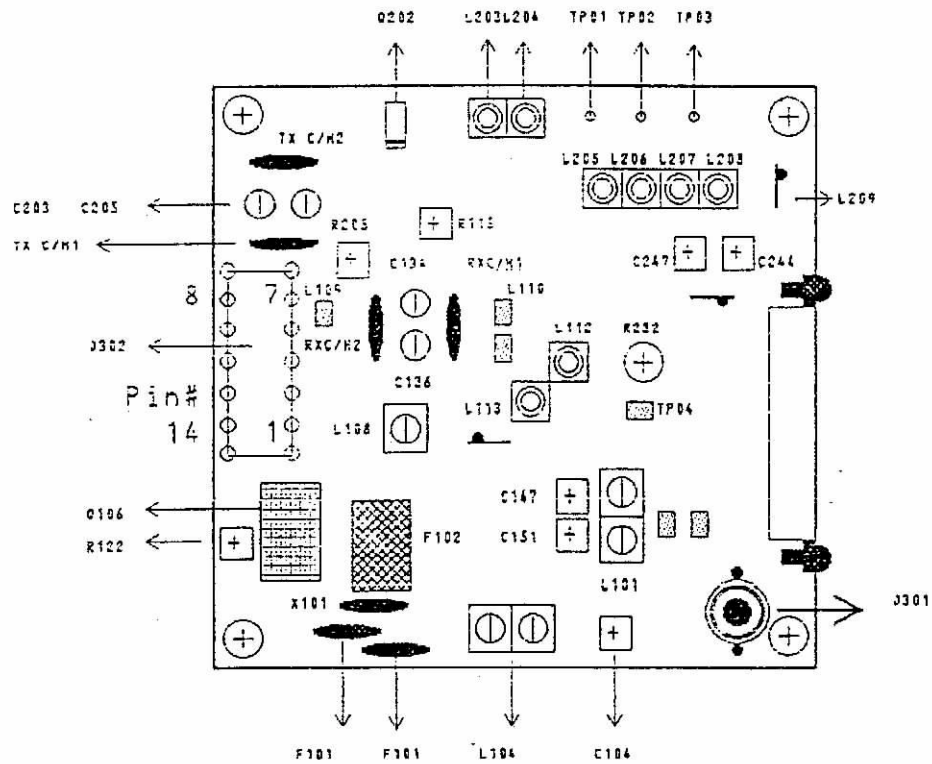


## Tune up Information

## Alignment Procedure for cu35u

CU35UJB.JC.

## Test Point Location



## J302 Connector(I/O)端子

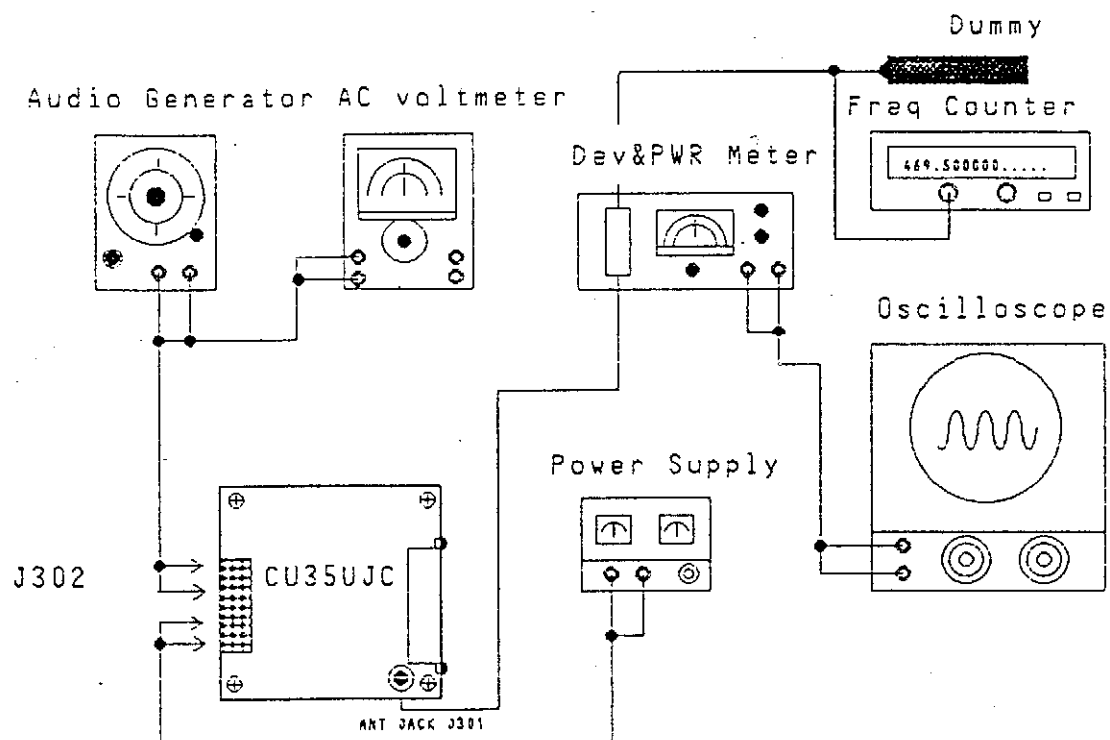
## Pin #

|                   |                     |
|-------------------|---------------------|
| 1. Test Point     | 8. N C              |
| 2. GND            | 9. N C              |
| 3. Data Out       | 10. C/H 2           |
| 4. Signal Present | 11. C/H 1           |
| 5. 7.5v DC        | 12. DC Out(TX)時 +5v |
| 6. Audio Mute     | 13. Key Line        |
| 7. Data Input     | 14. GND             |

## Tune up Information

## Alignment Procedure for CU35UJC

Fig-1 TX 調整結線図



## Tune up Information

## Alignment Procedure for CU35UJ8

Fig-2 RX 調整結線図

