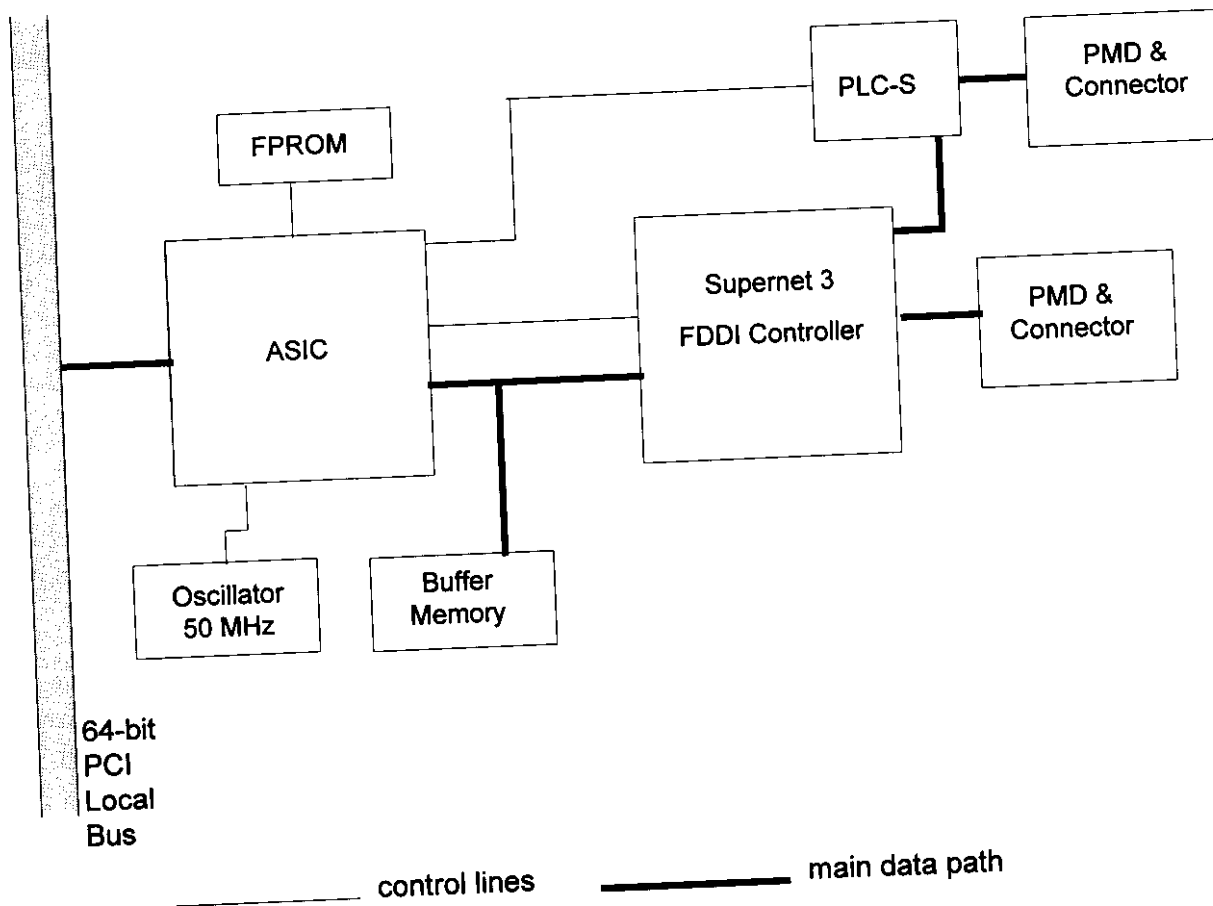# 4 BLOCK DIAGRAM OF EQUIPMENT

4.1 Block Diagram of SK-NET FDDI-LP 64 DAS

The Block Diagram displays the controller's main devices.

- Application Specific Integrated Circuit (ASIC)
- Reprogrammable Flash EPROM (FPROM)
- Supernet 3 FDDI Controller
- 128 Kbyte Buffer Memory
- 2 PLC-Transceiver Units

The PCI clock depends on the host PCI-System (in the tested system 33 MHz).
The Crystal Oscillator on the adapter card operates at 50 MHz.