

3.0 BLOCK DIAGRAM

This section should include the block diagram of the EUT showing all internal oscillators and clock frequencies. The rule section that requires a block diagram to be submitted is 2.1033(b)(5). Although a block diagram is not specifically required for some devices, including a block diagram in the application will make processing of it easier.

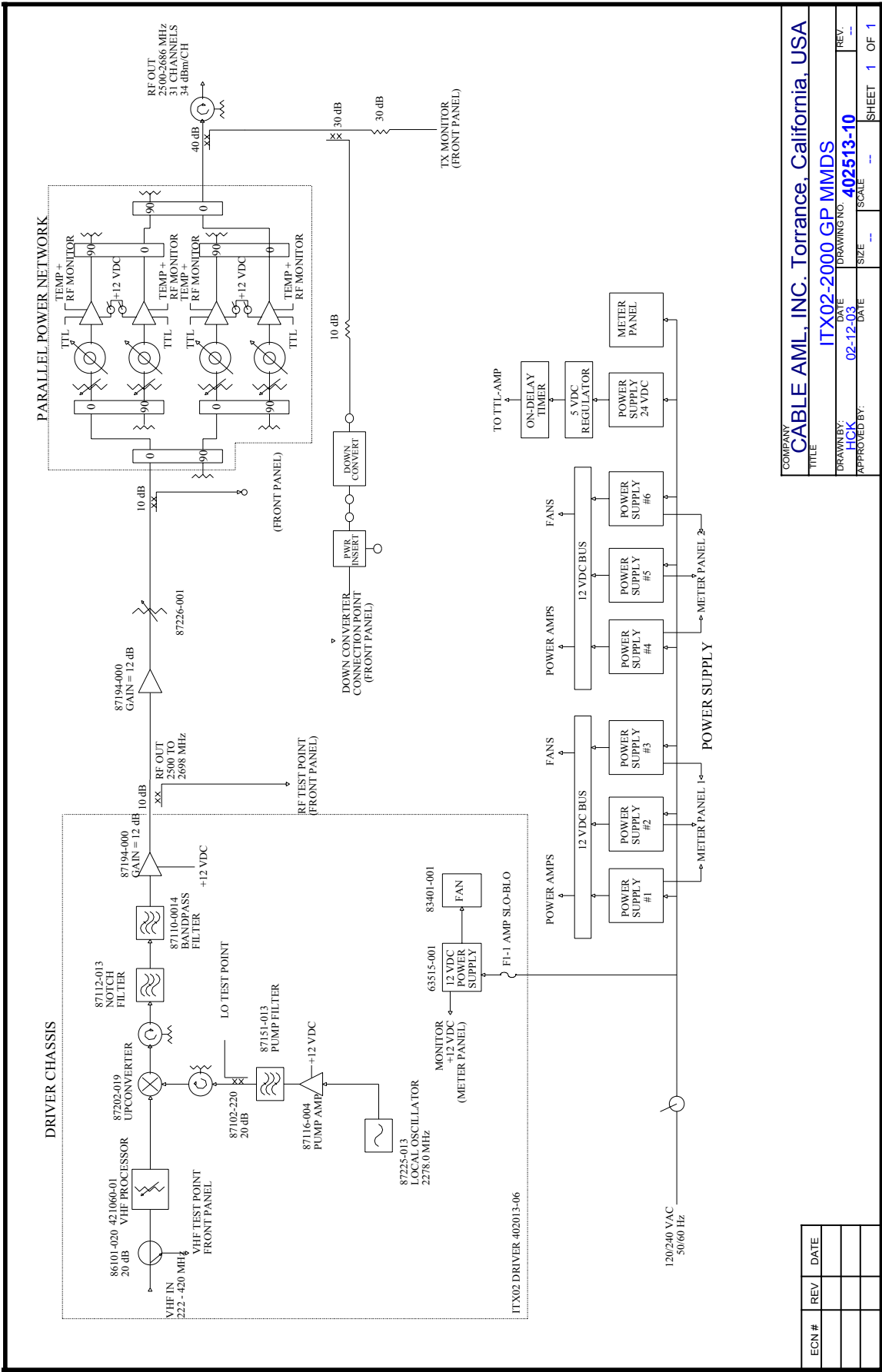


Figure 2. ITX02-2000 Block Diagram.