



MICROTEK
ELECTRONICS, INC.

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Kyle,

The following is the circuit description for our Minilink 2.4THP-90

"The DUT consists of a PLL controlled VCO driving a buffer amplifier and a power amplifier. The output of the power amplifier is low pass filtered before connection to the SMA antenna coupler. Output power is limited by the saturated power output of the power amplifier. Frequency stability is achieved by a 25ppm 10 mHz quartz crystal serving as the PLL reference oscillator.

The video input passes through a deviation control used to set the proper modulation level then, through a video low pass filter to insure high frequency noise will not cause over deviation and excessive bandwidth. The video voltage and PLL control voltage are summed to complete the modulation process."

Thanks,

Mike Henkoski
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