

APPROVAL SHEET

CQM730 Micro-Power Wi-Fi Module

Halogens Free Product

P/N: RFIQM0730010XB001

Customer : _____
Customer 's Part No. : _____
Approval No. : _____
Issue Date : _____

*Contents in this sheet are subject to change without prior notice.



品名：**RFIQM0730010XB001**

History List

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Contents

1	PRODUCT OVERVIEW.....	4
1.1	External Power Amplifier Module (CQM730x-1).....	6
1.2	CQM730-1 Development Kit.....	7
2	HARDWARE SPECIFICATION.....	8
2.1	Block Diagram.....	8
2.2	Pinout Description.....	9
2.2.1	Pin Map.....	9
2.2.2	Pin Definition.....	10
2.2.3	External Power Amplifier Pin Control.....	11
2.2.4	I/O Pin Mux Table.....	11
2.3	Computing Subsystem.....	12
3	ELECTRICAL CHARACTERISTICS.....	13
3.1	Absolute Maximum Ratings.....	13
3.2	Recommended Operating Conditions.....	13
4	RADIO PERFORMANCE.....	14
4.1.1	2.4GHz Radio Performance.....	14
4.1.2	5GHz Radio Performance.....	14
5	DATA RATE.....	16
6	POWER CONSUMPTION.....	17
6.1	Active Power.....	17
6.1.1	2.4GHz Active Power Consumption.....	17
6.1.2	5GHz Active Power Consumption.....	18
6.2	Sleep Power.....	19
7	ANTENNA ELECTRICAL SPECIFICATIONS.....	20
7.1	Measurement Information.....	20
7.1.1	Experimental Setup.....	20
7.1.2	Antenna Solution Detail.....	21
7.2	Antenna Characteristics: Passive and Active.....	21
7.2.1	VSWR.....	21
7.2.2	Antenna Efficiency and Peak Gain.....	22
7.2.3	3D Pattern & 3 views of antenna & 2D Radiation.....	25
8	MECHANICAL SPECIFICATION.....	28
8.1	External Power Amplifier Module (CQM730x-1).....	28
8.1.1	PCB Antenna.....	28
9	MANUFACTURING RECOMMENDATION.....	30
9.1	Power Layout Guideline.....	30
9.2	RF Layout Guideline.....	30
9.3	Soldering Recommendations.....	31
10	PACKAGING.....	32
11	REGULATORY COMPLIANCE.....	33
	REVISION HISTORY.....	34

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1 Product Overview

Powered by Qualcomm game changing micro walt ultra-low power Wi-Fi technology, Qualcomm QCC730 IoT connectivity LGA module portfolio ("CQM730-1") is purposely-designed to pack processing capabilities, embedded memory, Wi-Fi connectivity, on-module flash, and most of all ultra-low power technology into a single 32-pin LGA form factor with flexible choices of Pin, PCB and U.FL antenna. All variant antenna modules are pin compatible, allowing easy swap among modules. Its 1.27mm (0.05") pitch design enables seamless LGA pad into standard 1.27mm (0.05") header conversion to allow field replaceable header module portfolio if needed. Its compact size and on-chip SRAM and RRAM (NVM) contribute to reduced costs and enhanced performance, making it an attractive choice for space- constrained IoT devices requiring battery power.

Unlike many other Wi-Fi modules on the market, CQM730-1 has integrated 60MHz Arm Cortex-M4 processor with on-chip 640KB SRAM and 1.5MB RRAM to be capable of handling all protocol stacks all the way to cloud level. It has built-in Resistive RAM (RRAM), the industry latest Non-volatile Memory (NVM) technology, eliminating need for externally attached NOR flash for more streamlined and cost- effective system. Furthermore, CQM730-1 portfolio can be powered directly by a battery with only 1.8V input, making it suitable for battery-operated devices.

CQM730-1 has a variant supporting external power amplifier both on 2.4GHz and 5GHz to extend Wi-Fi range for an improved coverage.

CQM730-1 can operate in hostless mode, capable of running both the protocol stack and applications without requiring an external MCU. It can also be used as a Wi-Fi transceiver in hosted mode, offloading all the protocol stack to enable the external MCU to concentrate on IoT applications as well as managing system power consumption.

CQM730-1 has undergone rigorous regulatory compliance testing and is certified with FCC, CE, IC, UKCA, RCM, MIC, KC, SRRC and environmentally compliant with RoHS/WEEE directives.

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CQM730-1 includes the following configurations:

Module	Form Factor	Antenna
CQM730i-1B	12.28 x 19.82 x 2.2 mm, 1.27 mm pitch, 32-pin, LGA, iPA	PCB Antenna
CQM730i-1U	12.28 x 14.82 x 2.2 mm, 1.27 mm pitch, 32-pin, LGA, iPA	U.FL Antenna
CQM730x-1B	12.28 x 22.00 x 2.2 mm, 1.27 mm pitch, 32-pin, LGA, xPA	PCB Antenna
CQM730x-1U	12.28 x 18.00 x 2.2 mm, 1.27 mm pitch, 32-pin, LGA, xPA	U.FL Antenna

CQM730-1 is graphically illustrated below:

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1.1 External Power Amplifier Module (CQM730x-1)

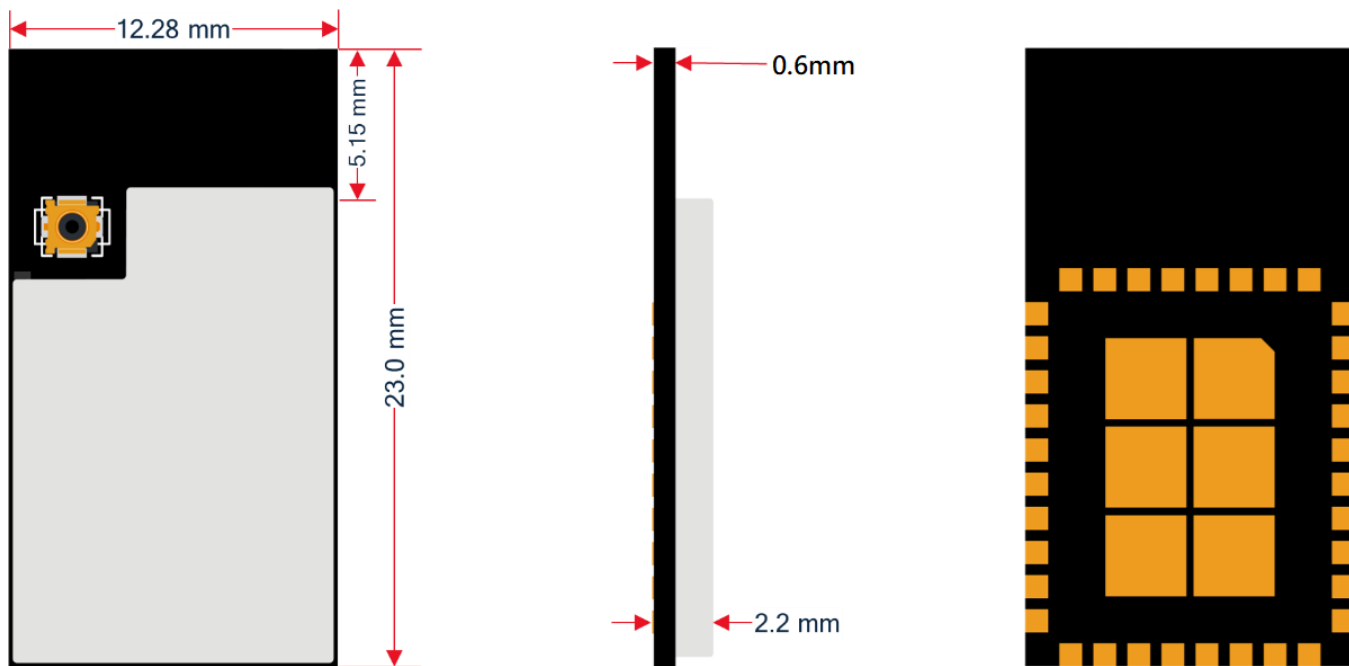


Figure 1 CQM730x-1B Module View

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1.2 CQM730-1 Development Kit

The CQM730-1 specific development kit to cover both internal PA module (CQM730i-1) and externa PA module (CQM45x-1) is provided to facilitate application software development as shown below:

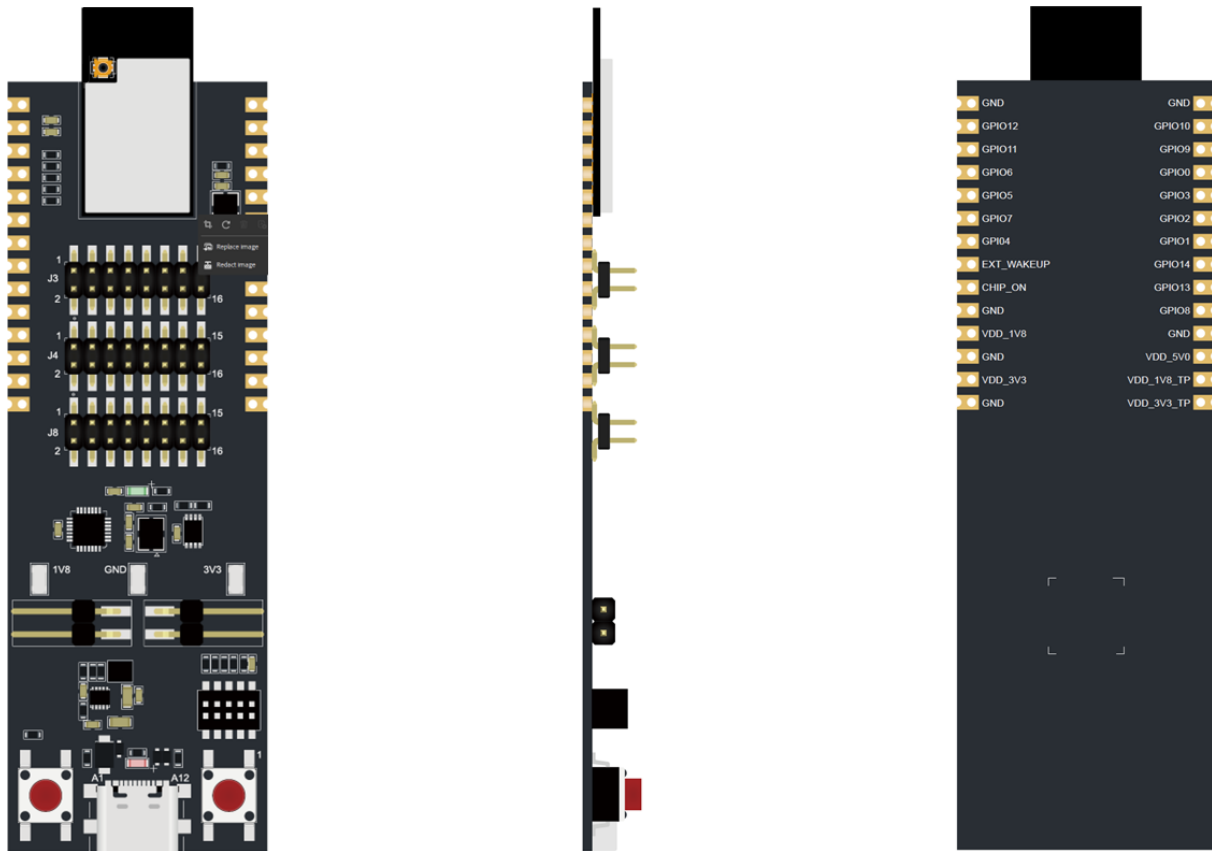


Figure 2: CQM730-1 Module Development Kit

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2 Hardware Specification

This section provides detailed hardware design and specification of CQM730-1. CQM730-1 hardware design has been optimized for small footprint and reduced RBOM cost.

2.1 Block Diagram

CQM730-1 integrates 2/4/8MB 2x3 8-USON NOR flash as stuffing options. The design also supports PCB antenna (CQM730-1B) or simply provides antenna pin (CQM730-1U) to allow customized antenna implementation on motherboard.

QCC730 module with external PA (CQM730x-1) block diagram is shown below.

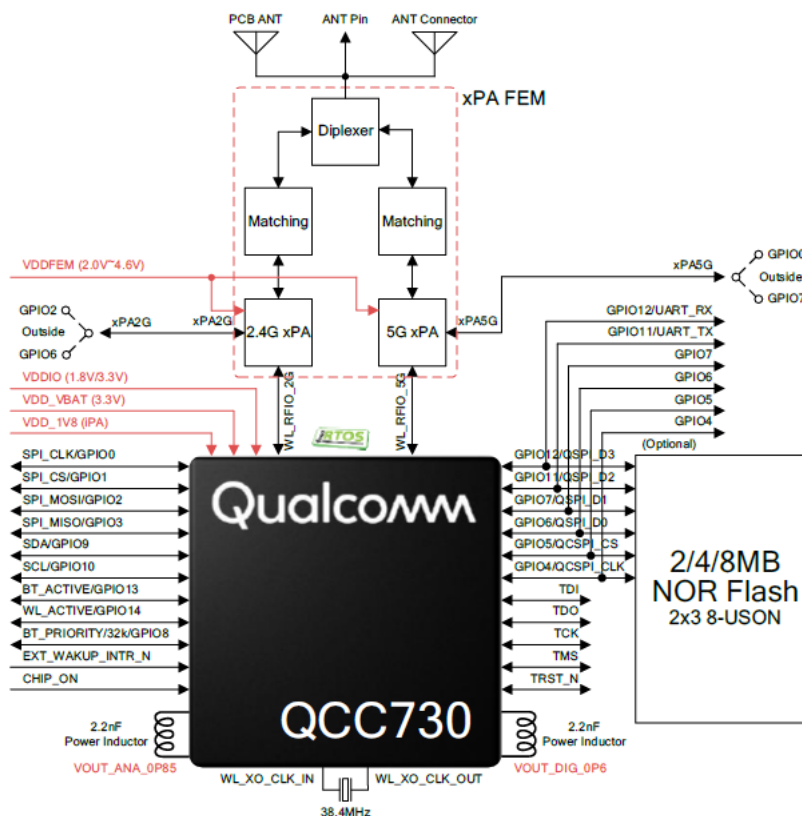


Figure 3: CQM730x-1 Module Block Diagram

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2.2 Pinout Description

In order to maintain pin compatible to allow easy drop replacement, All CQM730-1 shares the same pin map as illustrated below. CQM730x-1 will have Pin 4 as VDD_FEM to allow power supply for external power amplifier as well as Pin 2 for xPA2G (2.4GHz external PA) and Pin 3 for xPA5G (5GHz external PA) control.

2.2.1 Pin Map

CQM703-1 internal PA and external PA module pin map is listed below. Since pin antenna and U.FL antenna share the same pin definition, they are grouped together.

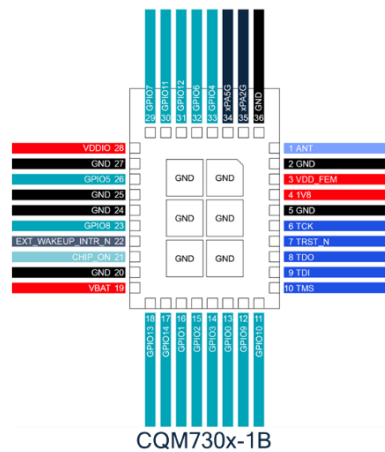


Figure 4: CQM730x-1 Module Pin Map

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2.2.2 Pin Definition

Pin definition is listed in the table below:

Pin	Pin Name	Type	Power Domain	Description
19	VBAT	PWR	-	Power input (1.8~3.6V)
28	VDDIO	PWR	-	I/O voltage input 1.8V or 3.3V
4	1V8	PWR	-	Internal PA power input
3	VDD_FEM	PWR	-	External PA power input
1	ANT or N/C	AI/AO	-	Pin antenna (1U) or N/C (1B/1U)
35	xPA2G	DI	VDDIO	2.4GHz xPA control
34	xPA5G	DI	VDDIO	5GHz xPA control
2,20,24,25,27,36	GND	GND	GND	Ground
22	EXT_WAKEUP	DI	-	External interrupt input
21	CHIP_ON	DI	-	Chip power on
7	TRST_N	DI	-	JTAG
8	TDO	DO	-	JTAG
9	TDI	DI	-	JTAG
10	TMS	DI	-	JTAG
6	TCK	DI	-	JTAG
13	GPIO0	DI/DO	VDDIO	Generic PIO
16	GPIO1	DI/DO	VDDIO	Generic PIO
15	GPIO2	DI/DO	VDDIO	Generic PIO
14	GPIO3	DI/DO	VDDIO	Generic PIO
33	GPIO4	DI/DO	VDDIO	Generic PIO
26	GPIO5	DI/DO	VDDIO	Generic PIO
32	GPIO6	DI/DO	VDDIO	Generic PIO
29	GPIO7	DI/DO	VDDIO	Generic PIO
23	GPIO8	DI/DO	VDDIO	Generic PIO
12	GPIO9	DI/DO	VDDIO	Generic PIO
11	GPIO10	DI/DO	VDDIO	Generic PIO
30	GPIO11	DI/DO	VDDIO	Generic PIO
31	GPIO12	DI/DO	VDDIO	Generic PIO
18	GPIO13	DI/DO	VDDIO	Generic PIO
17	GPIO14	DI/DO	VDDIO	Generic PIO

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2.2.3 External Power Amplifier Pin Control

xPA2G can be controlled by GPIO0 and GPIO2 while xPA5G can be controlled by GPIO6 and GPIO7. The configuration table is shown as below:

xPA2G	xPA5G	Description
GPIO2	GPIO0	No SPI interface
GPIO6	GPIO7	No QSPI (NOR Flash) support

2.2.4 I/O Pin Mux Table

GPIO	QSPI	SPI	I2C	UART	BT	PTA	xPA	FEM	Switch ANT	32k Oscillator
GPIO0		SPI_CLK					xPA5G			
GPIO1		SPI_CS		UART_RX				FEM_ENB5G	ANT_SWITCH_STATUS	
GPIO2		SPI_MOSI					xPA2G			
GPIO3		SPI_MISO		UART_TX				FEM_ENB2G		
GPIO4	QSPI_CLK							FEM_ENB2G	ANT_SWITCH_CTRL	
GPIO5 (AON)	QSPI_CS							FEM_ENB5G	ANT_SWITCH_STATUS	
GPIO6	QSPI_D0						xPA2G			
GPIO7	QSPI_D1						xPA5G			
GPIO8						BT_PRIORITY			ANT_SWITCH_CTRL	32k_IN
GPIO9 (AON)			SDA	UART_TX						
GPIO10			SCL	UART_RX						
GPIO11	QSPI_D2			UART_TX						
GPIO12	QSPI_D3			UART_RX						
GPIO13				UART_TX	WSI_CLK_BT	BT_ACTIVE				
GPIO14				UART_RX	WSI_DATA_BT	WL_ACTIVE				

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2.3 Computing Subsystem

CQM730-1 have Qualcomm QCC730 at his core which integrates Arm Cortex-M3 @60MHz, 1.5MB RRAM and 640KB SRAM as well as on-module NOR flash with size up to 32MB. The code can be executed from flash via Execution-in-Place (XiP).

The computing subsystem software architecture is shown below:

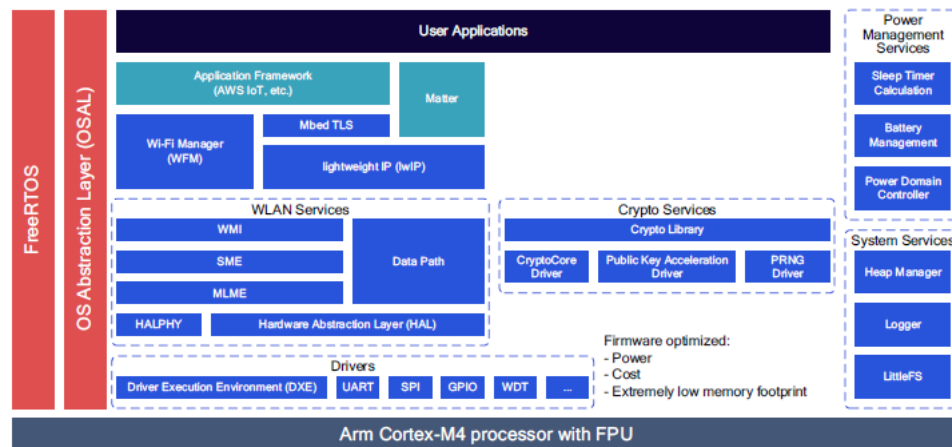


Figure 5: Computing Subsystem Software Architecture

Software SDK will be open sourced at GitHub with blocks in dot line will be offered in binary format inside software SDK package.

GitHub Download Link: To be provided later

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3 Electrical Characteristics

3.1 Absolute Maximum Ratings

The absolute maximum ratings provided in this section reflect the stress levels that, if exceeded, may cause permanent damage to the device. No functionality is guaranteed outside the operating specifications. Functionality and reliability are only guaranteed within the operating.

Pin	Parameter	Min	Max	Unit
VBATT	Power input voltage	-0.3	4.0	V
1V8	Internal PA voltage	-0.3	1.98	V
VDDIO	I/O port voltage	-	VDD+0.03	V
VDD_FEM	External PA voltage	2.0	4.6	V
Tstore	Storage temperature	-55	150	°C

3.2 Recommended Operating Conditions

Pin	Parameter	Min	TYP	Max	Unit
VBATT	Power input voltage	-	3.3	-	V
1V8	Internal PA voltage	1.71	1.8	1.89	V
VDDIO 1.8V	I/O port voltage	1.71	1.8	1.89	V
VDDIO 3.3V	I/O port voltage	-0.3	-	VDD+0.3	V
VDD_FEM	External PA voltage	2.0	3.3	4.6	V

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4 Radio Performance

The radio performance for both max transmit power and receive sensitivity on both 2.4GHz and 5GHz band is listed in the table below:

4.1.1 2.4GHz Radio Performance

Standard	Channel BW	PHY	Modulation	MCS Index	Data Rate (Mbps)	Max Tx Power (dBm)		Rx Sensitivity (dBm)	
						iPA	xPA	iPA	xPA
802.11b	22MHz	CCK (DSSS)	DBPSK	-	1				
		CCK (DSSS)	DQPSK	-	2				
		CCK (DSSS)	B/DQPSK	-	5.5				
		CCK (DSSS)	Q/DQPSK	-	11				
802.11g	20MHz	OFDM	BPSK	-	6				
		OFDM	BPSK	-	9				
		OFDM	QPSK	-	12				
		OFDM	QPSK	-	18				
		OFDM	16QAM	-	24				
		OFDM	16QAM	-	36	Not Supported			
		OFDM	64QAM	-	48	Not Supported			
		OFDM	64QAM	-	54	Not Supported			
802.11n	20MHz (HT20)	OFDM	BPSK	MCS0	7.2				
		OFDM	QPSK	MCS1	14.4				
		OFDM	QPSK	MCS2	21.7				
		OFDM	16QAM	MCS3	28.9				
		OFDM	16QAM	MCS4	43.3	Not Supported			
		OFDM	64QAM	MCS5	57.8	Not Supported			
		OFDM	64QAM	MCS6	65	Not Supported			
		OFDM	64QAM	MCS7	72.2	Not Supported			

4.1.2 5GHz Radio Performance

Standard	Channel BW	PHY	Modulation	MCS Index	Data Rate (Mbps)	Max Tx Power (dBm)		Rx Sensitivity (dBm)	
						iPA	xPA	iPA	xPA
802.11a	20MHz	OFDM	BPSK	-	6				
		OFDM	BPSK	-	9				
		OFDM	QPSK	-	12				
		OFDM	QPSK	-	18				
		OFDM	16QAM	-	24				

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Standard	Channel BW	PHY	Modulation	MCS Index	Data Rate (Mbps)	Max Tx Power (dBm)		Rx Sensitivity (dBm)	
						iPA	xPA	iPA	xPA
		OFDM	16QAM	-	36	Not Supported			
		OFDM	64QAM	-	48	Not Supported			
		OFDM	64QAM	-	54	Not Supported			
802.11n	20MHz (HT20)	OFDM	BPSK	MCS0	7.2				
		OFDM	QPSK	MCS1	14.4				
		OFDM	QPSK	MCS2	21.7				
		OFDM	16QAM	MCS3	28.9				
		OFDM	16QAM	MCS4	43.3	Not Supported			
		OFDM	64QAM	MCS5	57.8	Not Supported			
		OFDM	64QAM	MCS6	65	Not Supported			
		OFDM	64QAM	MCS7	72.2	Not Supported			

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5 Data Rate

Since CQM730-1 can support up to MCS3 on 802.11n. The max data rate at UDP/TCP level is listed below:

PAMPDU	Protocol	Rate	MCS Index	Traffic	Throughput	KPI %
No	UDP	28.9 Mbps	MCS3	DL	16.63 Mbps	92.39%
	UDP	28.9 Mbps	MCS3	UL	17.72 Mbps	98.44%
	TCP	28.9 Mbps	MCS3	DL	13.97 Mbps	93.13%
	TCP	28.9 Mbps	MCS3	UL	14.03 Mbps	93.54%
Yes	UDP	28.9 Mbps	MCS3	DL	23.5 Mbps	98.54%
	UDP	28.9 Mbps	MCS3	UL	22.58 Mbps	94.08%
	TCP	28.9 Mbps	MCS3	DL	16.02 Mbps	94.24%
	TCP	28.9 Mbps	MCS3	UL	16.17 Mbps	95.12%

UNLESS OTHER SPECIFIED TOLERANCES ON :
 X = N/A X.X = N/A X.XX = N/A
 ANGLES = N/A HOLEDIA = N/A



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SCALE : N/A

UNIT : mm

DRAWN BY : 詹惠雯

CHECKED BY : 詹惠雯

DESIGNED BY : 黃瑞郎

APPROVED BY : 陳振榮

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6 Power Consumption

CQM730-1 can operate in four power states as shown in the table below:

Power Mode	CPU	MEM	RTC	Wakeup Source	Description	Use Case
Active	On	On	On	N/A	Normal operation	Awake
Sleep	Off	Retained	Off	DTIM timer	- Enter When RTOS is idle, and WLAN is connected* - Maintain WLAN connection - When awake, continue to run from sleep point	WLAN DTIM
Deep Sleep	Off	Off	Off	RTC timer or EXT_WAKEUP_INTR_N	- Enter when RTOS is idle, and WLAN is disconnected* - Nothing maintained - When awake, restart from PBL	Alarm or Shutdown
Indefinite Sleep	Off	Off	Off	EXT_WAKEUP_INTR_N	Same as Deep Sleep, except not waked up by RTC timer	Shutdown

Note: Actual entering condition is complicated, such as considering sleep interval

6.1 Active Power

The active power consumption for both continuous transmitting and receiving on both 2.4GHz and 5GHz band is listed in the table below:

6.1.1 2.4GHz Active Power Consumption

Standard	Channel BW	PHY	Modulation	MCS Index	Data Rate (Mbps)	Continuous Transmit (mW)		Continuous Receive (mW)	
						iPA	xPA	iPA	xPA
802.11b	22MHz	CCK (DSSS)	DBPSK	-	1				
		CCK (DSSS)	DQPSK	-	2				
		CCK (DSSS)	B/DQPSK	-	5.5				
		CCK (DSSS)	Q/DQPSK	-	11				
802.11g	20MHz	OFDM	BPSK	-	6				
		OFDM	BPSK	-	9				
		OFDM	QPSK	-	12				
		OFDM	QPSK	-	18				
		OFDM	16QAM	-	24				
		OFDM	16QAM	-	36	Not Supported			
		OFDM	64QAM	-	48	Not Supported			
		OFDM	64QAM	-	54	Not Supported			
802.11n	20MHz (HT20)	OFDM	BPSK	MCS0	7.2				
		OFDM	QPSK	MCS1	14.4				
		OFDM	QPSK	MCS2	21.7				

UNLESS OTHER SPECIFIED TOLERANCES ON :
X=N/A X.X=N/A X.XX=N/A
ANGLES=N/A HOLEDIA=N/A



INPAQ TECHNOLOGY CO., LTD.

SCALE : N/A

UNIT : mm

DRAWN BY : 詹惠雯

CHECKED BY : 詹惠雯

DESIGNED BY : 黃瑞郎

APPROVED BY : 陳振榮

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Standard	Channel BW	PHY	Modulation	MCS Index	Data Rate (Mbps)	Continuous Transmit (mW)		Continuous Receive (mW)	
802.11n	20MHz (HT20)	OFDM	16QAM	MCS3	28.9				
		OFDM	16QAM	MCS4	43.3	Not Supported			
		OFDM	64QAM	MCS5	57.8	Not Supported			
		OFDM	64QAM	MCS6	65	Not Supported			
		OFDM	64QAM	MCS7	72.2	Not Supported			

Note: Vbatt = 3.0V

6.1.2 5GHz Active Power Consumption

Standard	Channel BW	PHY	Modulation	MCS Index	Data Rate (Mbps)	Continuous Transmit (mW)		Continuous Receive (mW)	
						iPA	xPA	iPA	xPA
802.11a	20MHz	OFDM	BPSK	-	6				
		OFDM	BPSK	-	9				
		OFDM	QPSK	-	12				
		OFDM	QPSK	-	18				
		OFDM	16QAM	-	24				
		OFDM	16QAM	-	36	Not Supported			
		OFDM	64QAM	-	48	Not Supported			
		OFDM	64QAM	-	54	Not Supported			
802.11n	20MHz (HT20)	OFDM	BPSK	MCS0	7.2				
		OFDM	QPSK	MCS1	14.4				
		OFDM	QPSK	MCS2	21.7				
		OFDM	16QAM	MCS3	28.9				
		OFDM	16QAM	MCS4	43.3	Not Supported			
		OFDM	64QAM	MCS5	57.8	Not Supported			
		OFDM	64QAM	MCS6	65	Not Supported			
		OFDM	64QAM	MCS7	72.2	Not Supported			

Note: Vbatt = 3.0V

UNLESS OTHER SPECIFIED TOLERANCES ON :
X = N/A X.X = N/A X.XX = N/A
ANGLES = N/A HOLEDIA = N/A



INPAQ TECHNOLOGY CO., LTD.

SCALE : N/A

UNIT : mm

DRAWN BY : 詹惠雯

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DESIGNED BY : 黃瑞郎

APPROVED BY : 陳振榮

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6.2 Sleep Power

Power Mode	CPU	Internal Power Amplifier		External Power Amplifier		Unit
		2.4GHz	5GHz	2.4GHz	5GHz	
Standby	Power Down					μA
	Deep Sleep					μA
	Listen					μA
	DTIM sleep (128KB RAM Retention)					μA
	DTIM sleep (650KB RAM Retention)					μA
DTIM	DTIM 1					μA
	DTIM 3					μA
	DTIM 5					μA
	DTIM 10					μA
	DTIM 100					μA

Note: Vbatt = 3.3V

UNLESS OTHER SPECIFIED TOLERANCES ON :
 X = N/A X.X = N/A X.XX = N/A
 ANGLES = N/A HOLEDIA = N/A



INPAQ TECHNOLOGY CO., LTD.

SCALE : N/A

UNIT : mm

DRAWN BY : 詹惠雯

CHECKED BY : 詹惠雯

DESIGNED BY : 黃瑞郎

APPROVED BY : 陳振榮

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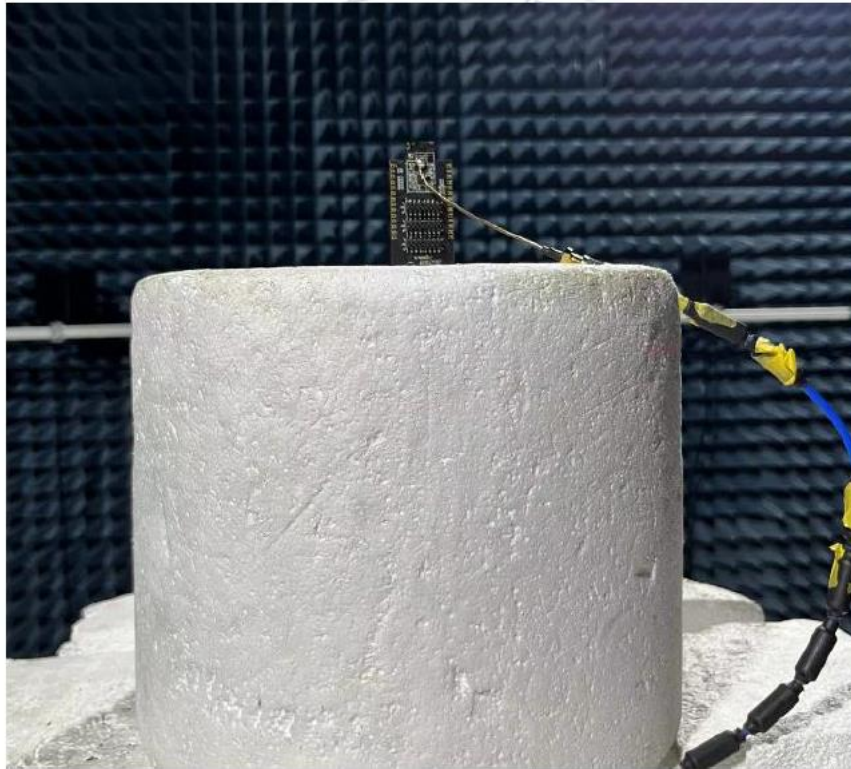
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7 Antenna Electrical specifications

7.1 Measurement Information

7.1.1 Experimental Setup



UNLESS OTHER SPECIFIED TOLERANCES ON :
X = N/A X.X = N/A X.XX = N/A
ANGLES = N/A HOLEDIA = N/A



INPAQ TECHNOLOGY CO., LTD.

SCALE : N/A

UNIT : mm

DRAWN BY : 詹惠雯

CHECKED BY : 詹惠雯

DESIGNED BY : 黃瑞郎

APPROVED BY : 陳振榮

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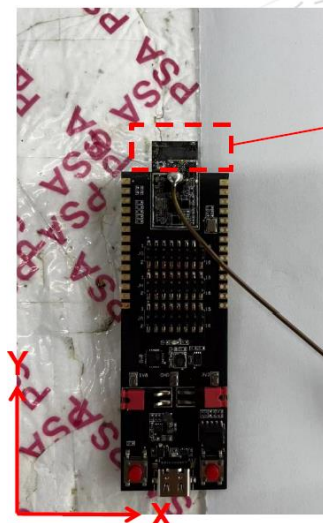
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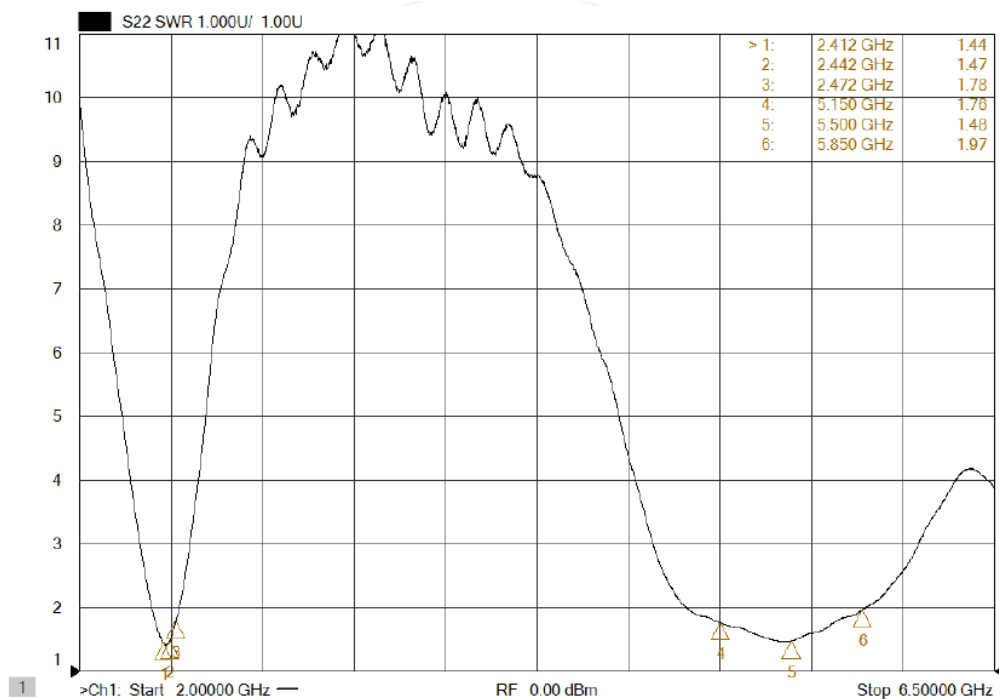
7.1.2 Antenna Solution Detail



Printed antenna design
Tiny height: 5mm
Good performance

7.2 Antenna Characteristics: Passive and Active

7.2.1 VSWR



UNLESS OTHER SPECIFIED TOLERANCES ON :
X = N/A X.X = N/A X.XX = N/A
ANGLES = N/A HOLEDIA = N/A



INPAQ TECHNOLOGY CO., LTD.

SCALE : N/A

UNIT : mm

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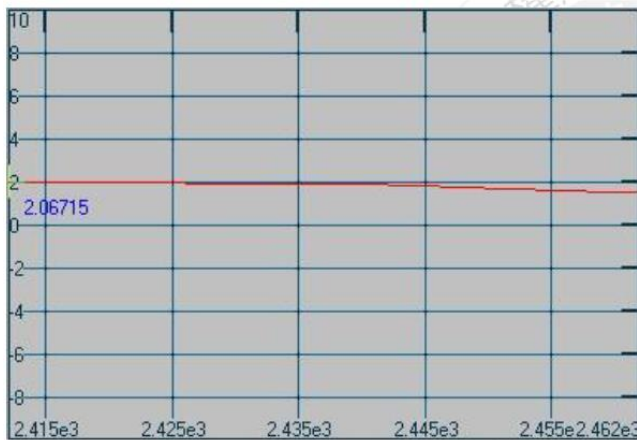
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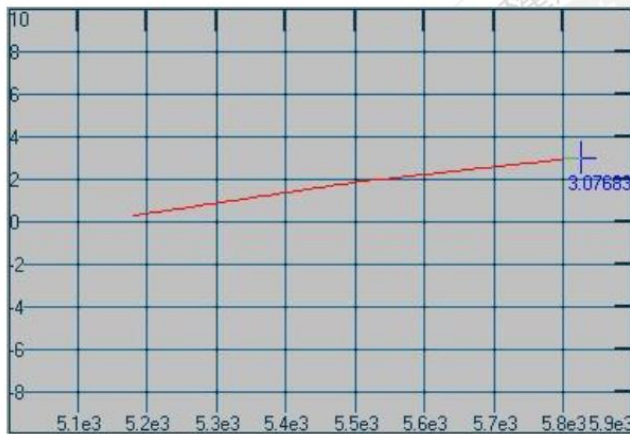
7.2.2 Antenna Efficiency and Peak Gain

2G

Maximum Peak Gain at 2412 MHz : 2.06dBi



Maximum Efficiency at 2412 MHz : 43.07%

5G

Maximum Peak Gain at 5850 MHz : 3.07dBi



Maximum Efficiency at 5850 MHz : 55.45%

UNLESS OTHER SPECIFIED TOLERANCES ON :
X = N/A X.X = N/A X.XX = N/A
ANGLES = N/A HOLEDIA = N/A

SCALE : N/A

UNIT : mm

DRAWN BY : 詹惠雯

CHECKED BY : 詹惠雯

DESIGNED BY : 黃瑞郎

APPROVED BY : 陳振榮

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ANT_2.4G

Frequency(MHz)	Efficiency(%)	Peak gain(dBi)
2412	43.08	2.07
2417	42.90	2.04
2422	42.72	2.02
2427	42.55	1.99
2432	42.37	1.97
2437	42.19	1.95
2442	42.00	1.92
2447	41.45	1.82
2452	40.95	1.73
2457	40.40	1.63
2462	39.85	1.53
2467	40.00	1.43
2472	39.69	1.34
2484	39.39	1.24

UNLESS OTHER SPECIFIED TOLERANCES ON :
X = N/A X.X = N/A X.XX = N/A
ANGLES = N/A HOLEDIA = N/A



INPAQ TECHNOLOGY CO., LTD.

SCALE : N/A

UNIT : mm

DRAWN BY : 詹惠雯

CHECKED BY : 詹惠雯

DESIGNED BY : 黃瑞郎

APPROVED BY : 陳振榮

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ANT_5G

Frequency(MHz)	Efficiency(%)	Peak gain(dBi)	Frequency(MHz)	Efficiency(%)	Peak gain(dBi)
5180	40.12	0.34	5500	49.16	1.91
5190	40.49	0.40	5510	49.42	1.96
5200	40.86	0.47	5520	49.54	1.98
5210	41.04	0.50	5530	43.62	0.95
5220	41.22	0.53	5540	49.92	2.05
5230	41.59	0.60	5550	50.17	2.10
5240	41.78	0.63	5560	50.30	2.12
5260	42.33	0.72	5580	50.93	2.24
5270	42.70	0.79	5590	51.18	2.28
5280	42.89	0.82	5600	51.18	2.28
5290	43.25	0.88	5610	51.31	2.31
5300	43.44	0.92	5620	51.43	2.33
5310	43.81	0.98	5630	51.68	2.38
5320	44.18	1.04	5640	51.93	2.42

Frequency(MHz)	Efficiency(%)	Peak gain(dBi)	Frequency(MHz)	Efficiency(%)	Peak gain(dBi)
5660	52.31	2.49	5745	53.95	2.80
5670	52.44	2.52	5755	54.07	2.82
5680	52.69	2.56	5765	54.33	2.87
5690	52.82	2.59	5775	54.45	2.89
5700	53.07	2.63	5785	54.70	2.94
5710	53.19	2.66	5795	54.83	2.96
5720	53.44	2.70	5805	55.08	3.01
			5825	55.46	3.08
			5835	55.54	3.09
			5845	55.75	3.13
			5855	55.95	3.17
			5865	56.16	3.21
			5875	56.37	3.25
			5885	56.57	3.28

UNLESS OTHER SPECIFIED TOLERANCES ON :
X = N/A X.X = N/A X.XX = N/A
ANGLES = N/A HOLEDIA = N/A



INPAQ TECHNOLOGY CO., LTD.

SCALE : N/A

UNIT : mm

DRAWN BY : 詹惠雯

CHECKED BY : 詹惠雯

DESIGNED BY : 黃瑞郎

APPROVED BY : 陳振榮

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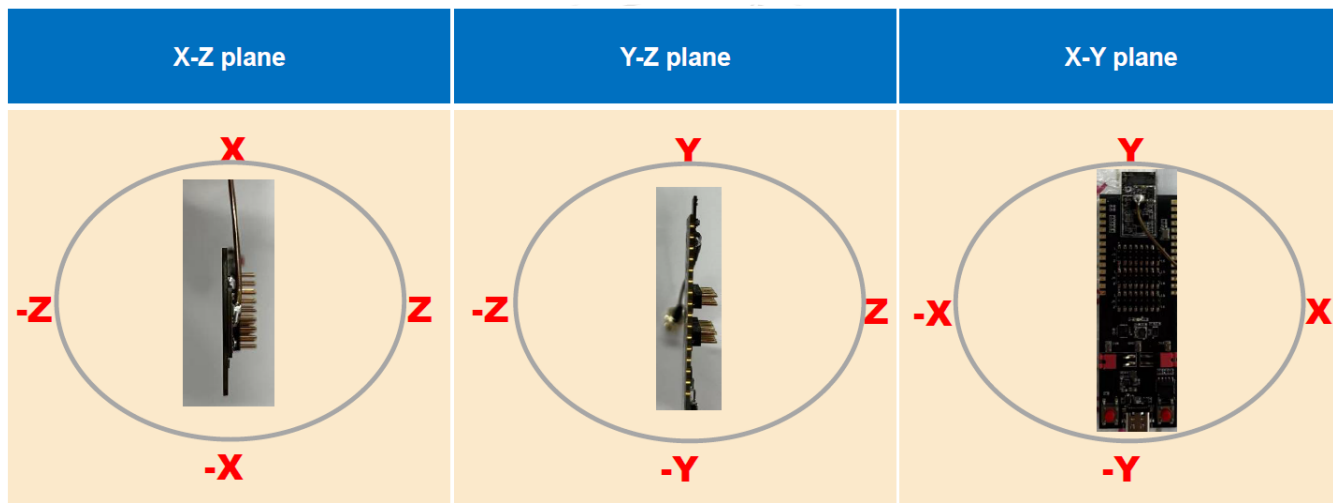
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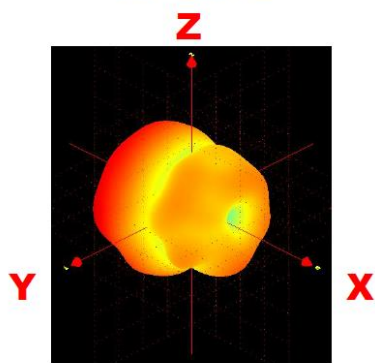
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7.2.3 3D Pattern & 3 views of antenna & 2D Radiation

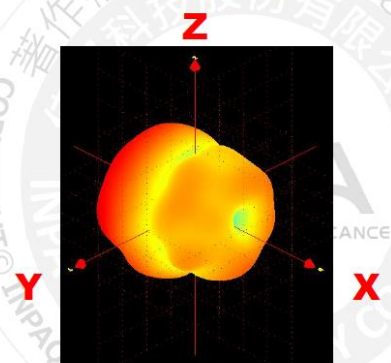


2412MHz



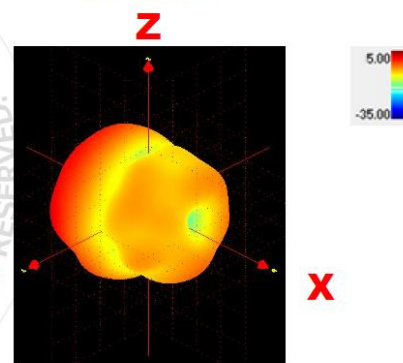
Peak gain 2.06dBi
Ave. gain -3.66dBi

2442MHz



Peak gain 1.92dBi
Ave. gain -3.76dBi

2462MHz



Peak gain 1.52dBi
Ave. gain -4.00dBi

UNLESS OTHER SPECIFIED TOLERANCES ON :
X = N/A X.X = N/A X.XX = N/A
ANGLES = N/A HOLEDIA = N/A



INPAQ TECHNOLOGY CO., LTD.

SCALE : N/A

UNIT : mm

DRAWN BY : 詹惠雯

CHECKED BY : 詹惠雯

DESIGNED BY : 黃瑞郎

APPROVED BY : 陳振榮

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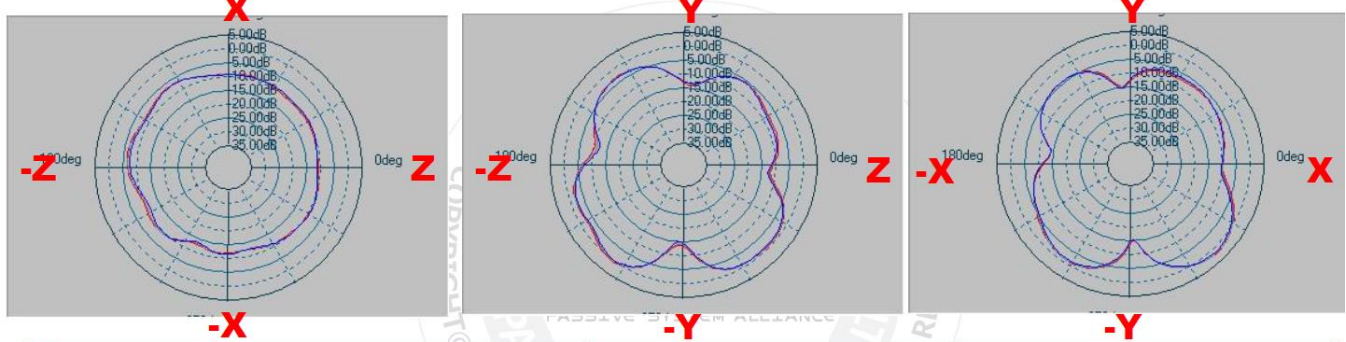
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2G

2.3 2D Radiation Patterns



	ZX plane		ZY plane		XY plane	
Frequency [MHz]	Max Value [dBi]	Average [dBi]	Max Value [dBi]	Average [dBi]	Max Value [dBi]	Average [dBi]
2412	-6.64	-9.57	0.04	-4.58	1.86	-4.61
2442	-6.91	-9.62	-0.24	-4.78	1.77	-4.68
2462	-7.51	-9.84	-0.55	-5.02	1.41	-4.97

UNLESS OTHER SPECIFIED TOLERANCES ON :
X = N/A X.X = N/A X.XX = N/A
ANGLES = N/A HOLEDIA = N/A



INPAQ TECHNOLOGY CO., LTD.

SCALE : N/A

UNIT : mm

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CHECKED BY : 詹惠雯

DESIGNED BY : 黃瑞郎

APPROVED BY : 陳振榮

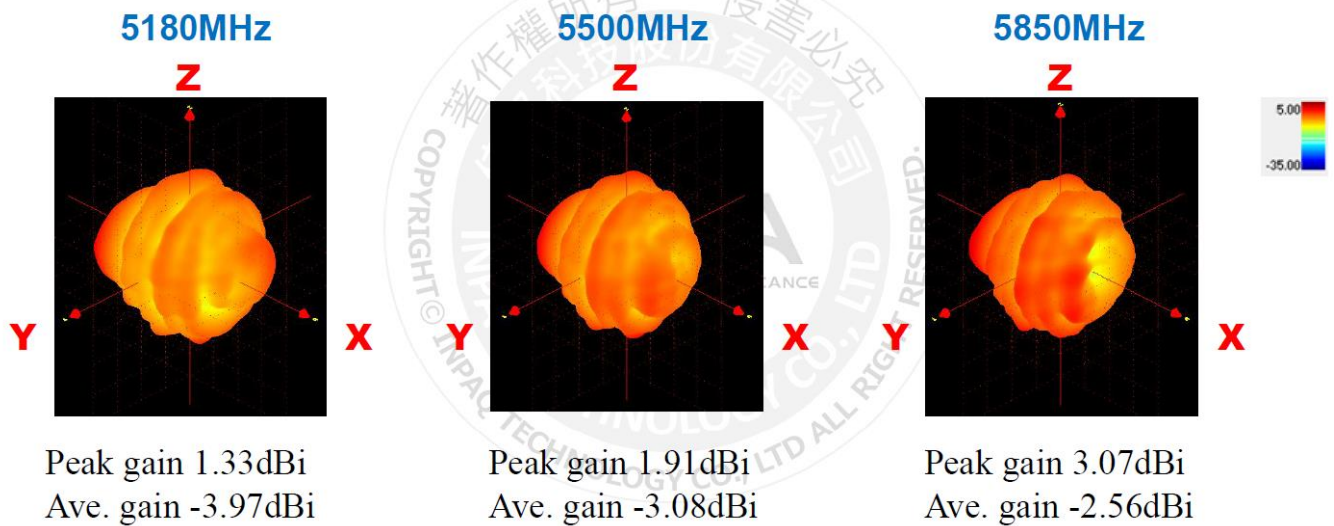
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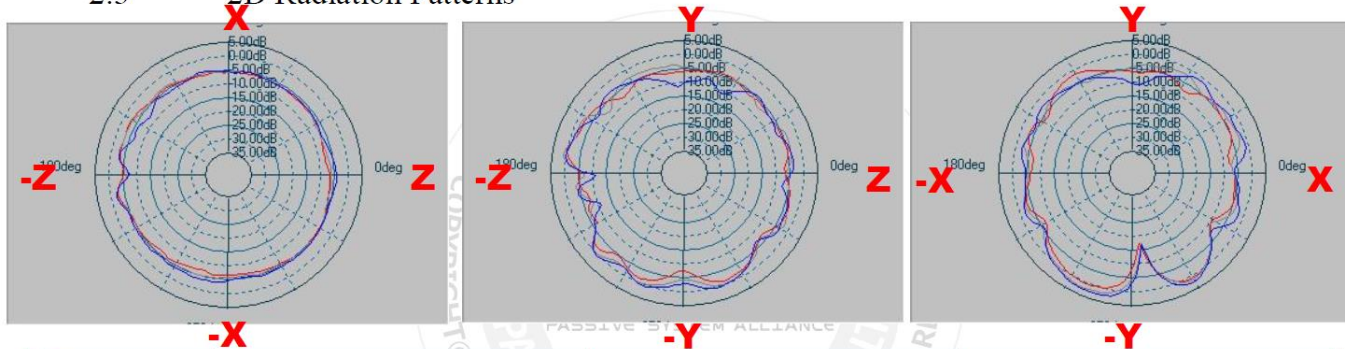
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NO.

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**5G**

2.3 2D Radiation Patterns



Frequency [MHz]	ZX plane		ZY plane		XY plane	
	Max Value [dBi]	Average [dBi]	Max Value [dBi]	Average [dBi]	Max Value [dBi]	Average [dBi]
5180	-2.89	-5.63	-0.51	-4.54	0.81	-4.05
5500	-2.46	-5.29	0.57	-3.57	1.87	-3.09
5850	-2.32	-5.20	0.36	-3.78	2.45	-2.45

UNLESS OTHER SPECIFIED TOLERANCES ON :
X = N/A X.X = N/A X.XX = N/A
ANGLES = N/A HOLEDIA = N/A

**INPAQ TECHNOLOGY CO., LTD.**

SCALE : N/A

UNIT : mm

DRAWN BY : 詹惠雯

CHECKED BY : 詹惠雯

DESIGNED BY : 黃瑞郎

APPROVED BY : 陳振榮

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8 Mechanical Specification

8.1 External Power Amplifier Module (CQM730x-1)

8.1.1 PCB Antenna

8.1.1.1 Dimension

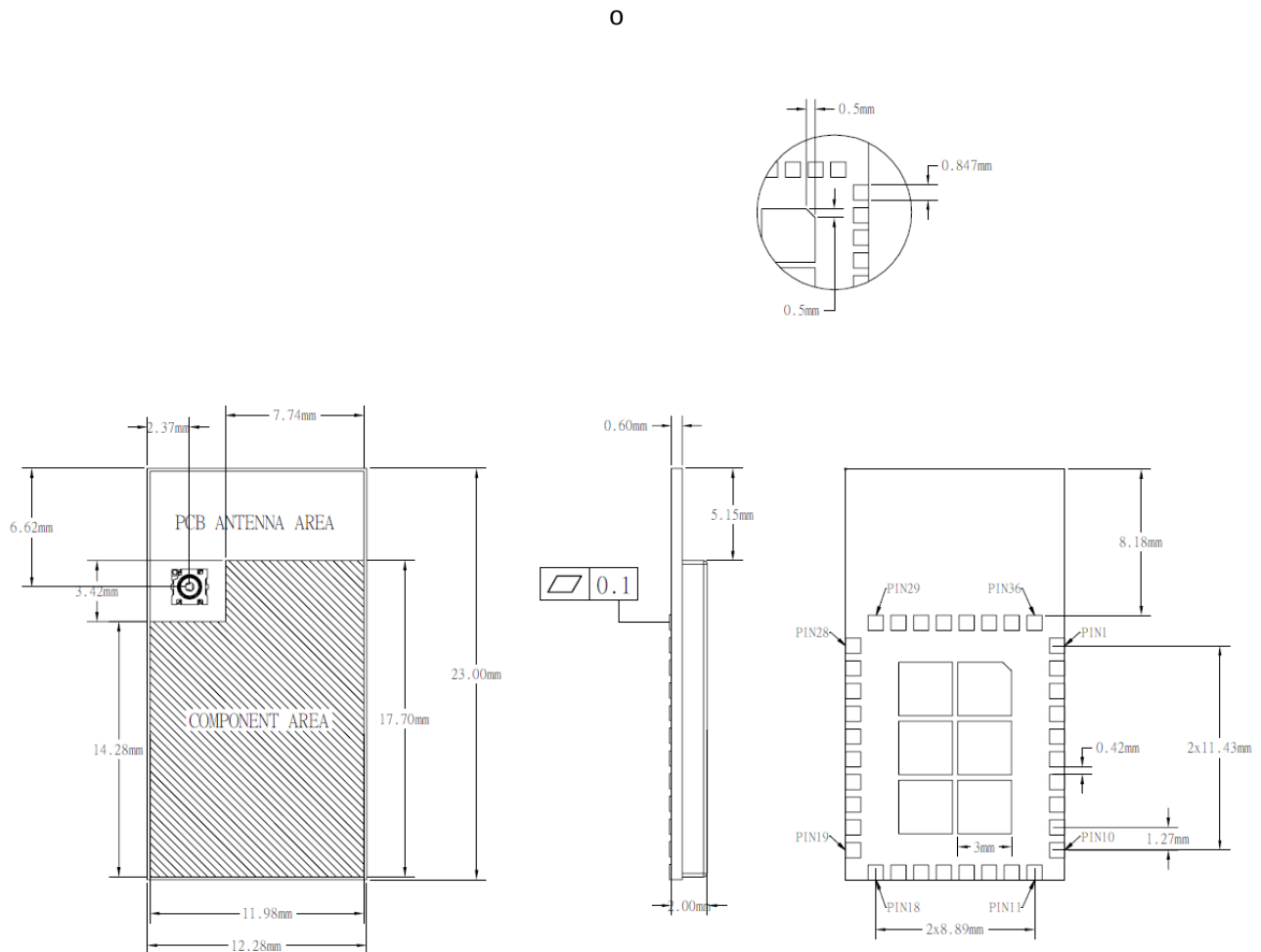


Figure 6: CQM730x-1B Dimension

UNLESS OTHER SPECIFIED TOLERANCES ON :
X = N/A X.X = N/A X.XX = N/A
ANGLES = N/A HOLEDIA = N/A



INPAQ TECHNOLOGY CO., LTD.

SCALE : N/A

UNIT : mm

DRAWN BY : 詹惠雯

CHECKED BY : 詹惠雯

DESIGNED BY : 黃瑞郎

APPROVED BY : 陳振榮

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8.1.1.2 Recommended PCB Landing Pattern

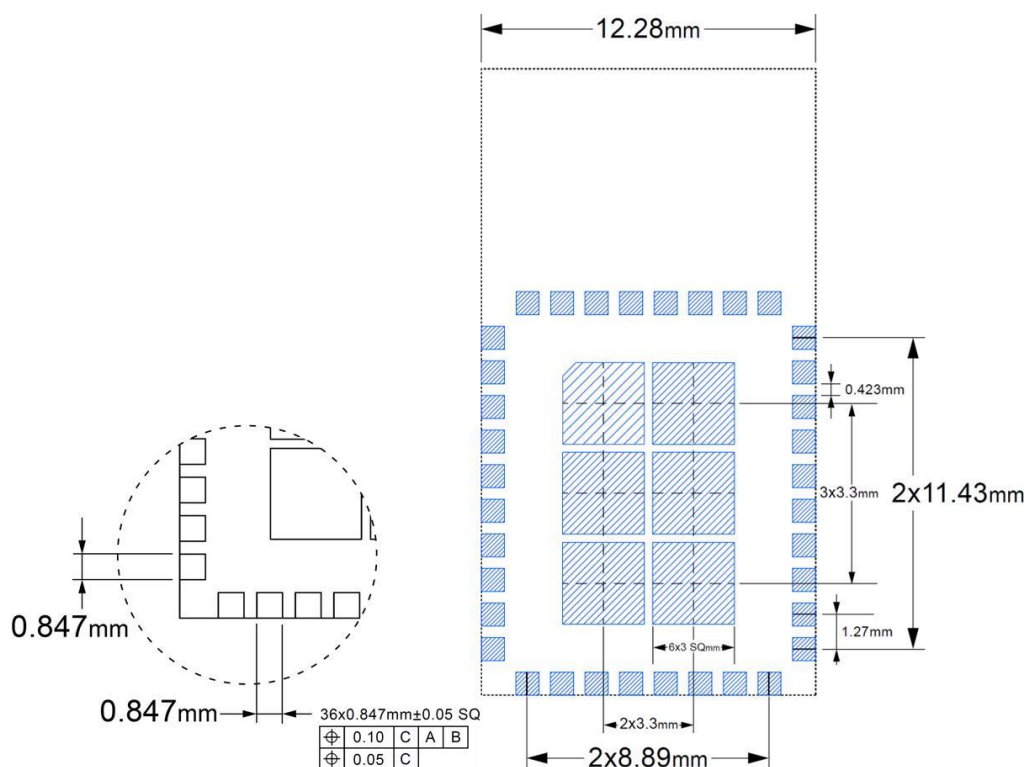


Figure 7: CQM730x-1P PCB Landing Pattern

UNLESS OTHER SPECIFIED TOLERANCES ON :
X = N/A X.X = N/A X.XX = N/A
ANGLES = N/A HOLEDIA = N/A



INPAQ TECHNOLOGY CO., LTD.

SCALE : N/A

UNIT : mm

DRAWN BY : 詹惠雯

CHECKED BY : 詹惠雯

DESIGNED BY : 黃瑞郎

APPROVED BY : 陳振榮

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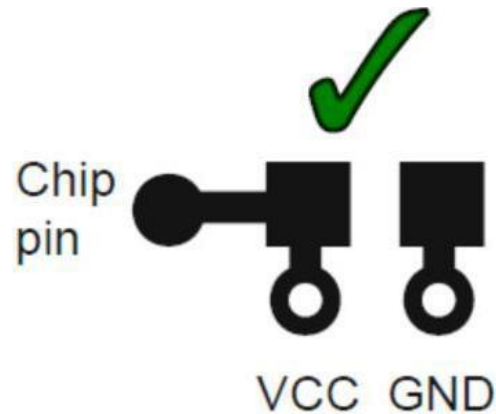
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9 Manufacturing Recommendation

9.1 Power Layout Guideline

CQM730-1 is powered by either 3V battery or DC 3.3V. Power pin connection capacitor is as close as possible to chip and pin. Decoupling the power supply from the chip using a capacitor. Use capacitors to prevent noise from coupling back to the power plane.



9.2 RF Layout Guideline

To optimize antenna performance, place the module in the corner of the PCB as shown below. Do not cover copper and trace the antenna clearance area. Keep the antenna area as far away as possible from the power supply and metal components. Connect all GND pins directly to a solid GND plane. Place GND vias as close as possible to the GND pin. Use a good layout method to avoid excessive noise coupling with signal lines or supply voltage lines.

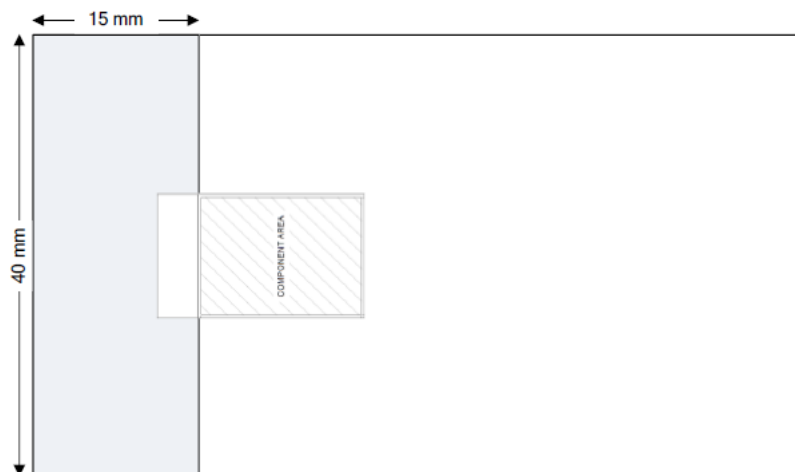


Figure 8: RF Layout Guideline

UNLESS OTHER SPECIFIED TOLERANCES ON : X=N/A X.X=N/A X.XX=N/A ANGLES=N/A HOLEDIA=N/A			INPAQ TECHNOLOGY CO., LTD.	
SCALE : N/A	UNIT : mm		THIS DRAWINGS AND SPECIFICATIONS ARE THE PROPERTY OF INPAQ TECHNOLOGY CO.,LTD.AND SHALL NOT BE REPRODUCED OR USED AS THE BASIS FOR THE MANUFACTURE OR SALE OF APPARATUS OR DEVICES WITHOUT PERMISSION	
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DESIGNED BY : 黃瑞郎	APPROVED BY : 陳振榮			
TITLE : RFIQM0730010XB001		DOCUMENT NO.	ENS070033800-000803518344	SPEC REV. A0

9.3 Soldering Recommendations

CQM730-1 can be SMT on the board following the temperature curve graph:

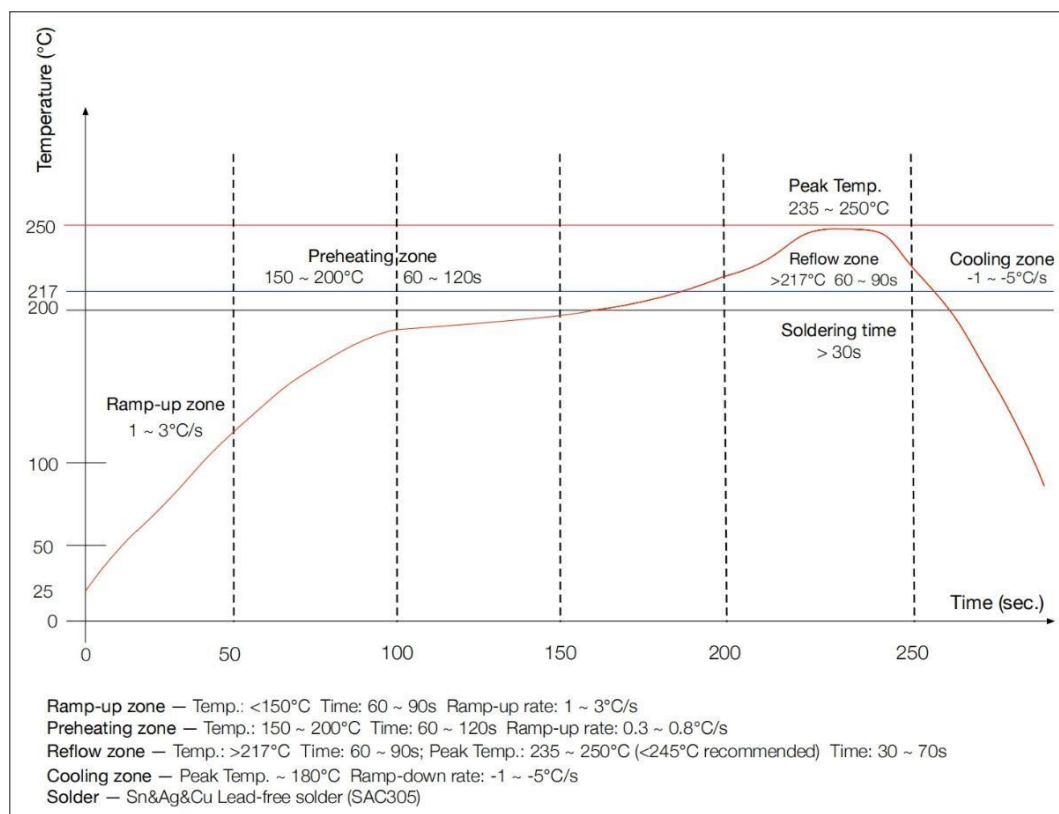


Figure 9: Soldering Guideline

UNLESS OTHER SPECIFIED TOLERANCES ON :
 X = N/A X.X = N/A X.XX = N/A
 ANGLES = N/A HOLEDIA = N/A



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CHECKED BY : 詹惠雯

DESIGNED BY : 黃瑞郎

APPROVED BY : 陳振榮

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10 Packaging

CQM730-1 are packaged on reels loaded with 1000 modules. Each reel is placed in an antistatic bag with a desiccant pack and a humidity card and placed in an 36x25x12cm box. Anti-static warnings and labels adhere to the outside of the bag.

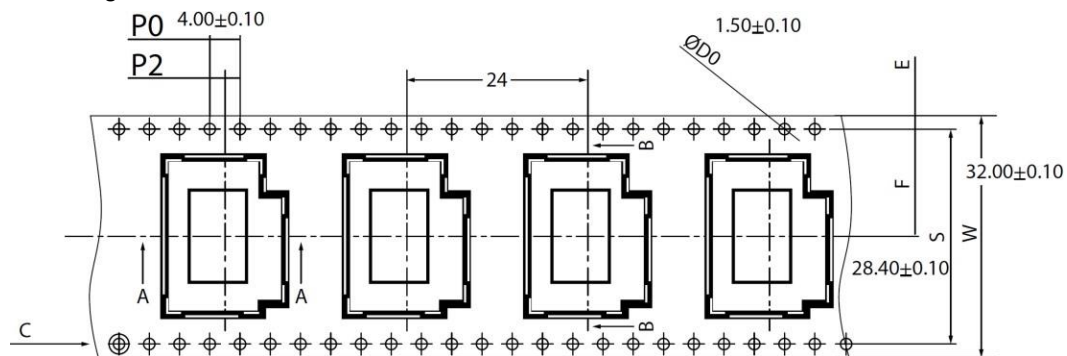


Figure 10: Module Packaging

Warning

The MeshConnect Modules contain highly sensitive electronic circuitry. Handling without proper ESD protection may destroy or damage the module permanently.

Warning

The MeshConnect Modules are moisture-sensitive devices. Appropriate handling instructions and precautions are summarized in J-STD-033. Read carefully to prevent permanent damage due to moisture intake.

Moisture Sensitivity Level (MSL)

CQM100-1 is qualified to moisture sensitivity (MSL3) in accordance with JEDEC J-STD-020

Storage

Storage/shelf life in sealed bags is 12 months at <40°C and <90% relative humidity.12.2 Packing Label

UNLESS OTHER SPECIFIED TOLERANCES ON : X=N/A X.X=N/A X.XX=N/A ANGLES=N/A HOLEDIA=N/A			INPAQ TECHNOLOGY CO., LTD.	
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DESIGNED BY : 黃瑞郎	APPROVED BY : 陳振榮			
TITLE : RFIQM0730010XB001		DOCUMENT NO.	ENS070033800-000803518344	SPEC REV. A0

11 Regulatory Compliance

Country	Certification	No
USA	FCC 15C	Coming soon
Europe Union	CE	Coming soon
Canada	IC	Coming soon
Japan	MIC	Coming soon
Korea	KC	Coming soon
Australia	RCM	Coming soon
United Kingdom	UKCA	Coming soon
China	SRRC	Coming soon

UNLESS OTHER SPECIFIED TOLERANCES ON :
 X = N/A X.X = N/A X.XX = N/A
 ANGLES = N/A HOLEDIA = N/A



INPAQ TECHNOLOGY CO., LTD.

SCALE : N/A

UNIT : mm

DRAWN BY : 詹惠雯

CHECKED BY : 詹惠雯

DESIGNED BY : 黃瑞郎

APPROVED BY : 陳振榮

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Revision History

Revision	Description	Date
1.0	Initial draft	July 1, 2024

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SCALE : N/A	UNIT : mm	THIS DRAWINGS AND SPECIFICATIONS ARE THE PROPERTY OF INPAQ TECHNOLOGY CO.,LTD.AND SHALL NOT BE REPRODUCED OR USED AS THE BASIS FOR THE MANUFACTURE OR SALE OF APPARATUS OR DEVICES WITHOUT PERMISSION	
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