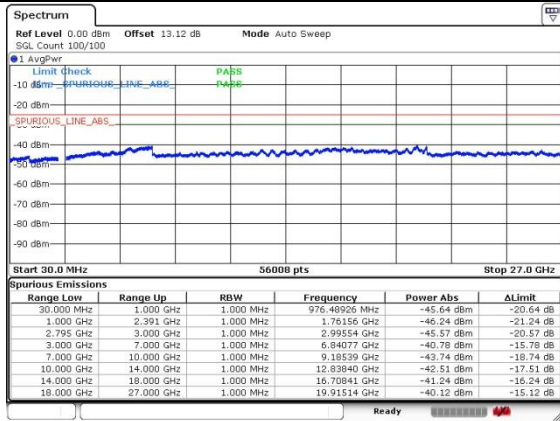




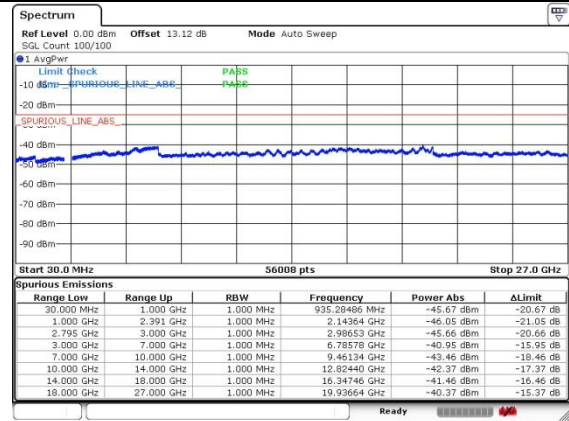
n41 50MHz

Lowest Channel / BPSK



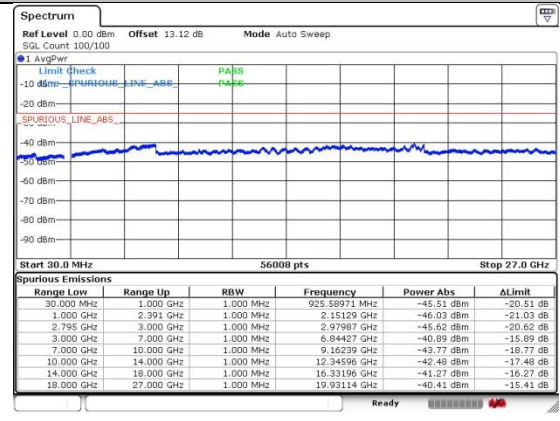
Date: 29 MAY 2020 05:28:47

Lowest Channel / QPSK



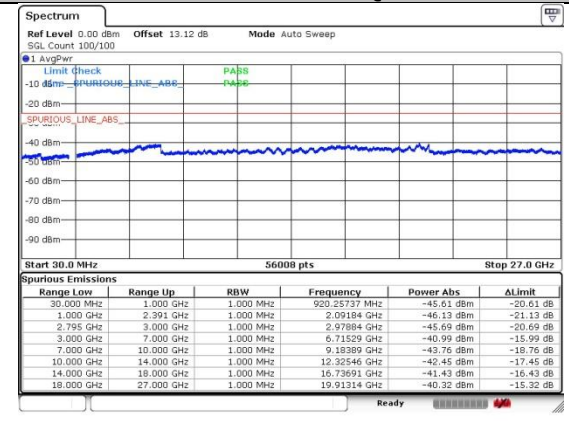
Date: 29 MAY 2020 05:32:11

Middle Channel / BPSK



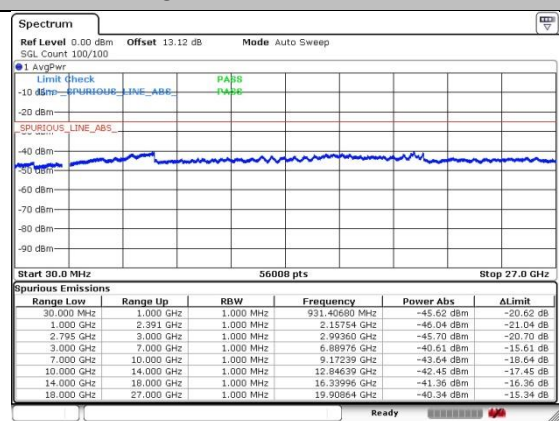
Date: 29 MAY 2020 05:30:04

Middle Channel / QPSK



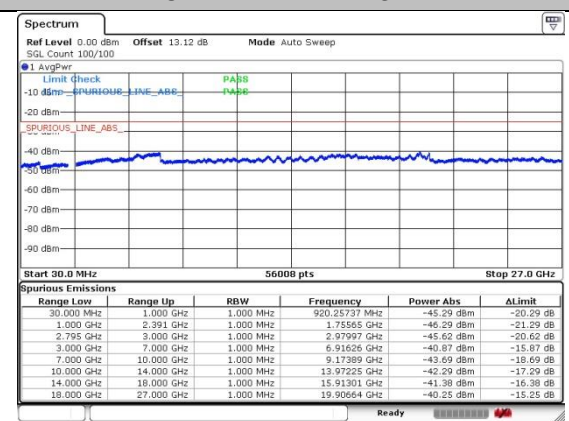
Date: 29 MAY 2020 05:33:33

Highest Channel / BPSK



Date: 29 MAY 2020 05:31:10

Highest Channel / QPSK

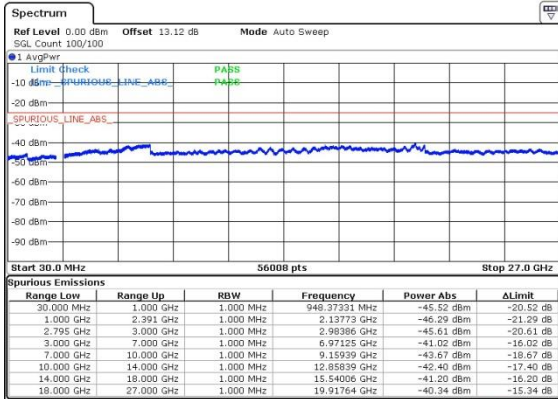


Date: 29 MAY 2020 05:34:58



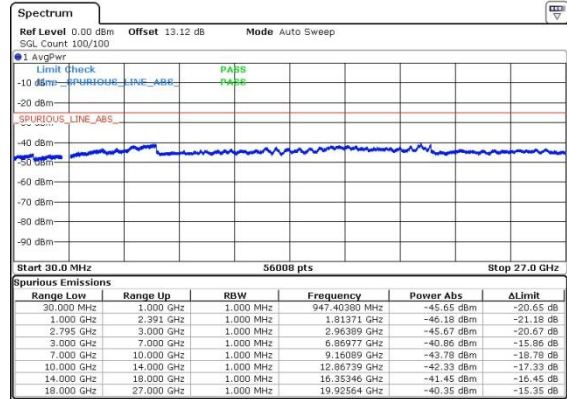
n41 50MHz

Lowest Channel / 16QAM



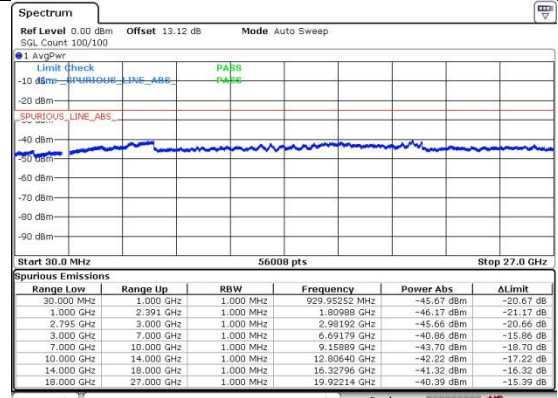
Date: 29 MAY 2020 05:35:50

Lowest Channel / 64QAM



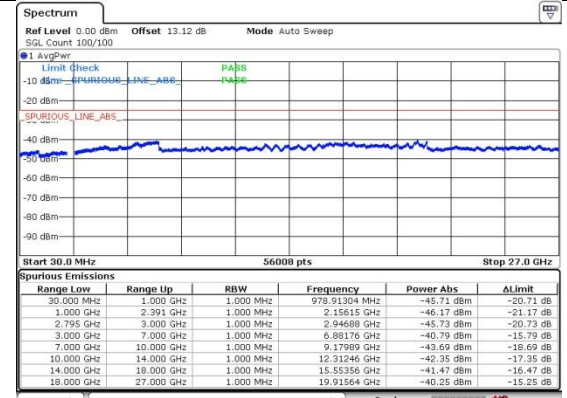
Date: 29 MAY 2020 05:39:25

Middle Channel / 16QAM



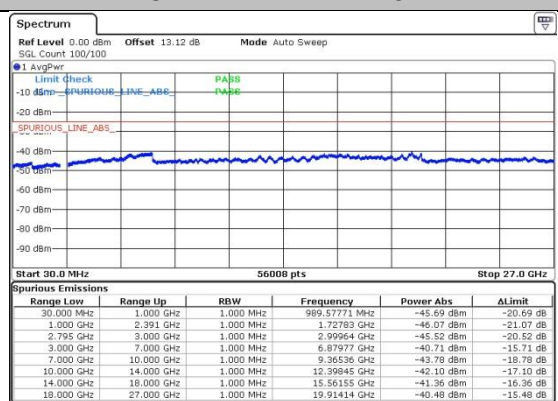
Date: 29 MAY 2020 05:38:28

Middle Channel / 64QAM



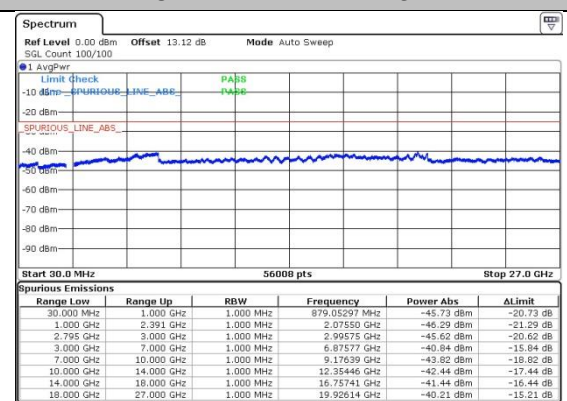
Date: 29 MAY 2020 05:40:57

Highest Channel / 16QAM



Date: 29 MAY 2020 05:37:08

Highest Channel / 64QAM

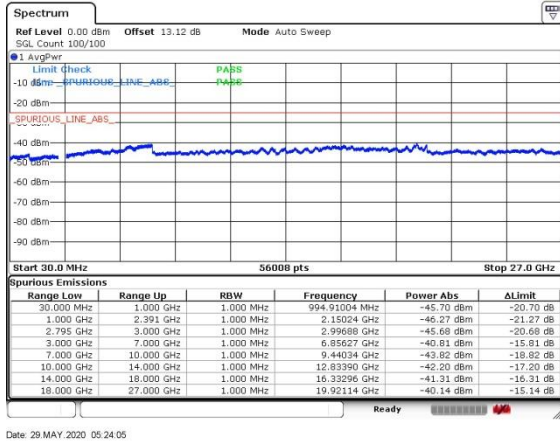


Date: 29 MAY 2020 05:41:57

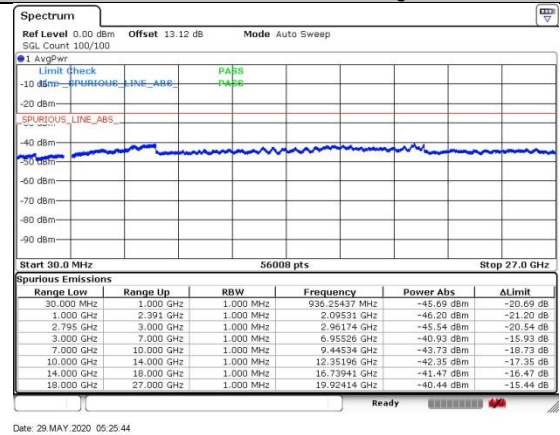


n41 50MHz

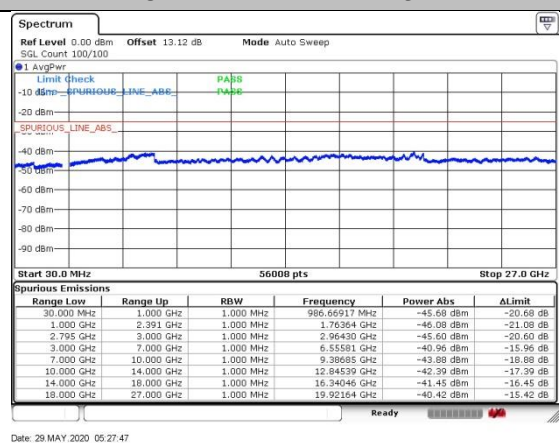
Lowest Channel / 256QAM



Middle Channel / 256QAM



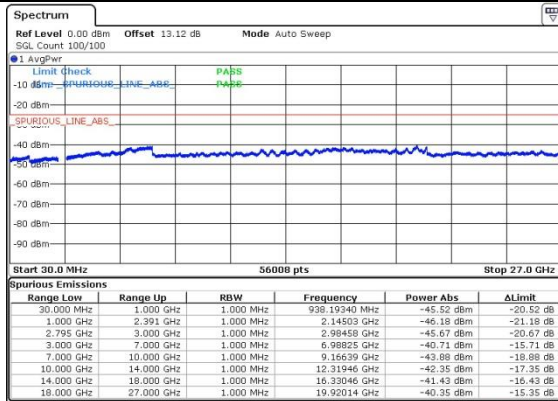
Highest Channel / 256QAM





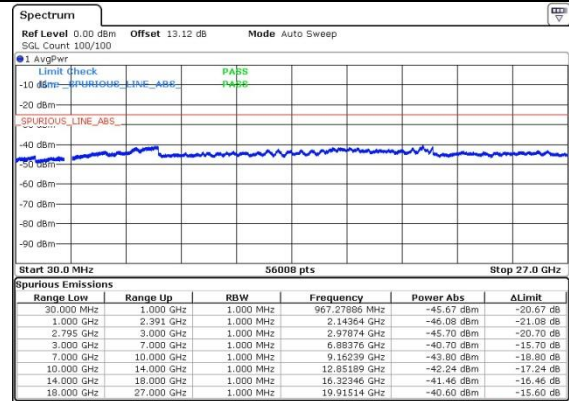
n41 60MHz

Lowest Channel / BPSK



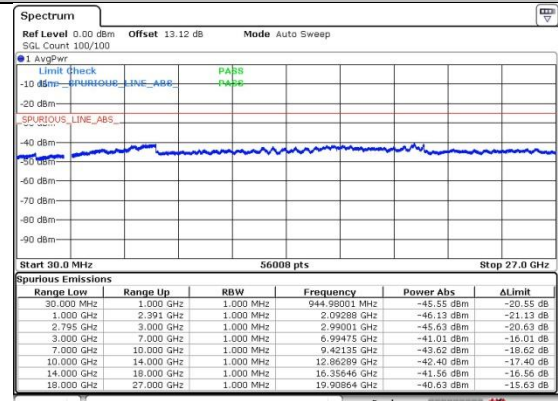
Date: 29 MAY 2020 02:18:31

Lowest Channel / QPSK



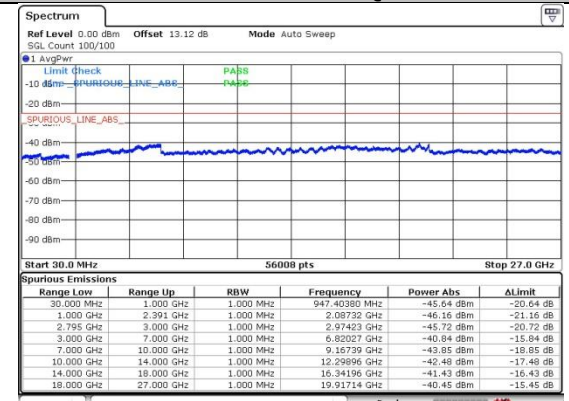
Date: 29 MAY 2020 02:21:17

Middle Channel / BPSK



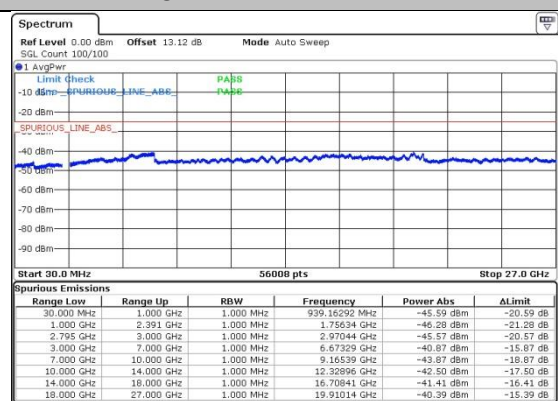
Date: 29 MAY 2020 02:19:22

Middle Channel / QPSK



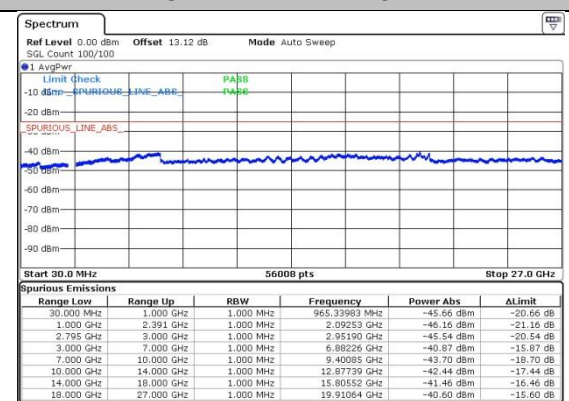
Date: 29 MAY 2020 02:26:07

Highest Channel / BPSK



Date: 29 MAY 2020 02:20:10

Highest Channel / QPSK

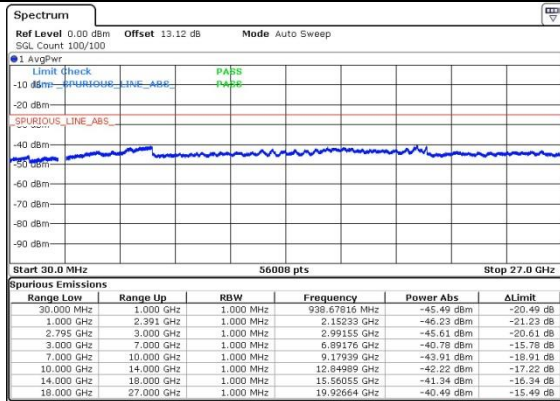


Date: 29 MAY 2020 02:27:30



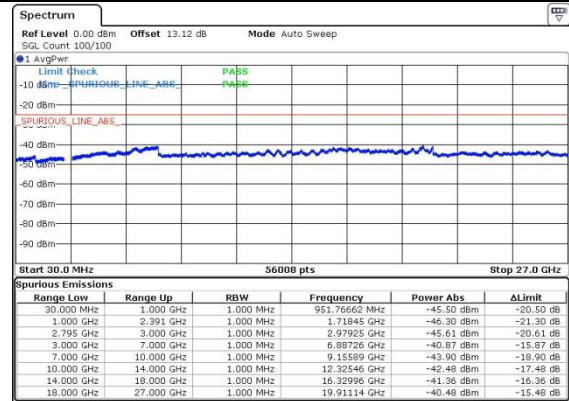
n41 60MHz

Lowest Channel / 16QAM



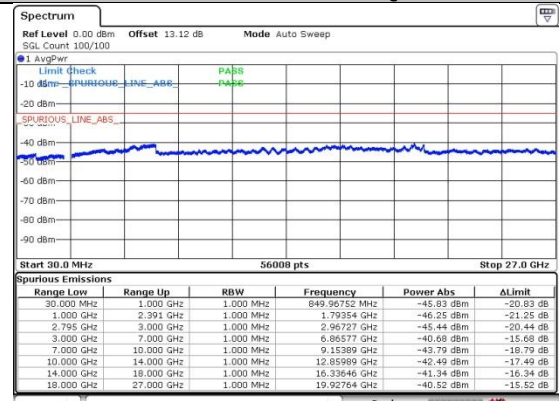
Date: 29 MAY 2020 02:28:30

Lowest Channel / 64QAM



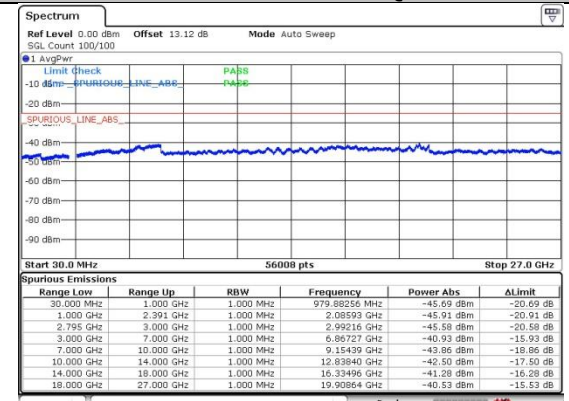
Date: 29 MAY 2020 02:36:51

Middle Channel / 16QAM



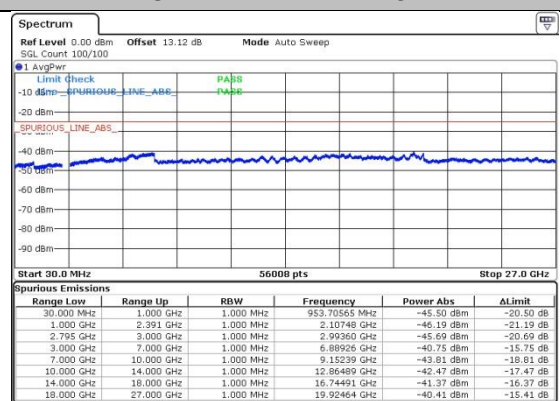
Date: 29 MAY 2020 02:30:05

Middle Channel / 64QAM



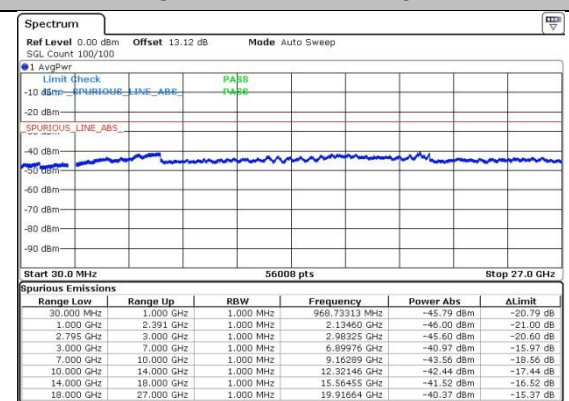
Date: 29 MAY 2020 02:16:48

Highest Channel / 16QAM



Date: 29 MAY 2020 02:31:48

Highest Channel / 64QAM

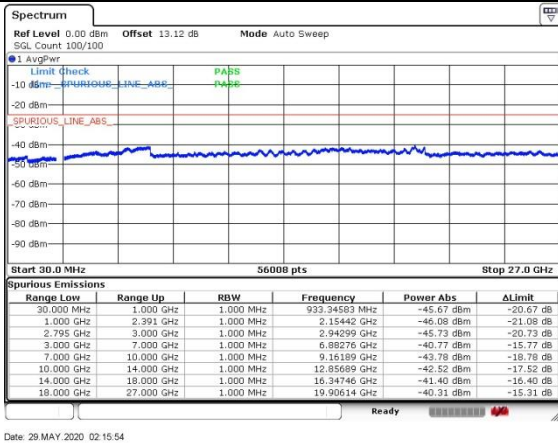


Date: 29 MAY 2020 02:17:38

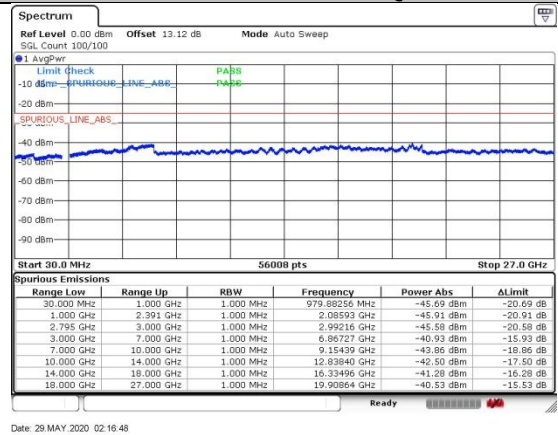


n41 60MHz

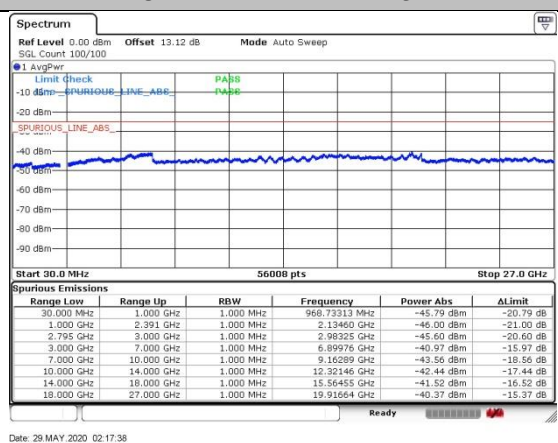
Lowest Channel / 256QAM



Middle Channel / 256QAM



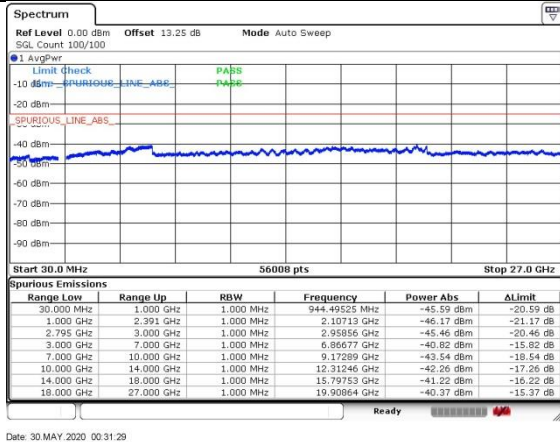
Highest Channel / 256QAM



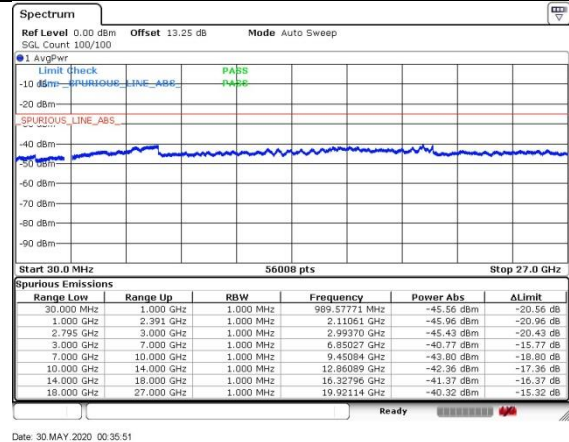


n41 80MHz

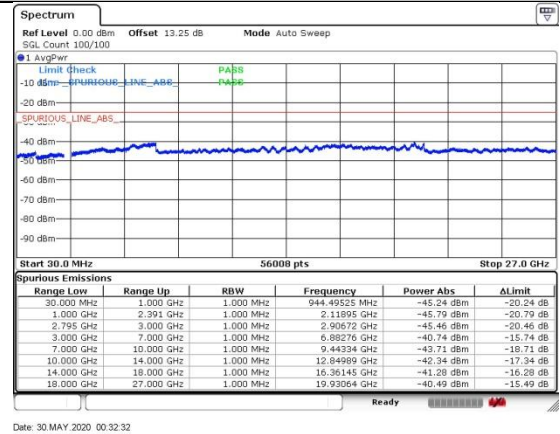
Lowest Channel / BPSK



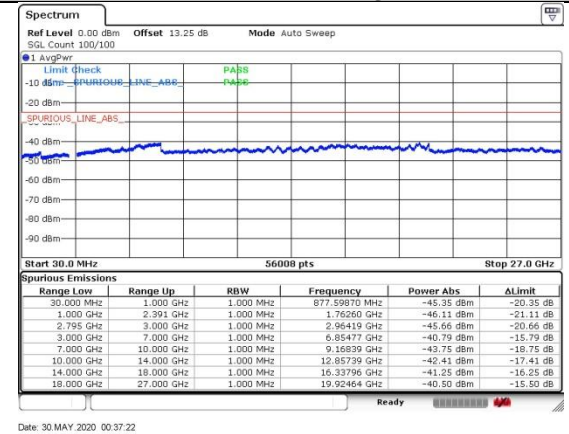
Lowest Channel / QPSK



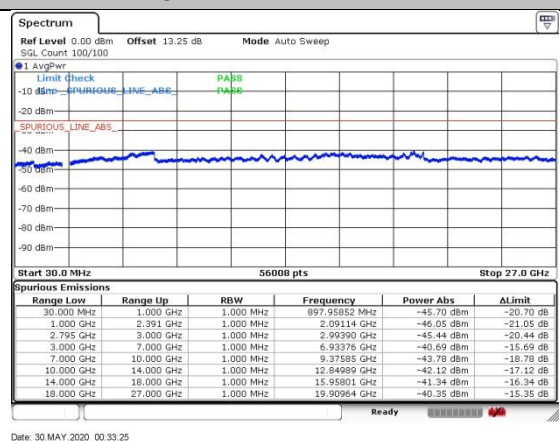
Middle Channel / BPSK



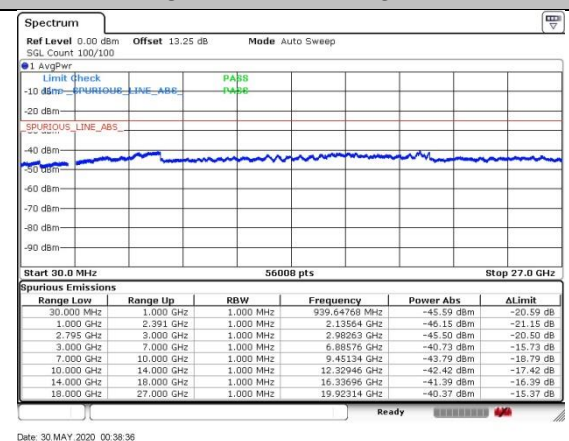
Middle Channel / QPSK



Highest Channel / BPSK



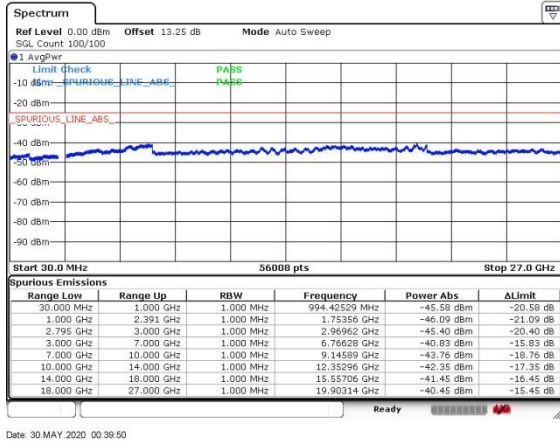
Highest Channel / QPSK



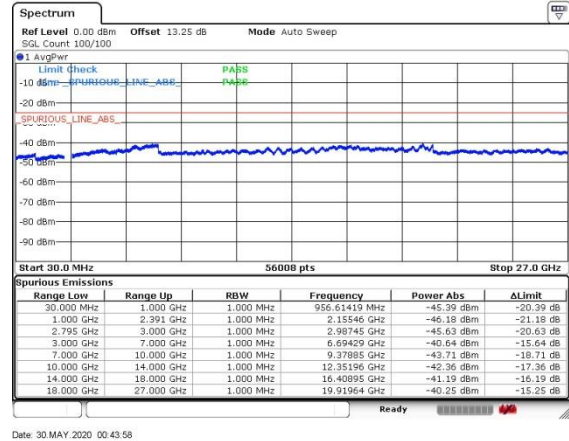


n41 80MHz

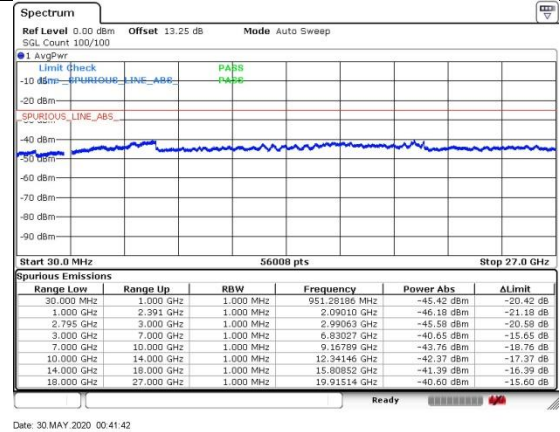
Lowest Channel / 16QAM



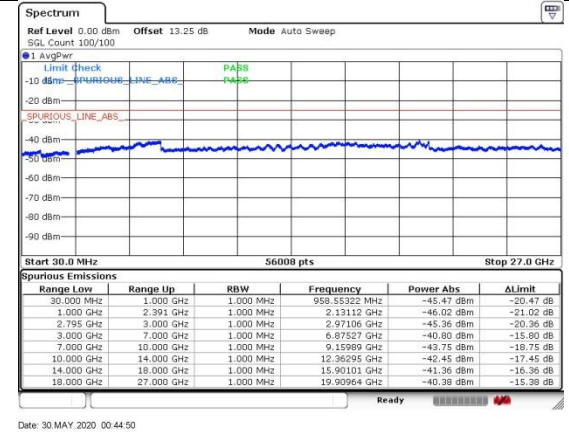
Lowest Channel / 64QAM



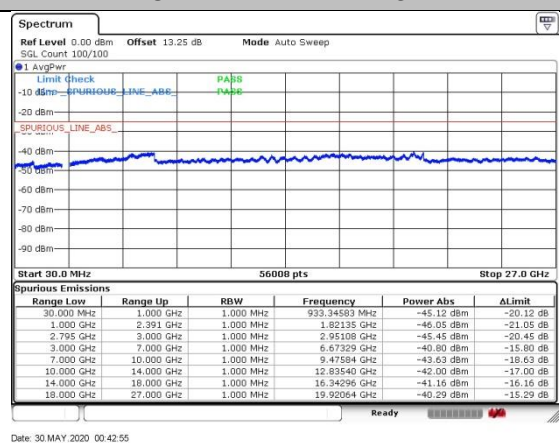
Middle Channel / 16QAM



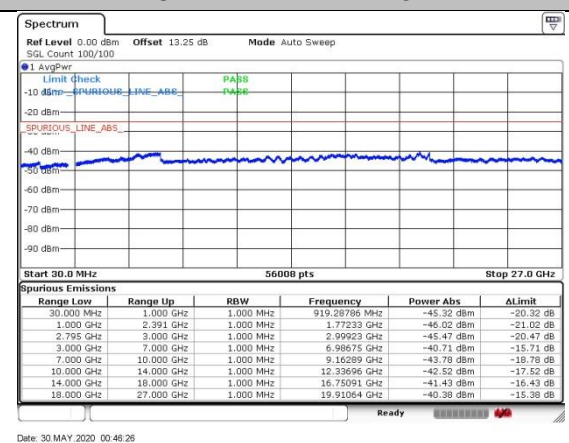
Middle Channel / 64QAM



Highest Channel / 16QAM



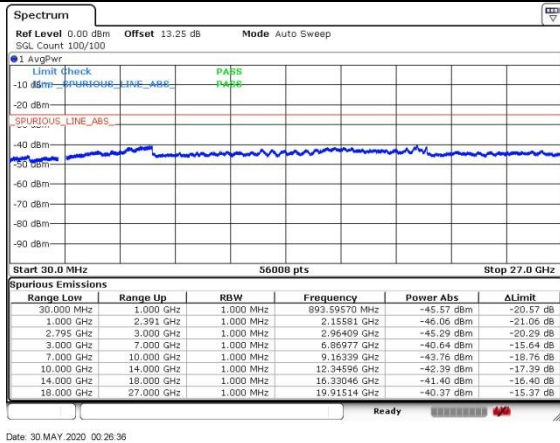
Highest Channel / 64QAM



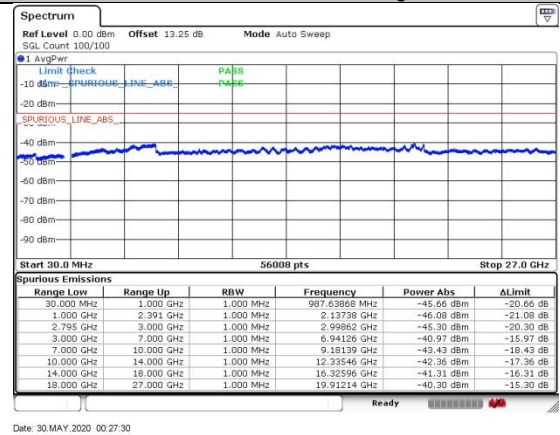


n41 80MHz

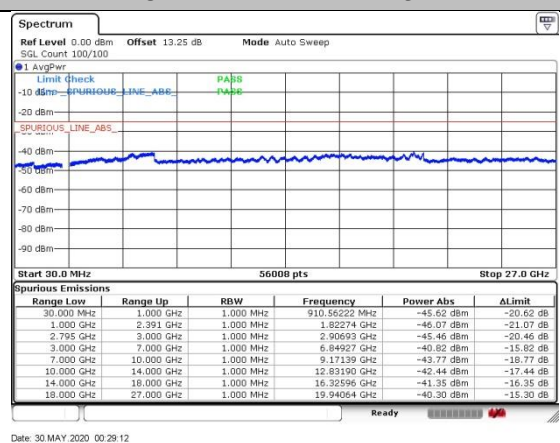
Lowest Channel / 256QAM



Middle Channel / 256QAM



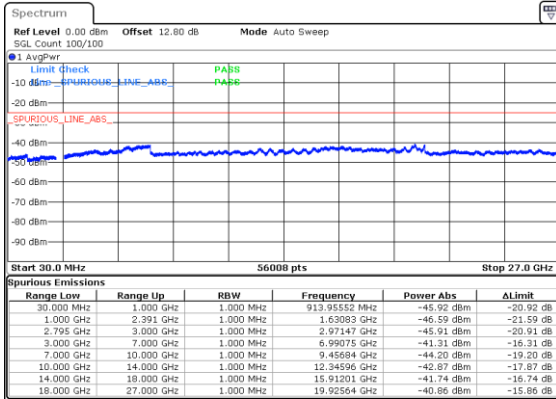
Highest Channel / 256QAM





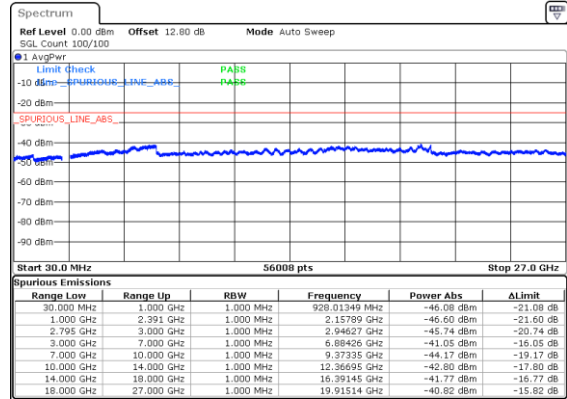
n41 90MHz

Lowest Channel / BPSK



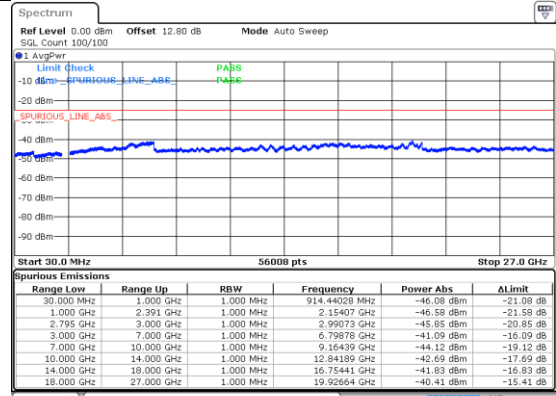
Date: 30 MAY 2020 14:14:41

Lowest Channel / QPSK



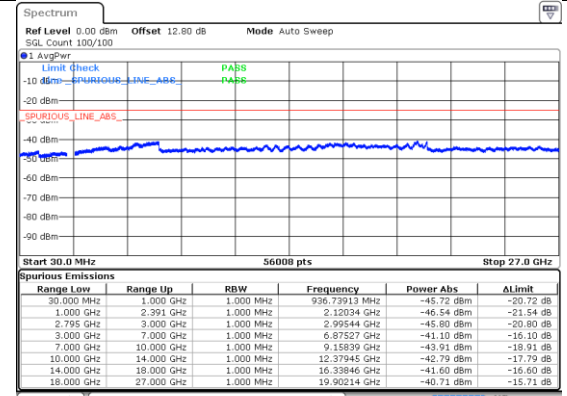
Date: 30 MAY 2020 14:18:02

Middle Channel / BPSK



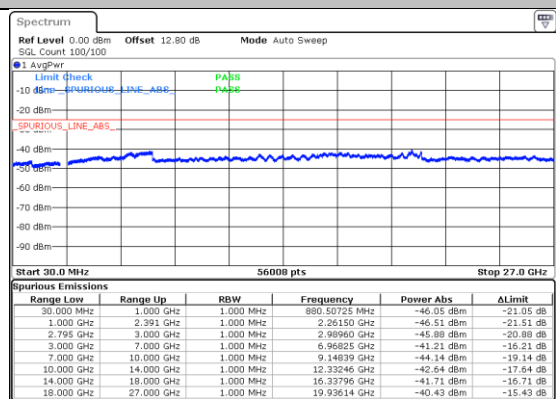
Date: 30 MAY 2020 15:16:15

Middle Channel / QPSK



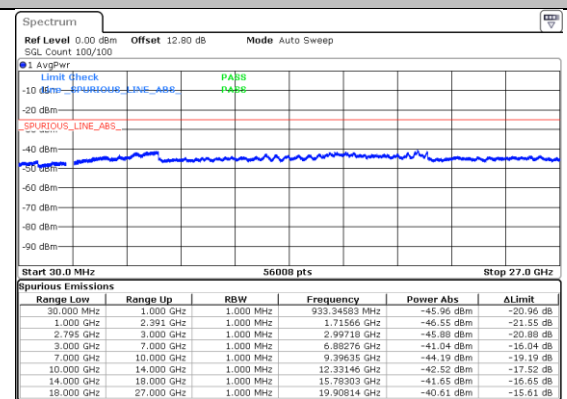
Date: 30 MAY 2020 15:18:39

Highest Channel / BPSK



Date: 30 MAY 2020 15:23:27

Highest Channel / QPSK

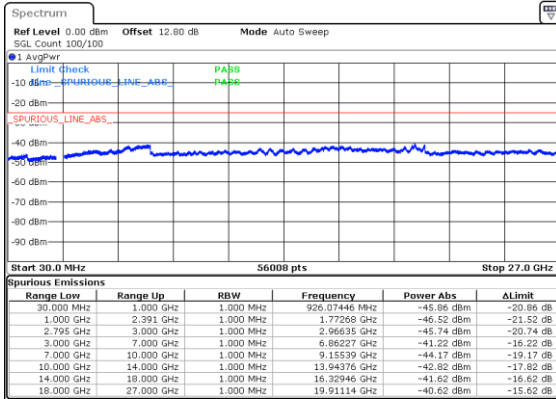


Date: 30 MAY 2020 15:25:41



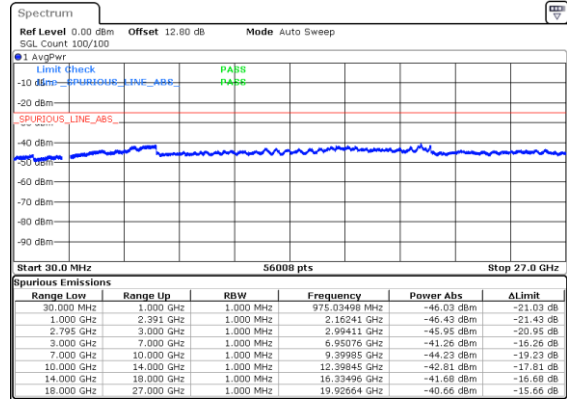
n41 90MHz

Lowest Channel / 16QAM



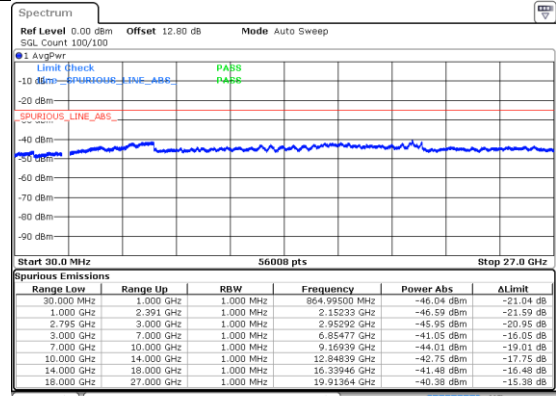
Date: 30 MAY 2020 14:19:21

Lowest Channel / 64QAM



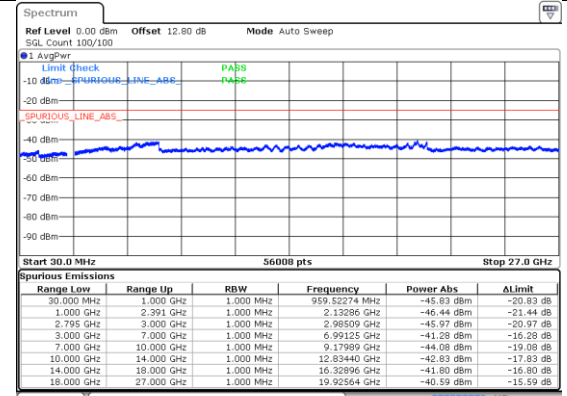
Date: 30 MAY 2020 14:21:23

Middle Channel / 16QAM



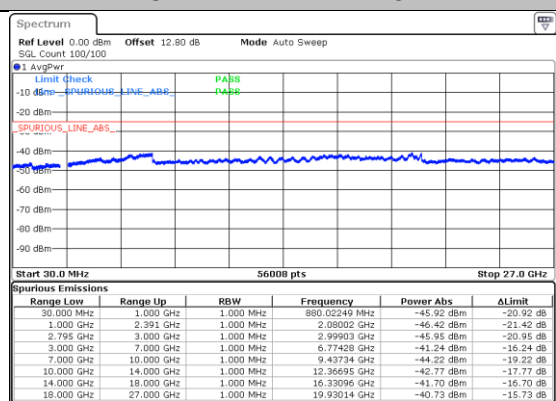
Date: 30 MAY 2020 15:19:34

Middle Channel / 64QAM



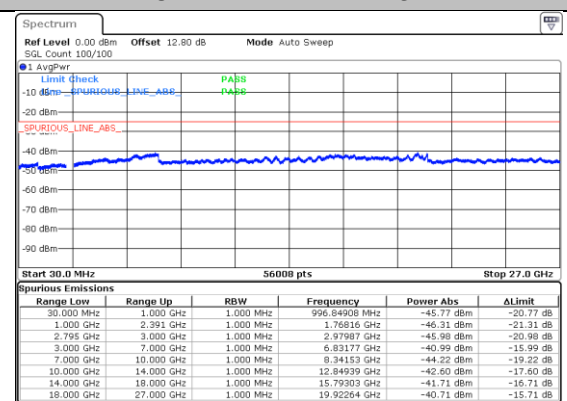
Date: 30 MAY 2020 15:20:31

Highest Channel / 16QAM



Date: 30 MAY 2020 15:26:37

Highest Channel / 64QAM

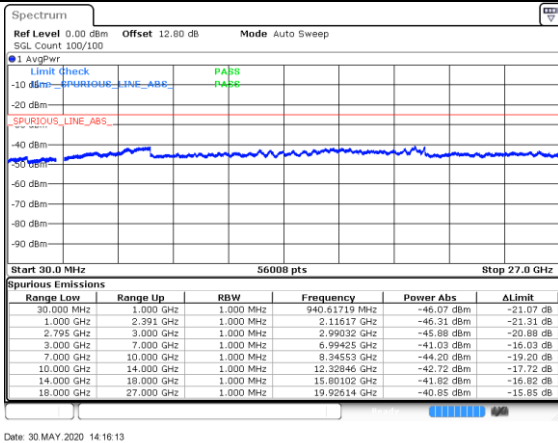


Date: 30 MAY 2020 15:27:34

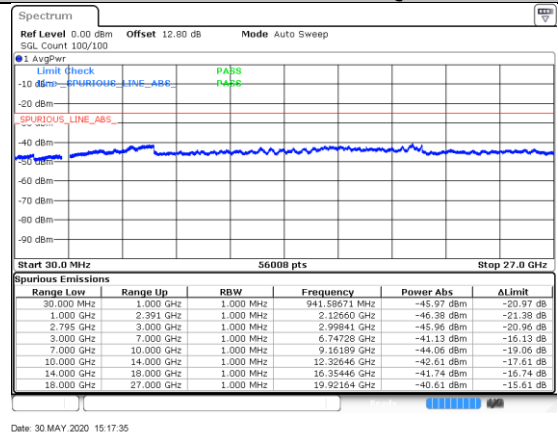


n41 90MHz

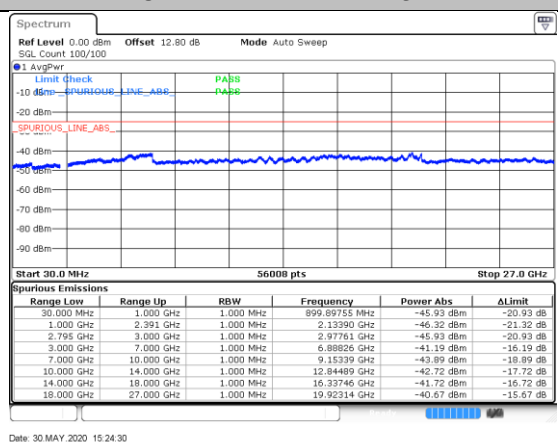
Lowest Channel / 256QAM



Middle Channel / 256QAM



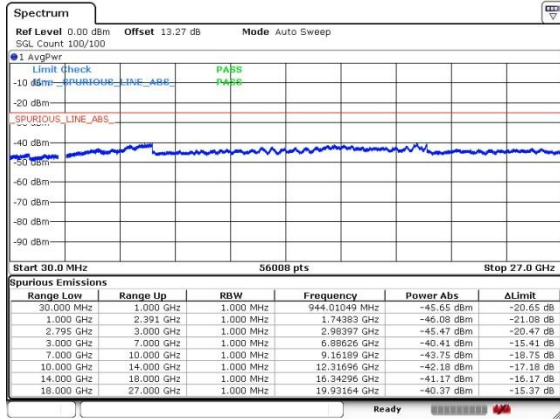
Highest Channel / 256QAM





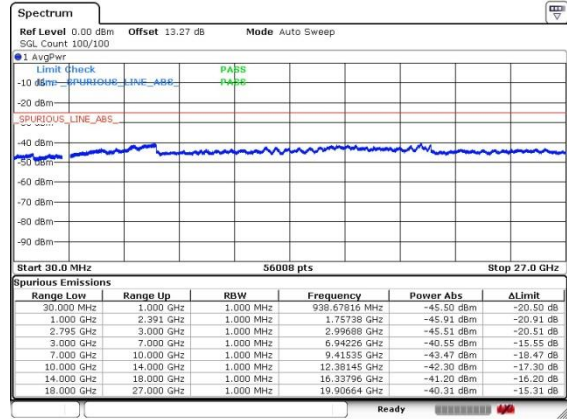
n41 100MHz

Lowest Channel / BPSK



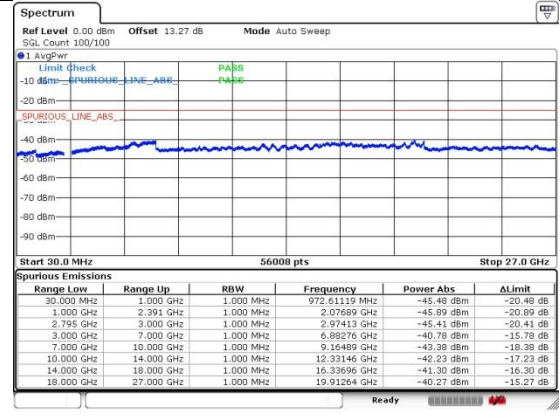
Date: 29 MAY 2020 04:26:42

Lowest Channel / QPSK



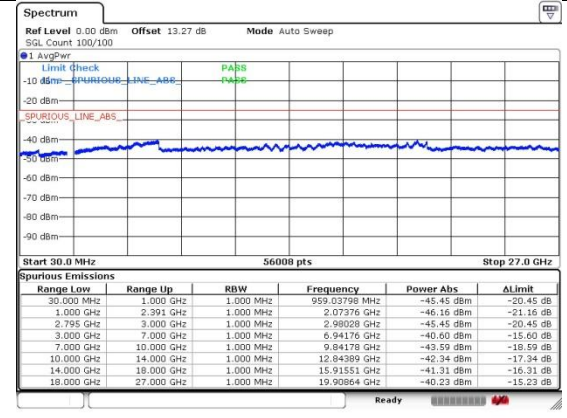
Date: 29 MAY 2020 04:34:06

Middle Channel / BPSK



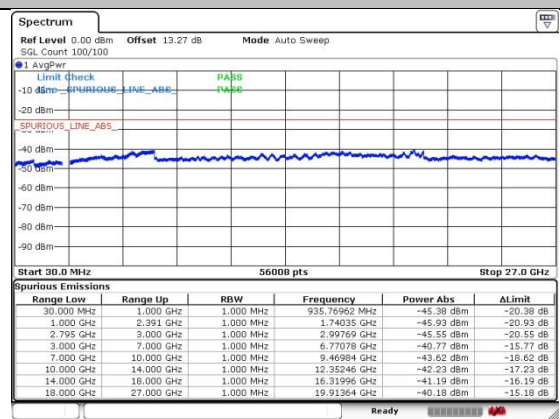
Date: 29 MAY 2020 04:28:05

Middle Channel / QPSK



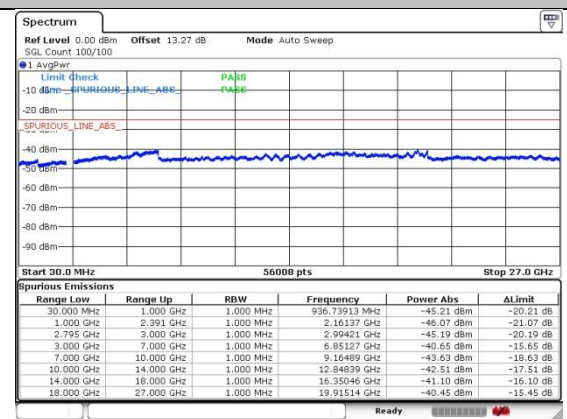
Date: 29 MAY 2020 04:35:20

Highest Channel / BPSK



Date: 29 MAY 2020 04:32:56

Highest Channel / QPSK

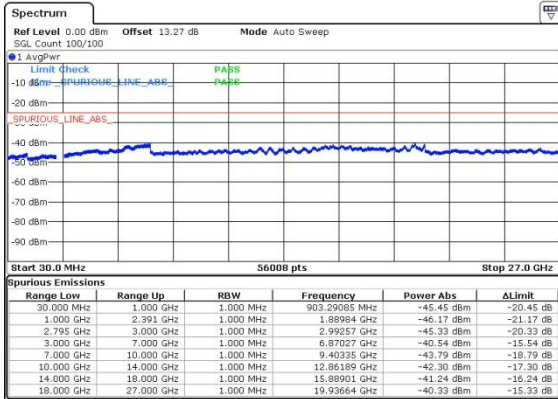


Date: 29 MAY 2020 04:36:51



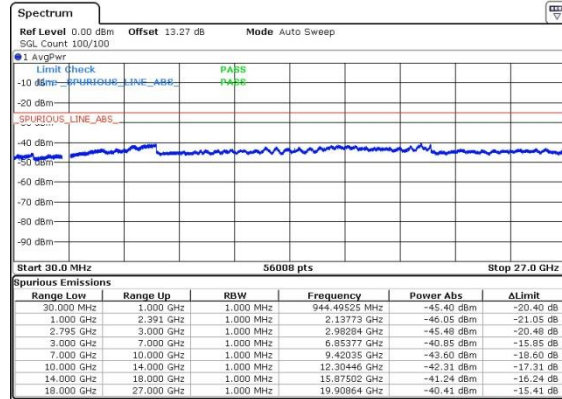
n41 100MHz

Lowest Channel / 16QAM



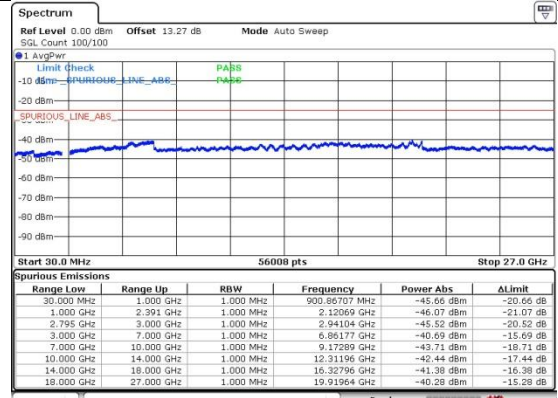
Date: 29 MAY 2020 04:38:22

Lowest Channel / 64QAM



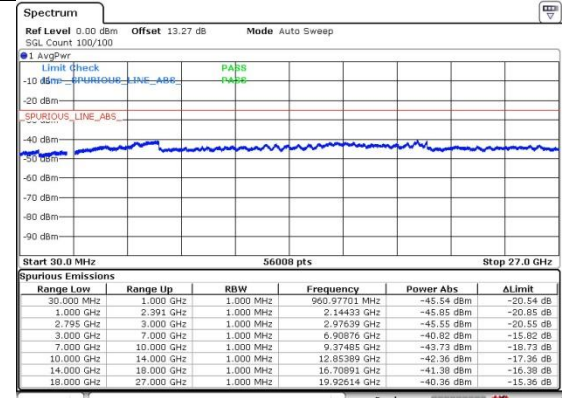
Date: 29 MAY 2020 04:42:14

Middle Channel / 16QAM



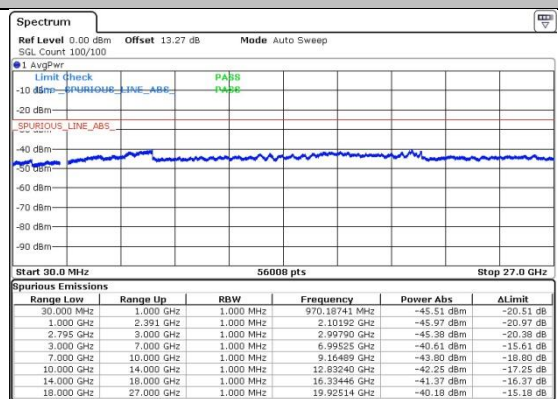
Date: 29 MAY 2020 04:39:40

Middle Channel / 64QAM



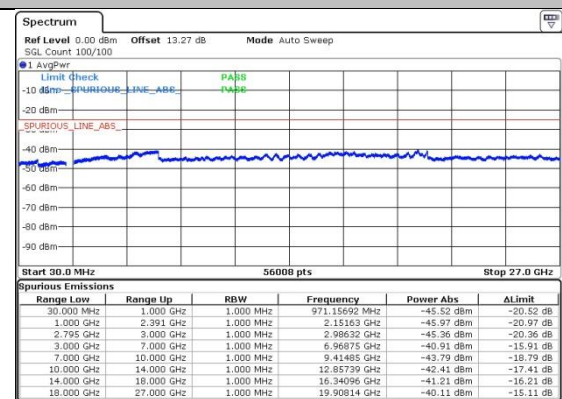
Date: 29 MAY 2020 04:43:19

Highest Channel / 16QAM



Date: 29 MAY 2020 04:40:56

Highest Channel / 64QAM

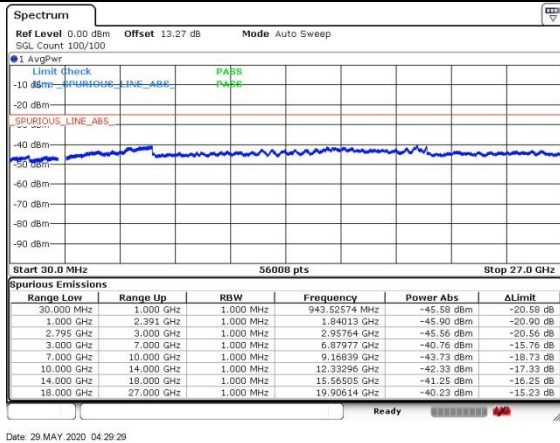


Date: 29 MAY 2020 04:44:35

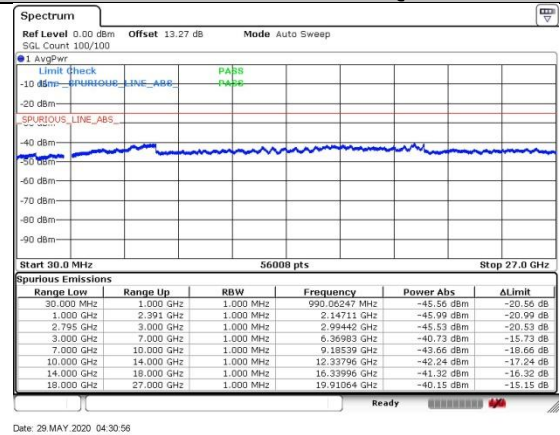


n41 100MHz

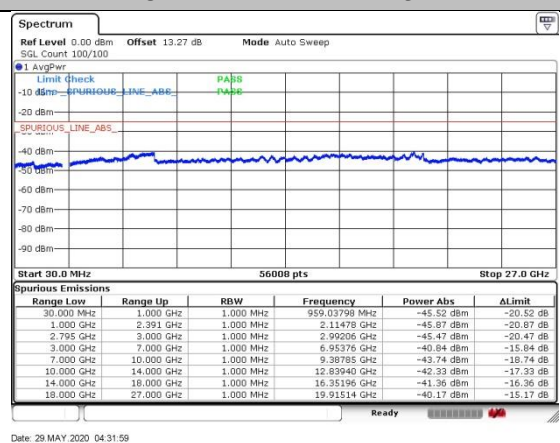
Lowest Channel / 256QAM



Middle Channel / 256QAM



Highest Channel / 256QAM



Frequency Stability

Test Conditions		n41 (BPSK) / Middle Channel	Limit
Temperature (°C)	Voltage (Volt)	n41 20MHz	Note 2.
		Deviation (ppm)	Result
50	Normal Voltage	0.0015	PASS
40	Normal Voltage	0.0002	
30	Normal Voltage	0.0013	
20(Ref.)	Normal Voltage	0.0000	
10	Normal Voltage	0.0000	
0	Normal Voltage	0.0016	
-10	Normal Voltage	0.0005	
-20	Normal Voltage	0.0019	
-30	Normal Voltage	0.0003	
20	Maximum Voltage	0.0015	
20	Normal Voltage	0.0000	
20	Battery End Point	0.0009	

Note:

1. Normal Voltage =3.8 V. ; Battery End Point (BEP) =3.4 V. ; Maximum Voltage =4.45 V.
2. Note: The frequency fundamental emissions stay within the authorized frequency block.



Appendix B. Test Results of Radiated Test

Radiated Spurious Emission

NR n41 90MHz (DFTs-OFDM)								
Channel	Frequency (MHz)	EIRP (dBm)	Limit (dBm)	Over Limit (dB)	S.G. Power (dBm)	TX Cable loss (dB)	TX Antenna Gain (dBi)	Polarization (H/V)
Middle	5100	-61.02	-25	-36.02	-71.23	3.03	13.24	H
	7650	-44.77	-25	-19.77	-54.22	3.56	13.01	H
	10206	-57.88	-25	-32.88	-67.40	3.92	13.44	H
	5100	-63.34	-25	-38.34	-73.55	3.03	13.24	V
	7650	-46.67	-25	-21.67	-56.12	3.56	13.01	V
	10206	-57.40	-25	-32.40	-66.92	3.92	13.44	V

Remark: Spurious emissions within 30-1000MHz were found more than 20dB below limit line.