



MOTOROLA

Date: July 1, 2010

Subject: Request for additional information regarding FCC ID: IHDT56LU1

Reference:

Correspondence Reference Number: IHD100730
Confirmation Number: 1Y1006140730-33
Date of Original Email: June 23, 2010

Prepared by:

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Questions and responses follow:

1. Please submit block diagrams for the DTS and DSS transmitter chips that show some detail of the RF sections of those transmitters, as well as listing the clock/oscillator values (p. 3/5 of the BLD document is a schematic, not a block diagram).

RESPONSE: Please refer to the revised block diagrams submitted online.

2. Please verify that different DTS data rates were investigated in the course of performing the EMC measurements, and that the data provided represents the worst case data. Please note that the test report should clearly state this.

RESPONSE: We verified and clearly state this under "General and Special Conditions" of the report. Please refer to the revised DTS EMC report submitted online.

3. Please correct the following typo: DTS EMC report p. 5/44: "LISN" is misspelled in the text.

RESPONSE: Please refer to the revised DTS EMC report submitted online.

4. DTS RF conducted spurious emissions (thru the 10th harmonic) were measured using an average detector. This is not permitted for a 15.247 device. Please re-measure using a peak detector and submit that data.

RESPONSE: Please refer to the revised DTS EMC report submitted online.

5. An average detector was used to perform the WCDMA occupied bandwidth measurement- a peak detector should be used. Please revise.

RESPONSE: Please refer to the revised PCE EMC report submitted online.