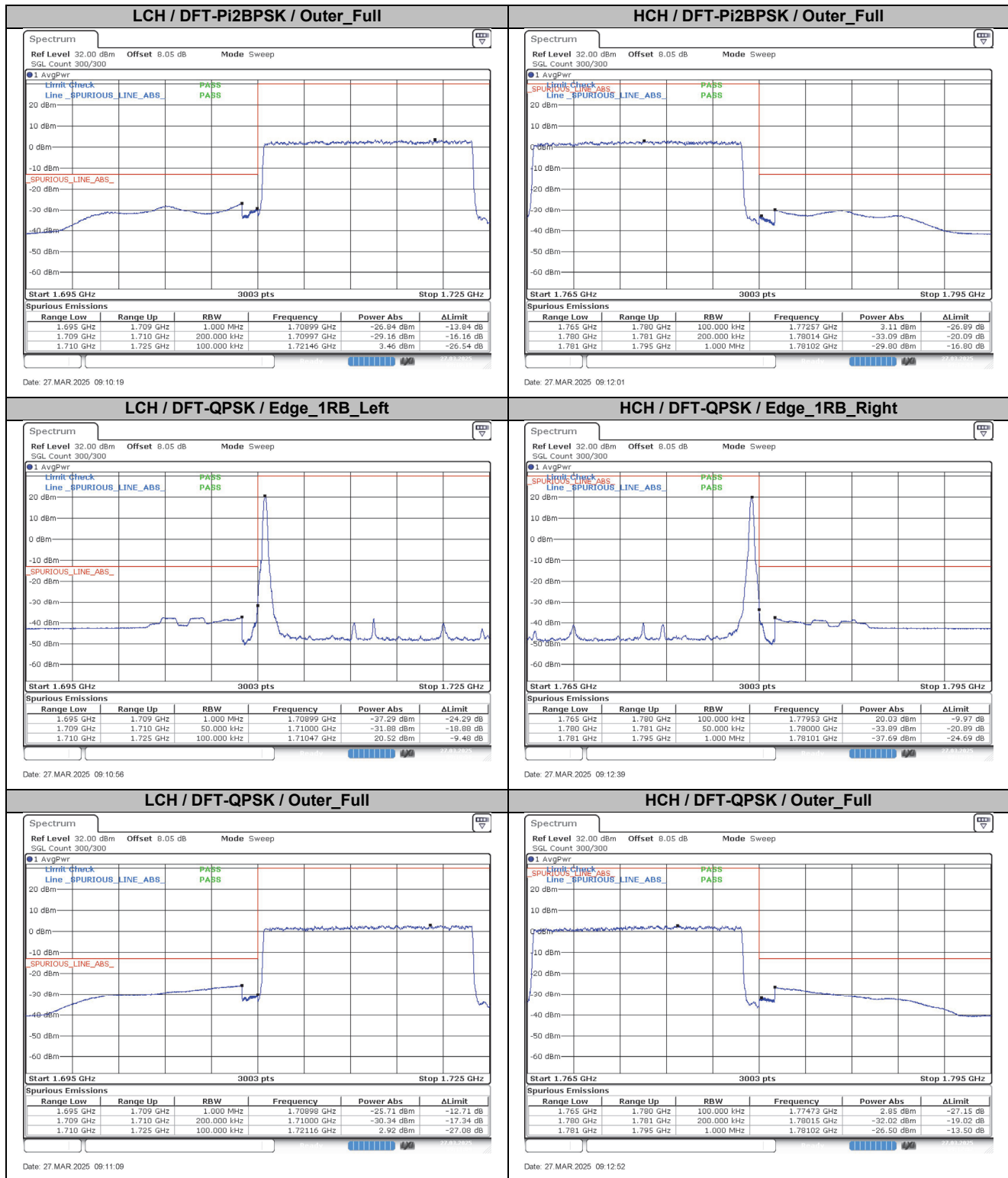
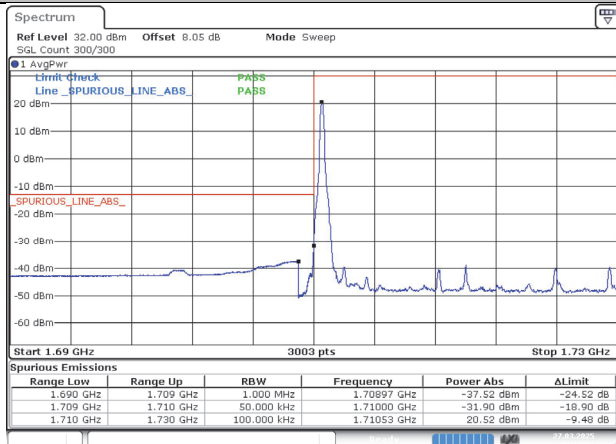




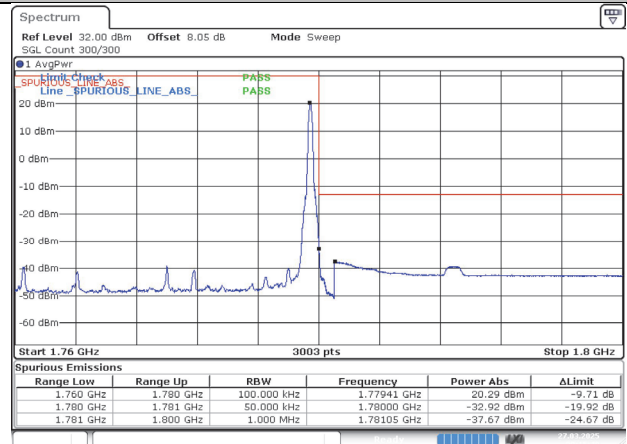
DC_5A_n66A / 15KHz / 20MHz

LCH / DFT-Pi2BPSK / Edge_1RB_Left



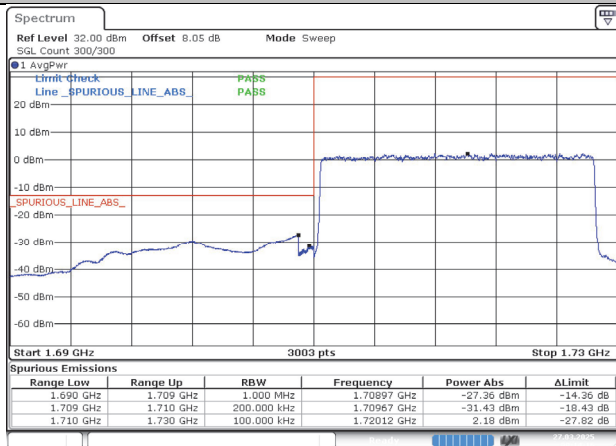
Date: 27 MAR 2025 09:14:01

HCH / DFT-Pi2BPSK / Edge_1RB_Right



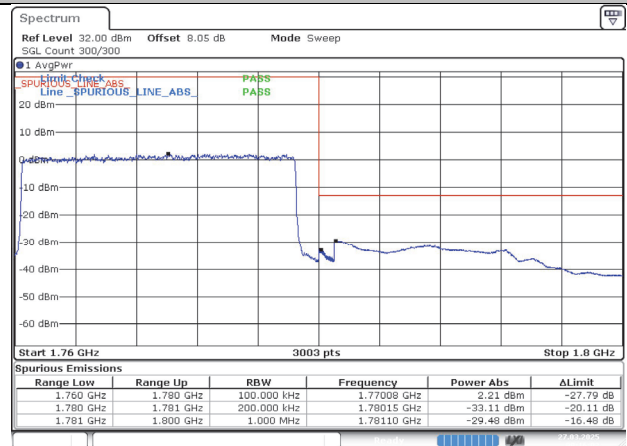
Date: 27 MAR 2025 09:15:49

LCH / DFT-Pi2BPSK / Outer_Full



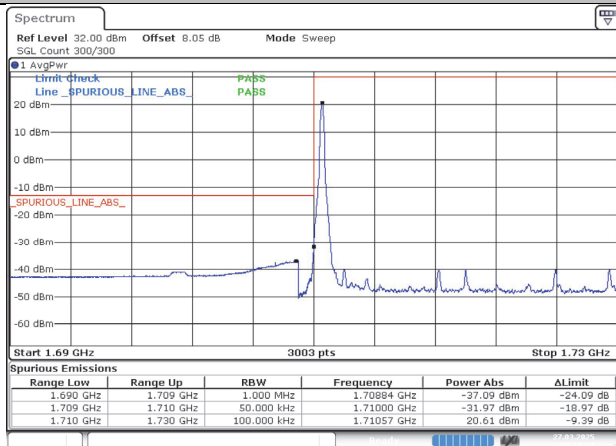
Date: 27 MAR 2025 09:14:16

HCH / DFT-Pi2BPSK / Outer_Full



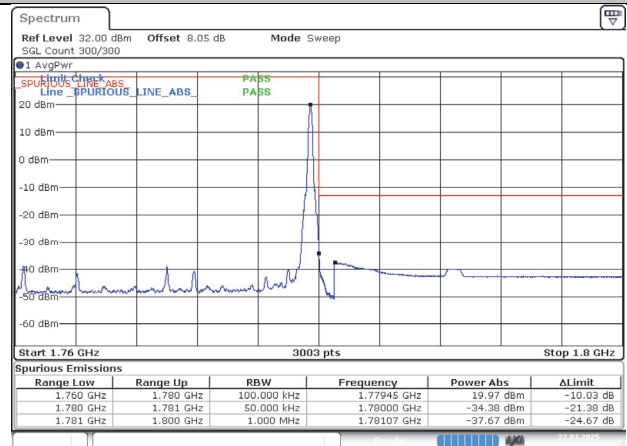
Date: 27 MAR 2025 09:16:02

LCH / DFT-QPSK / Edge_1RB_Left



Date: 27 MAR 2025 09:14:55

HCH / DFT-QPSK / Edge_1RB_Right



Date: 27 MAR 2025 09:16:41

