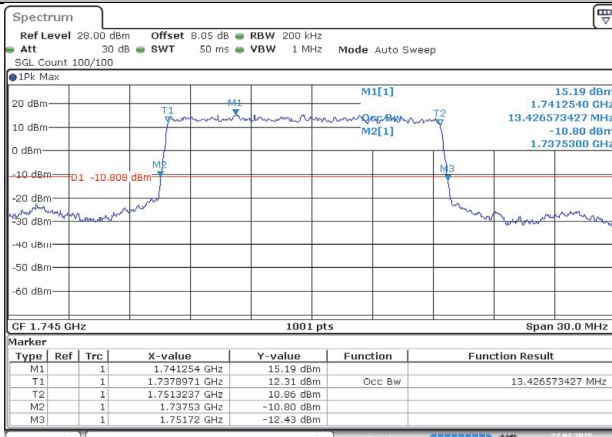


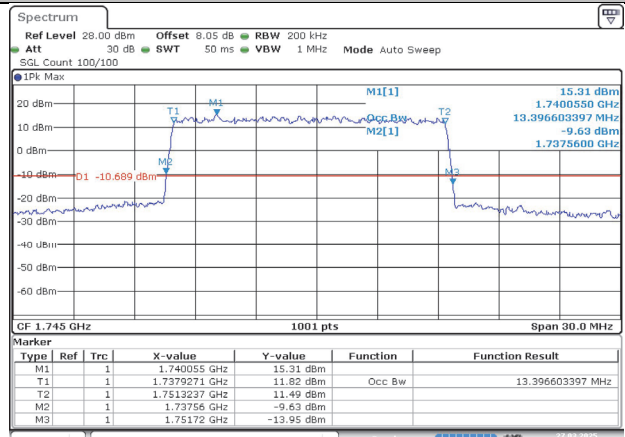
DC_5A_n66A / 15KHz / 15MHz

MCH / DFT-Pi2BPSK / Outer_Full



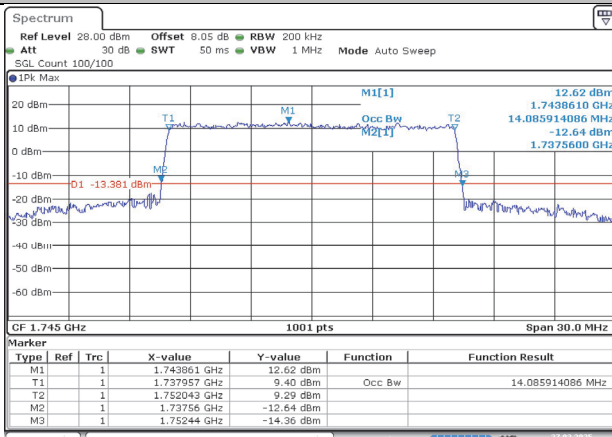
Date: 27 MAR 2025 08:54:14

MCH / DFT-QPSK / Outer_Full



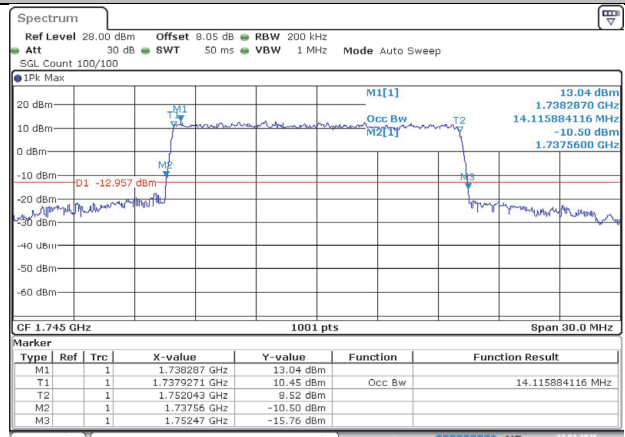
Date: 27 MAR 2025 08:54:24

MCH / CP-QPSK / Outer_Full



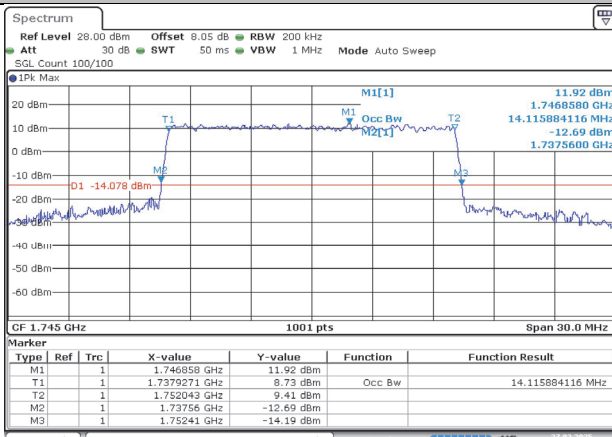
Date: 27 MAR 2025 08:55:02

MCH / CP-16QAM / Outer_Full



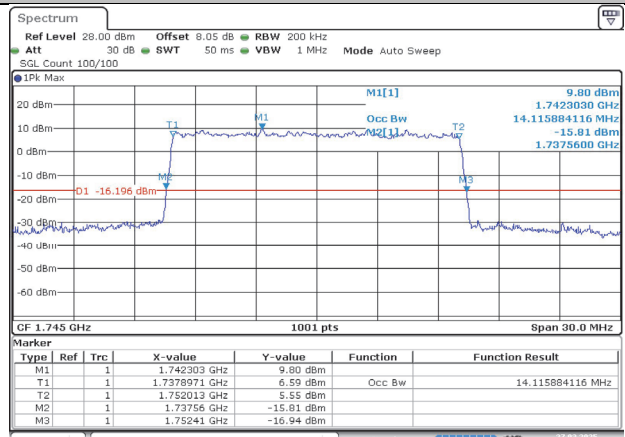
Date: 27 MAR 2025 08:55:11

MCH / CP-64QAM / Outer_Full



Date: 27 MAR 2025 08:55:20

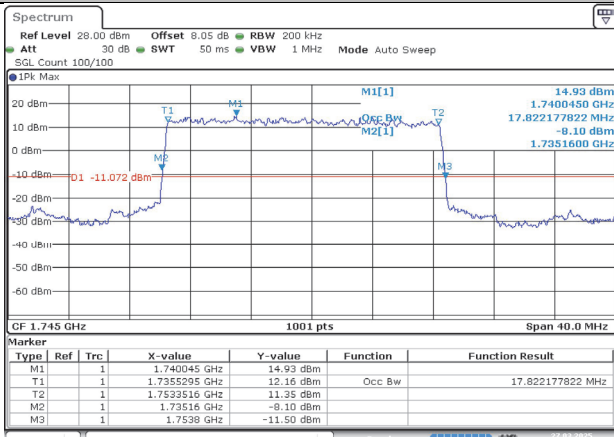
MCH / CP-256QAM / Outer_Full



Date: 27 MAR 2025 08:55:29

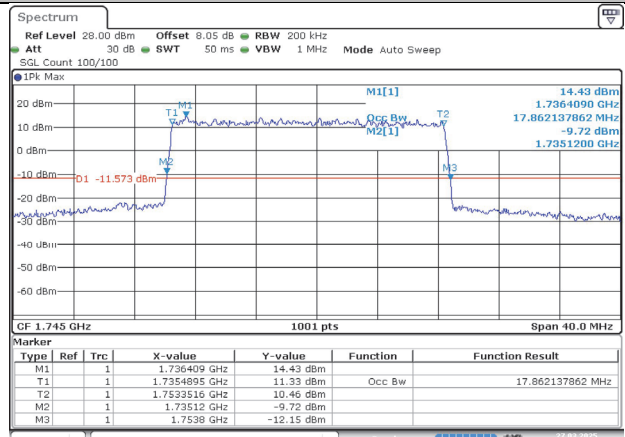
DC_5A_n66A / 15KHz / 20MHz

MCH / DFT-Pi2BPSK / Outer_Full



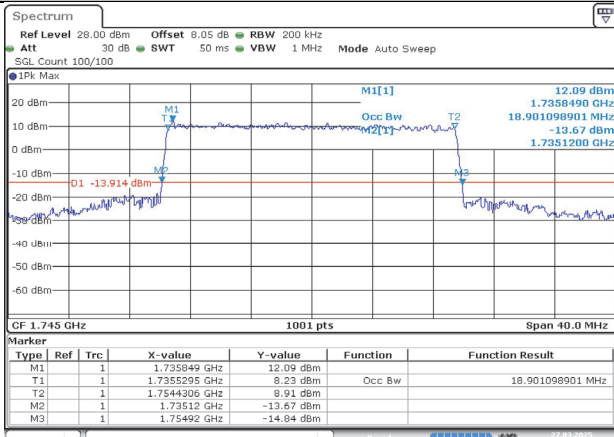
Date: 27 MAR 2025 08:55:43

MCH / DFT-QPSK / Outer_Full



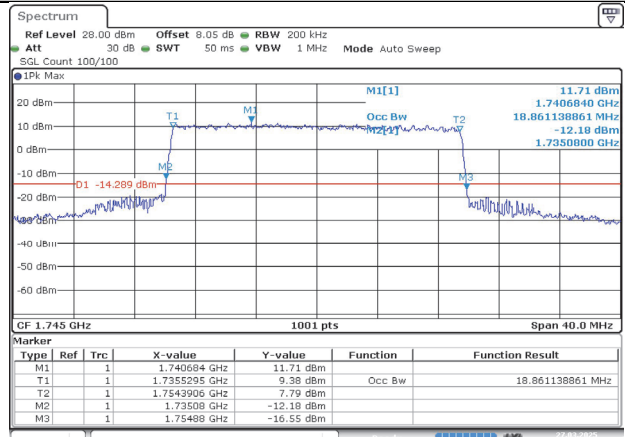
Date: 27 MAR 2025 08:55:53

MCH / CP-QPSK / Outer_Full



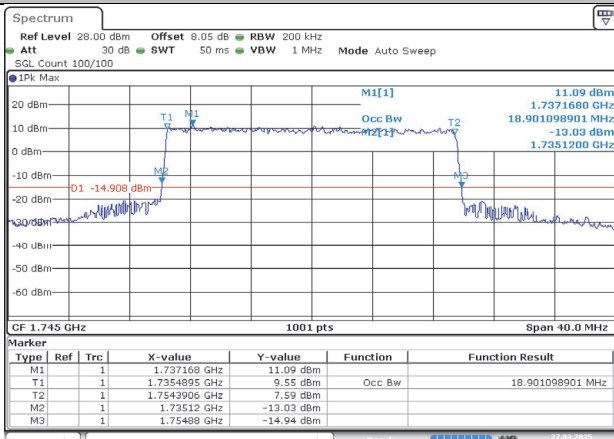
Date: 27 MAR 2025 08:56:34

MCH / CP-16QAM / Outer_Full



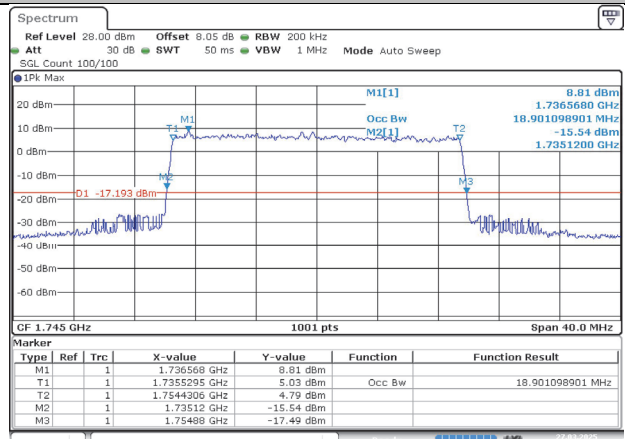
Date: 27 MAR 2025 08:56:44

MCH / CP-64QAM / Outer_Full



Date: 27 MAR 2025 08:56:54

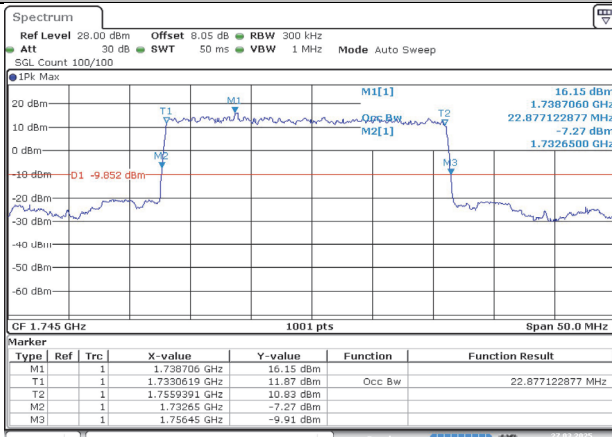
MCH / CP-256QAM / Outer_Full



Date: 27 MAR 2025 08:57:04

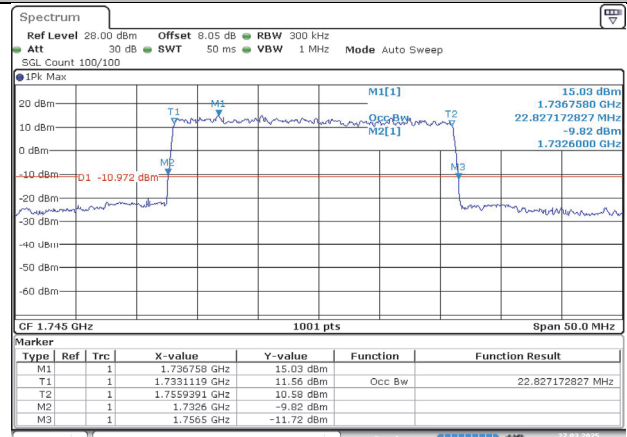
DC_5A_n66A / 15KHz / 25MHz

MCH / DFT-Pi2BPSK / Outer_Full



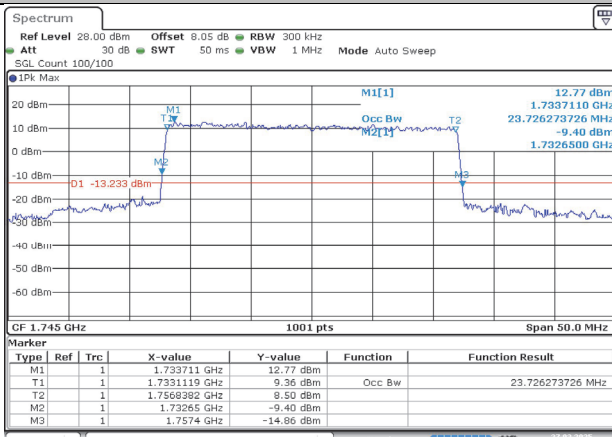
Date: 27 MAR 2025 08:57:16

MCH / DFT-QPSK / Outer_Full



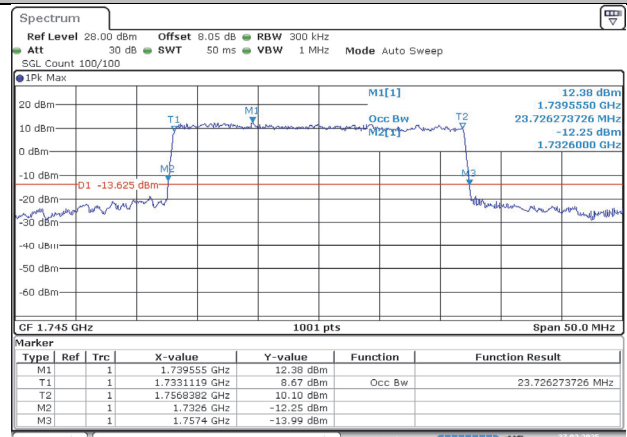
Date: 27 MAR 2025 08:57:25

MCH / CP-QPSK / Outer_Full



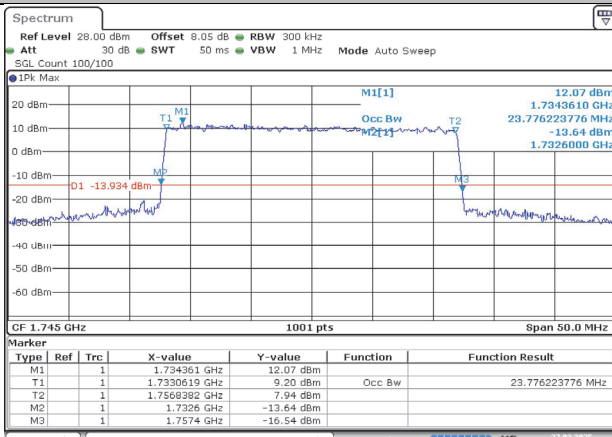
Date: 27 MAR 2025 08:57:58

MCH / CP-16QAM / Outer_Full



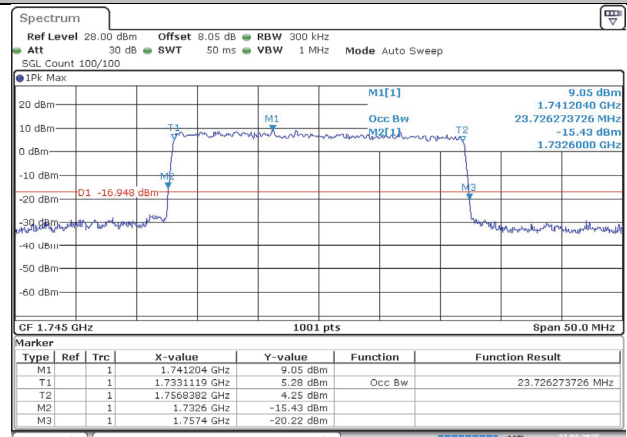
Date: 27 MAR 2025 08:58:06

MCH / CP-64QAM / Outer_Full



Date: 27 MAR 2025 08:58:14

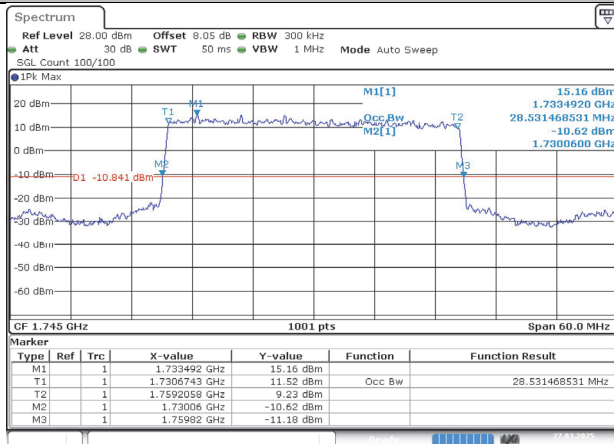
MCH / CP-256QAM / Outer_Full



Date: 27 MAR 2025 08:58:23

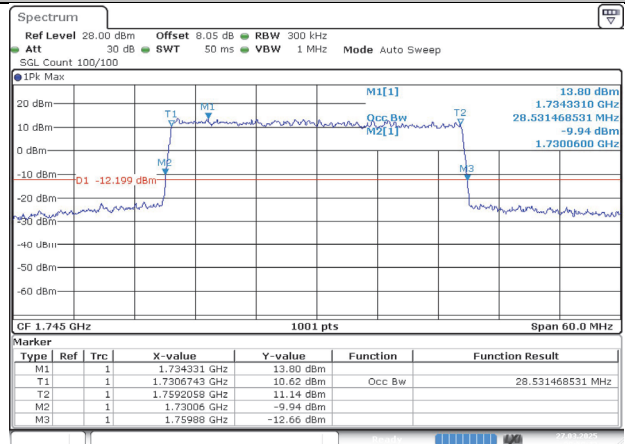
DC_5A_n66A / 15KHz / 30MHz

MCH / DFT-Pi2BPSK / Outer_Full



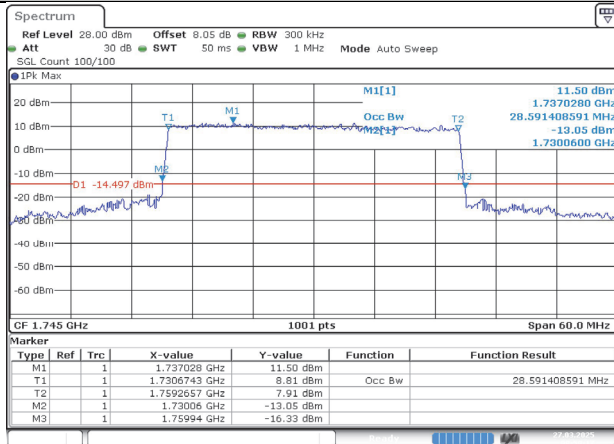
Date: 27 MAR 2025 08:58:35

MCH / DFT-QPSK / Outer_Full



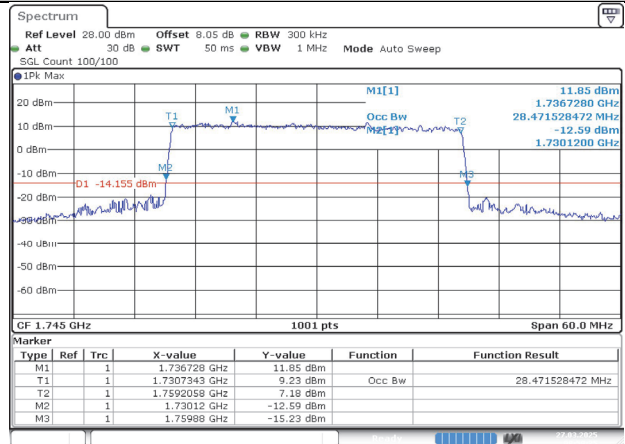
Date: 27 MAR 2025 08:58:43

MCH / CP-QPSK / Outer_Full



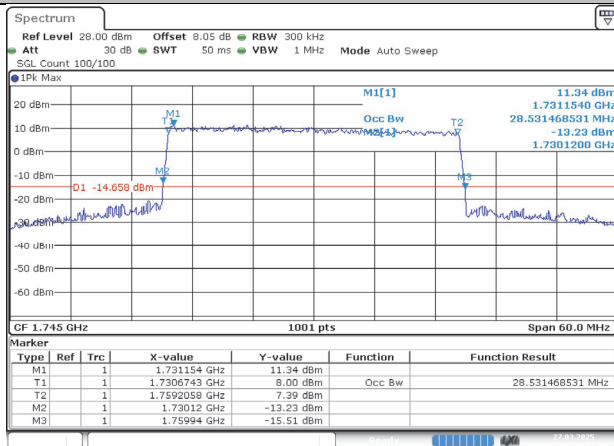
Date: 27 MAR 2025 08:59:17

MCH / CP-16QAM / Outer_Full



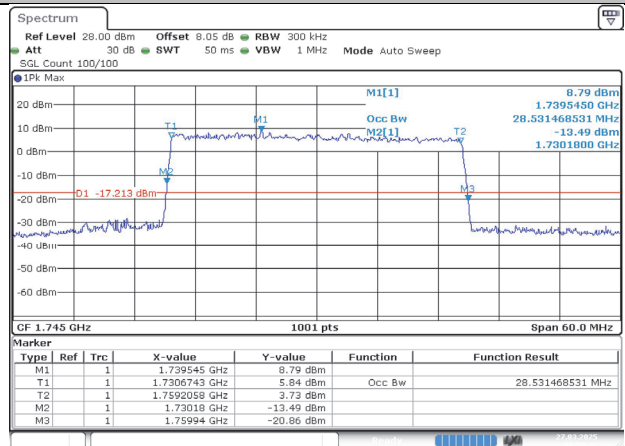
Date: 27 MAR 2025 08:59:25

MCH / CP-64QAM / Outer_Full



Date: 27 MAR 2025 08:59:33

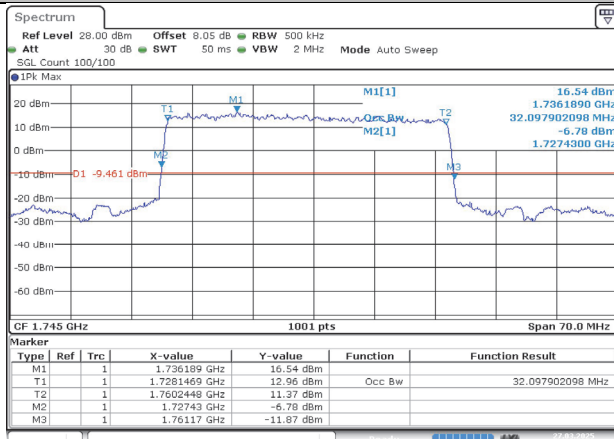
MCH / CP-256QAM / Outer_Full



Date: 27 MAR 2025 08:59:42

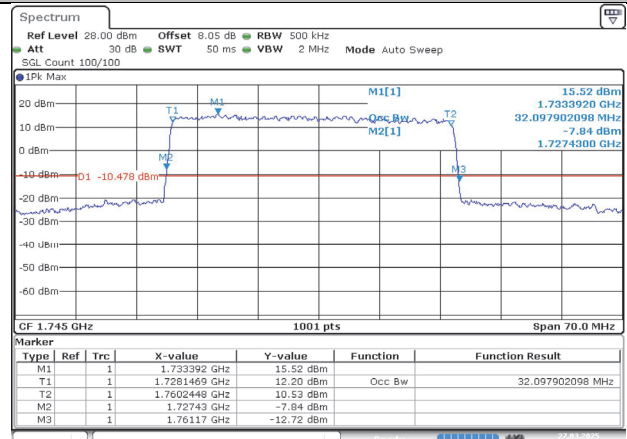
DC_5A_n66A / 15KHz / 35MHz

MCH / DFT-Pi2BPSK / Outer_Full



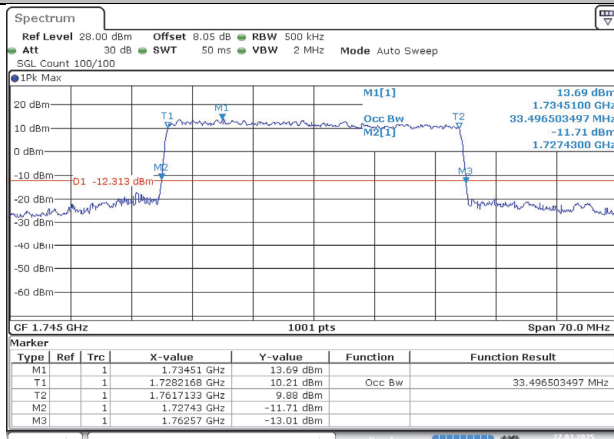
Date: 27 MAR 2025 08:59:54

MCH / DFT-QPSK / Outer_Full



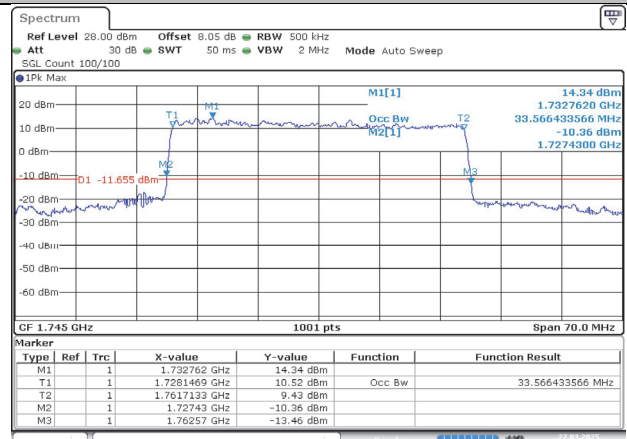
Date: 27 MAR 2025 09:00:03

MCH / CP-QPSK / Outer_Full



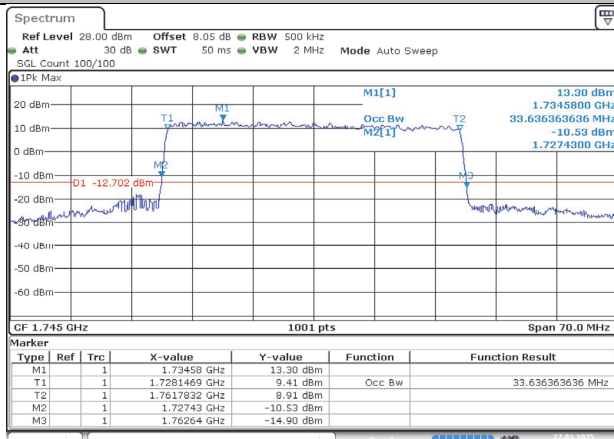
Date: 27 MAR 2025 09:00:38

MCH / CP-16QAM / Outer_Full



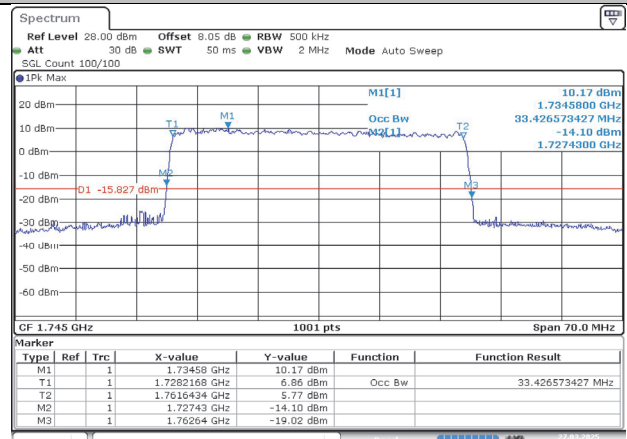
Date: 27 MAR 2025 09:00:46

MCH / CP-64QAM / Outer_Full



Date: 27 MAR 2025 09:00:55

MCH / CP-256QAM / Outer_Full



Date: 27 MAR 2025 09:01:04