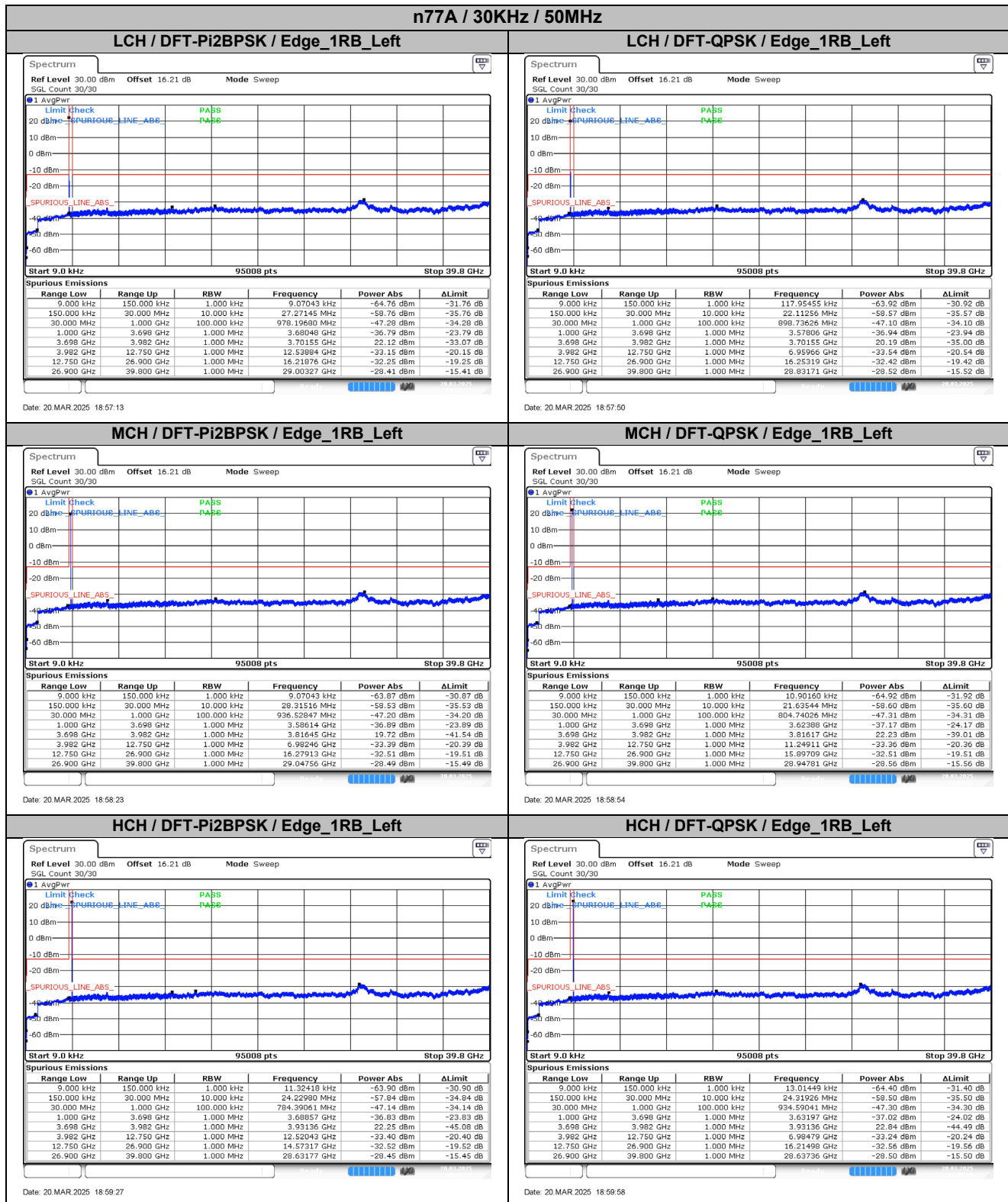
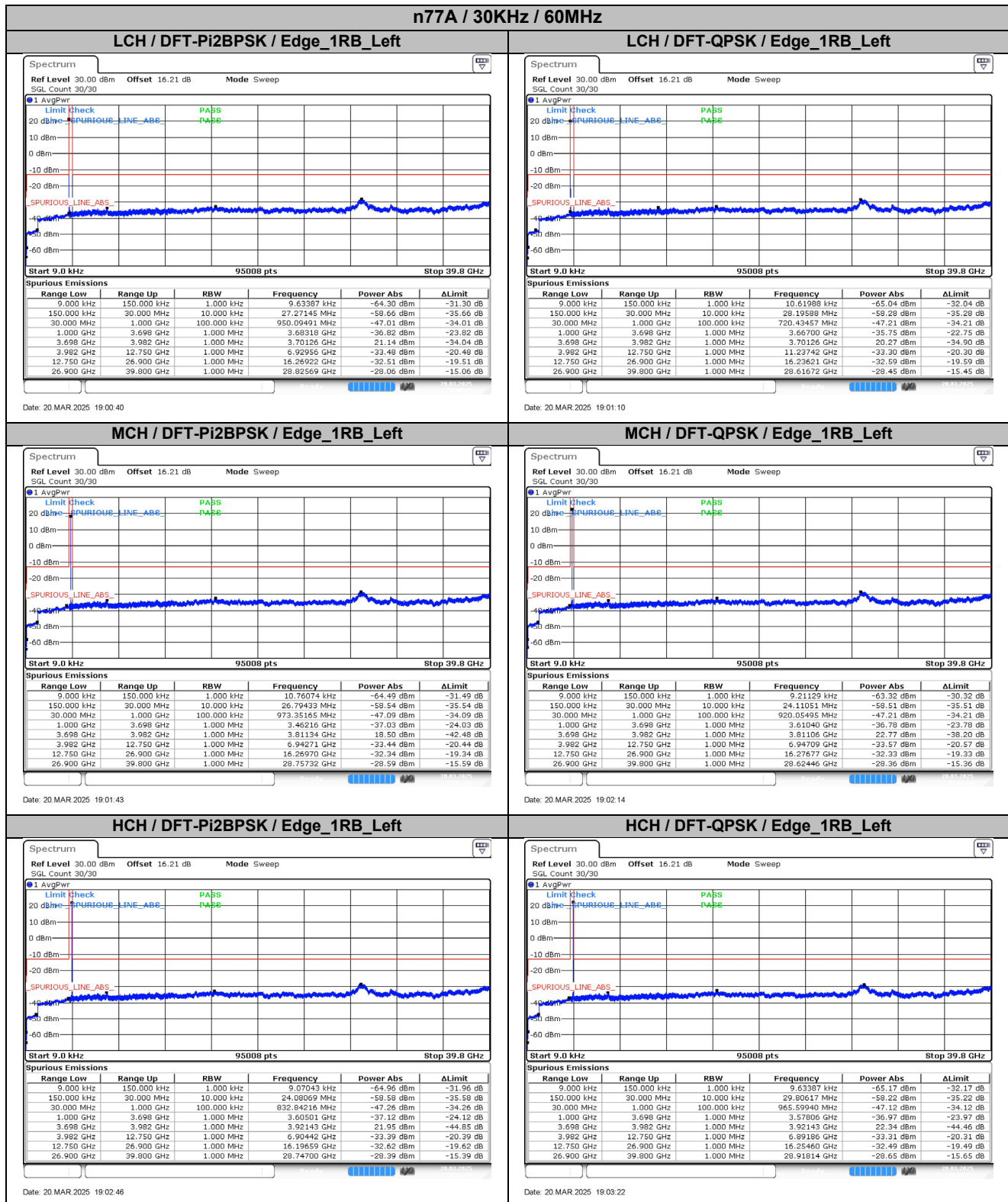
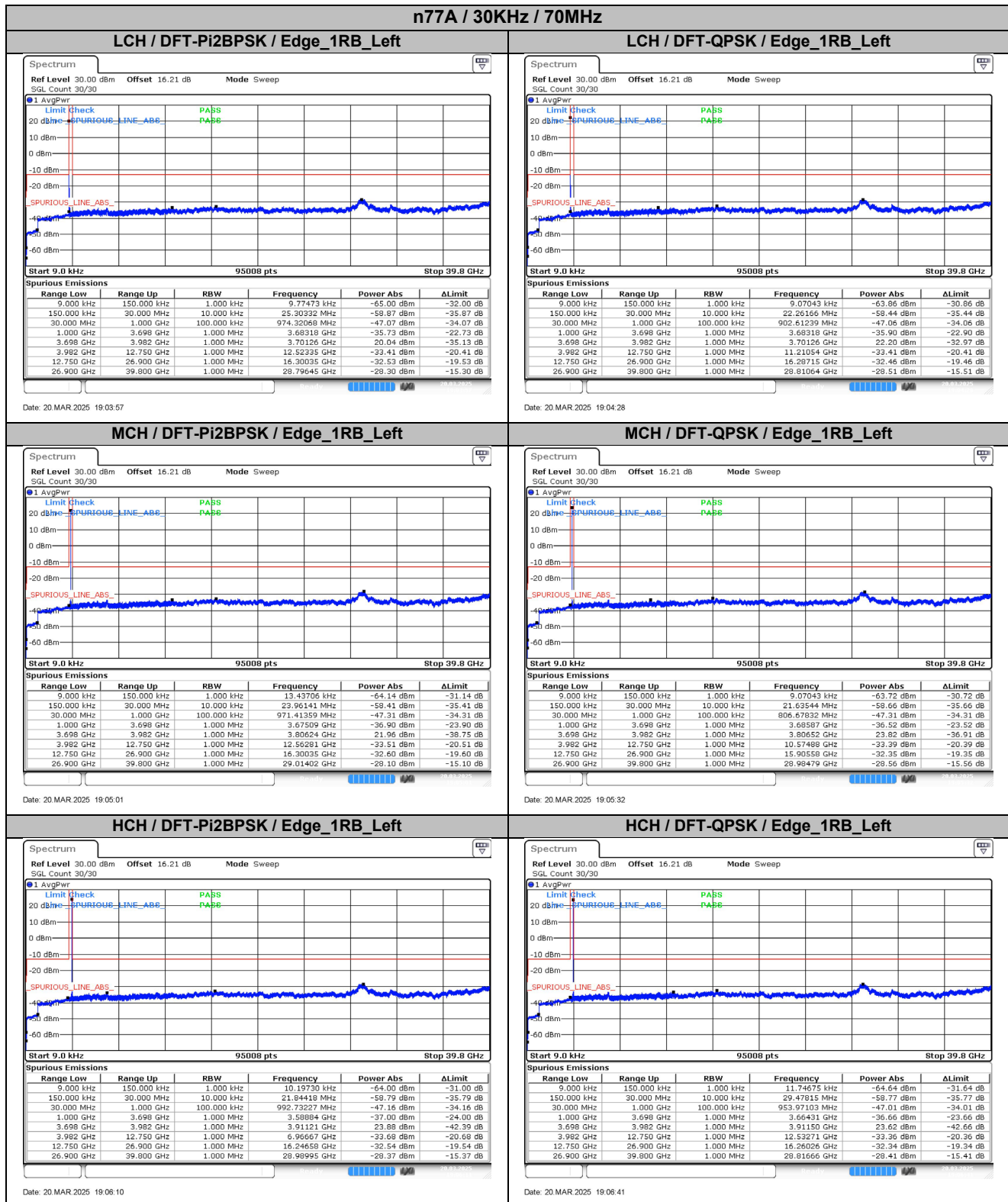


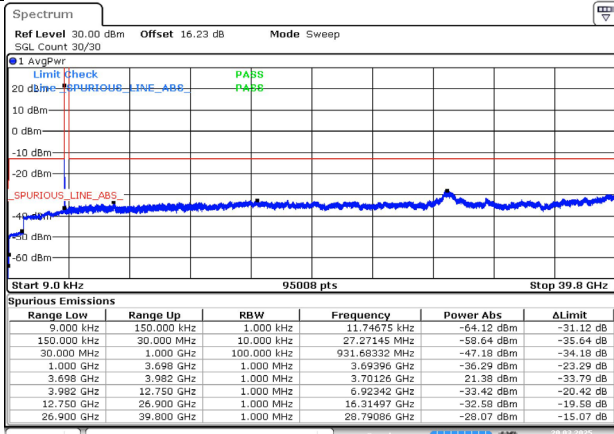






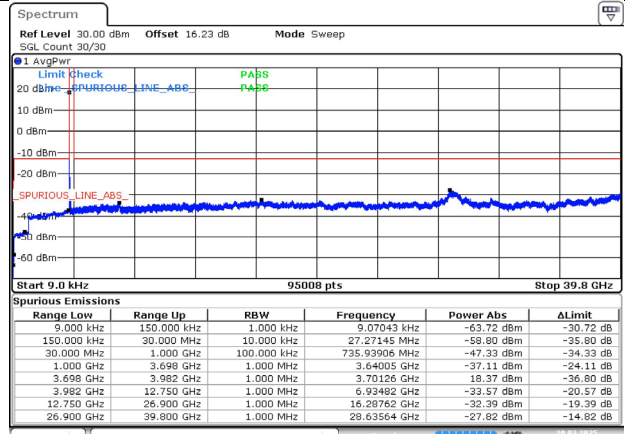
n77A / 30KHz / 80MHz

LCH / DFT-Pi2BPSK / Edge_1RB_Left



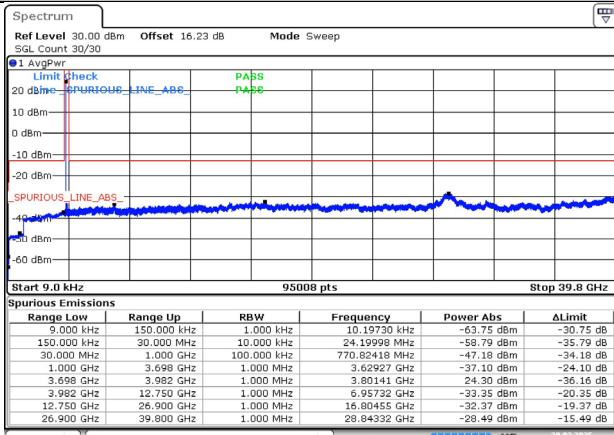
Date: 20 MAR 2025 19:07:16

LCH / DFT-QPSK / Edge_1RB_Left



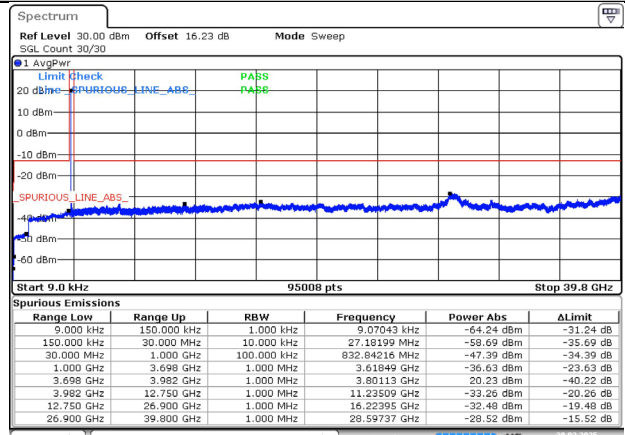
Date: 20 MAR 2025 19:07:48

MCH / DFT-Pi2BPSK / Edge_1RB_Left



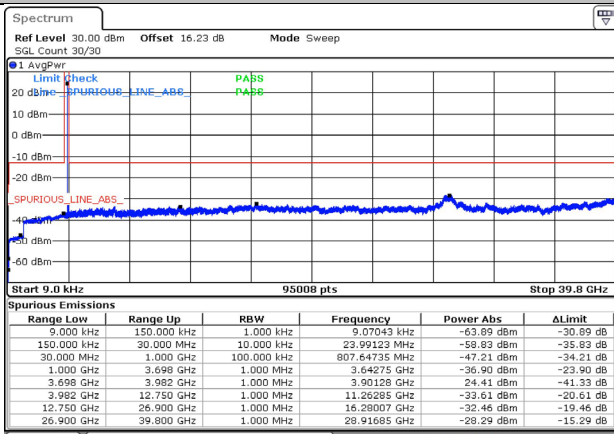
Date: 20 MAR 2025 19:08:22

MCH / DFT-QPSK / Edge_1RB_Left



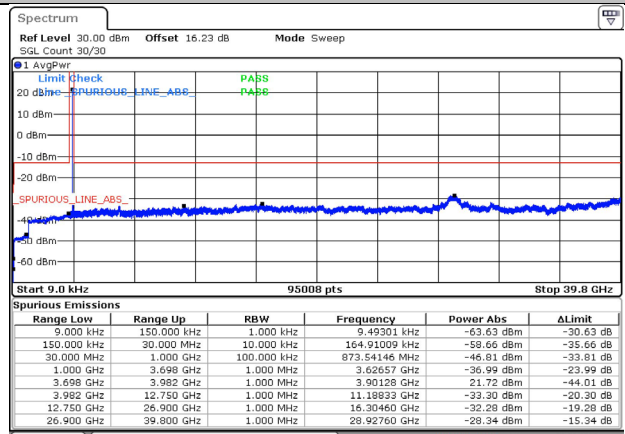
Date: 20 MAR 2025 19:08:59

HCH / DFT-Pi2BPSK / Edge_1RB_Left



Date: 20 MAR 2025 19:09:33

HCH / DFT-QPSK / Edge_1RB_Left



Date: 20 MAR 2025 19:10:04