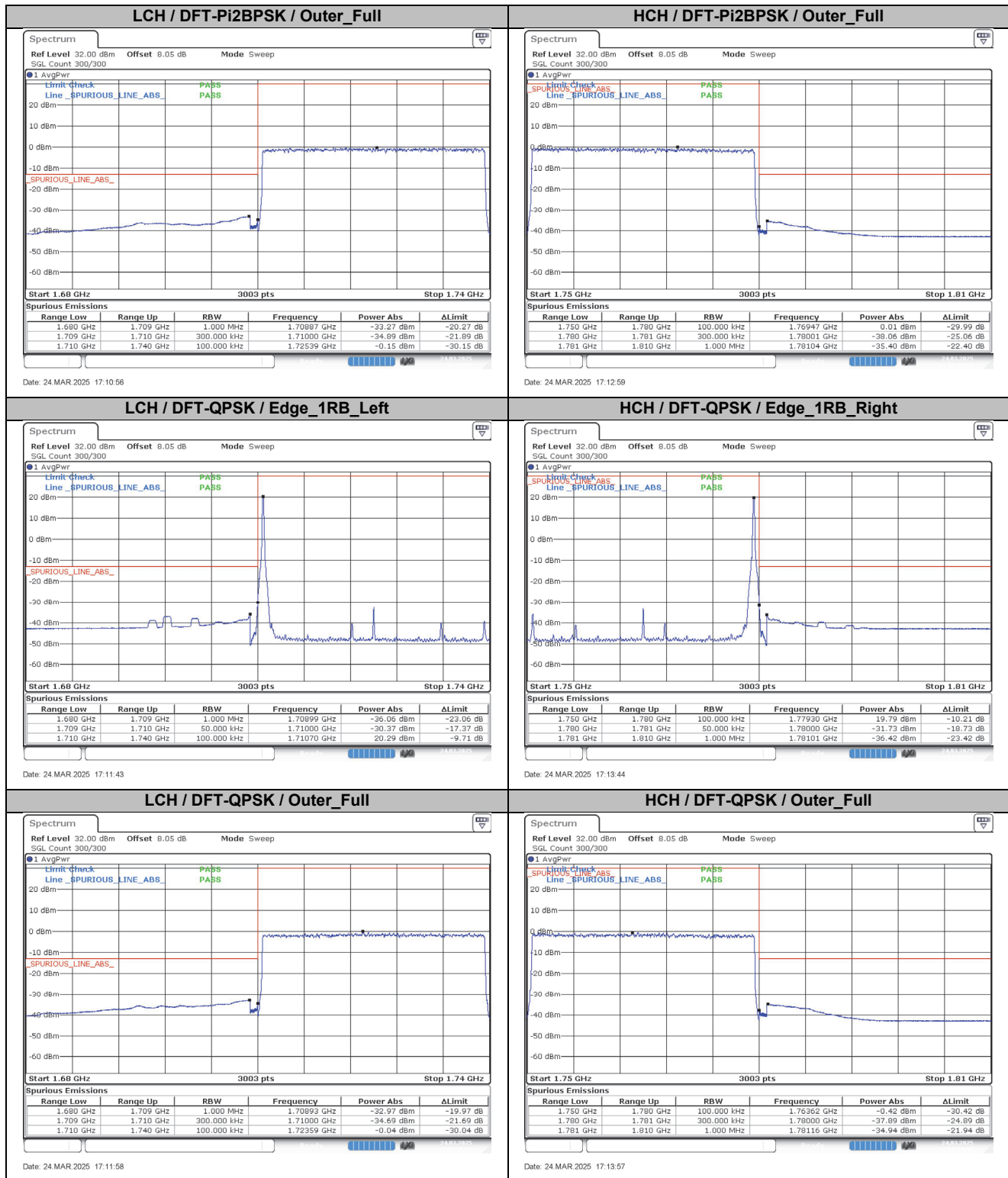
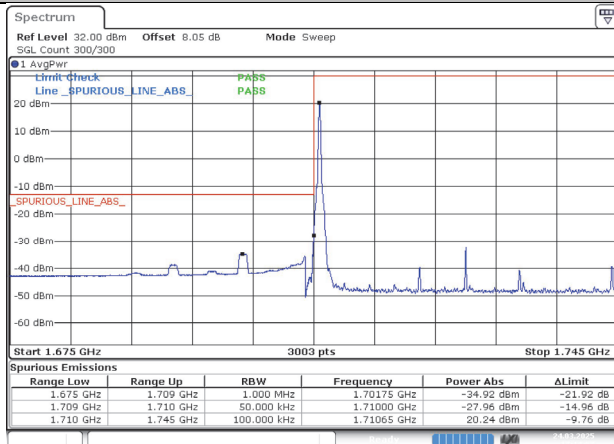




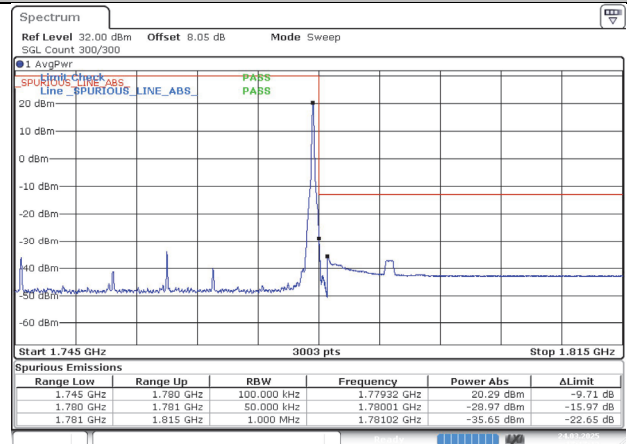
## n66A / 15KHz / 35MHz

## LCH / DFT-Pi2BPSK / Edge\_1RB\_Left



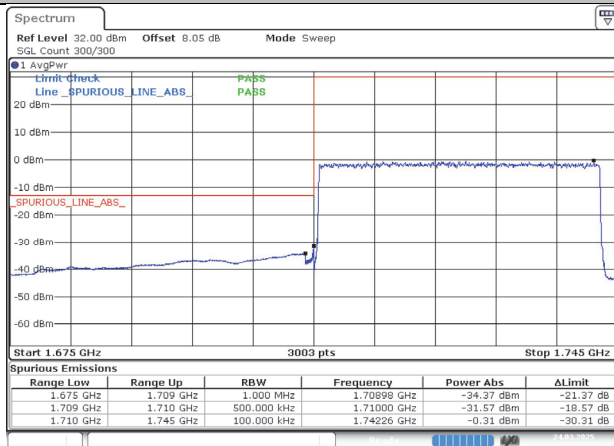
Date: 24 MAR 2025 17:14:56

## HCH / DFT-Pi2BPSK / Edge\_1RB\_Right



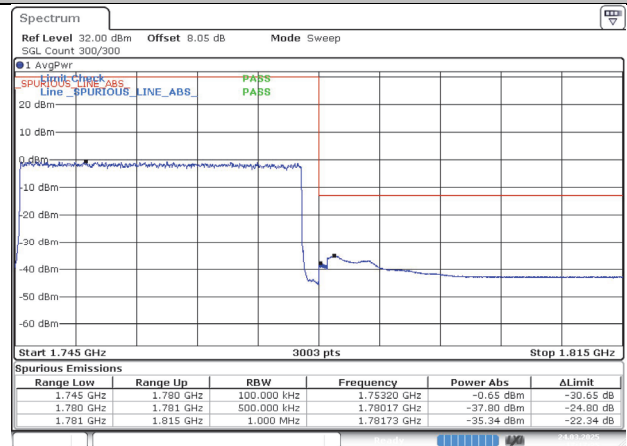
Date: 24 MAR 2025 17:17:13

## LCH / DFT-Pi2BPSK / Outer\_Full



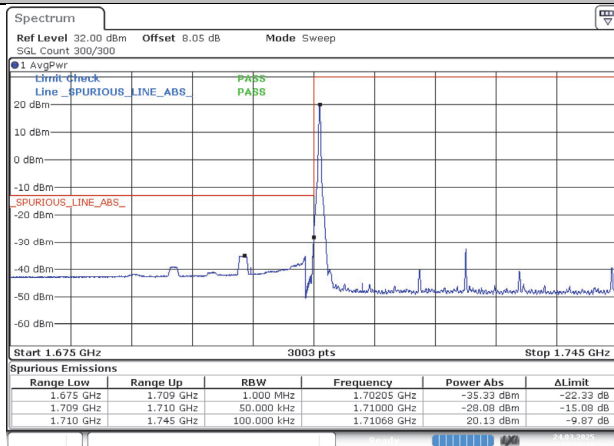
Date: 24 MAR 2025 17:15:12

## HCH / DFT-Pi2BPSK / Outer\_Full



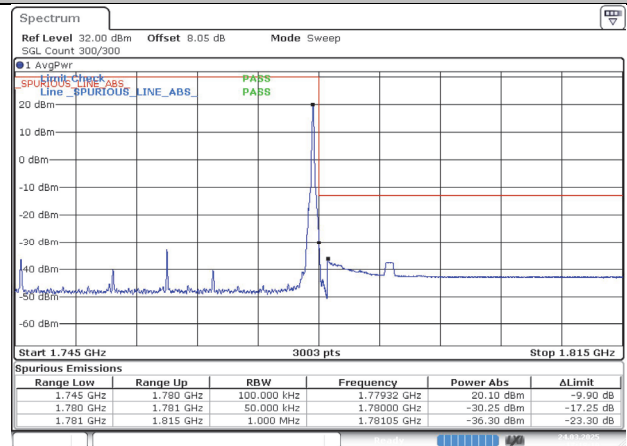
Date: 24 MAR 2025 17:17:25

## LCH / DFT-QPSK / Edge\_1RB\_Left



Date: 24 MAR 2025 17:16:10

## HCH / DFT-QPSK / Edge\_1RB\_Right



Date: 24 MAR 2025 17:18:11

