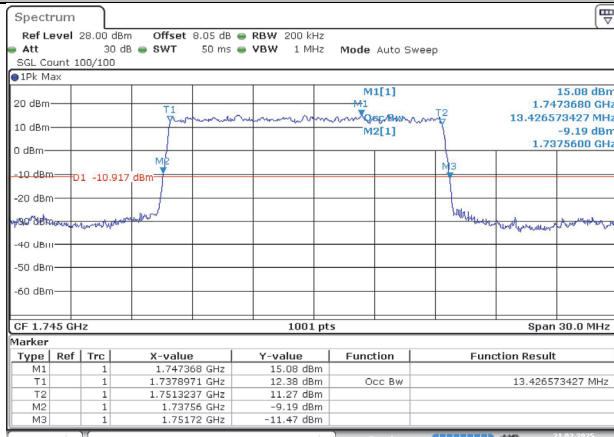


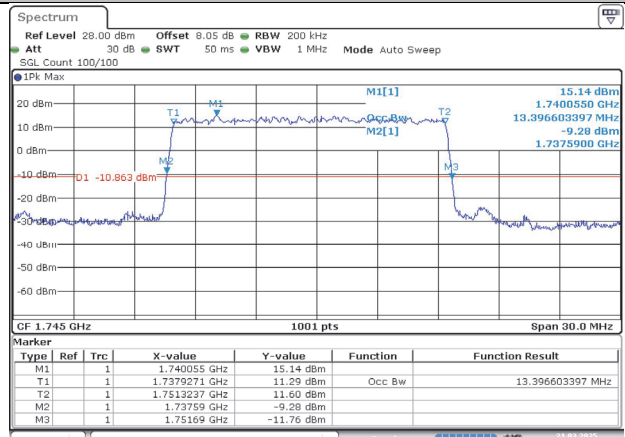
n66A / 15KHz / 15MHz

MCH / DFT-Pi2BPSK / Outer_Full



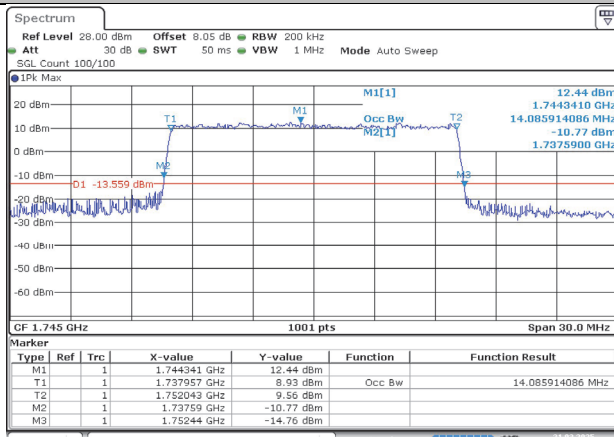
Date: 21.MAR.2025 16:24:27

MCH / DFT-QPSK / Outer_Full



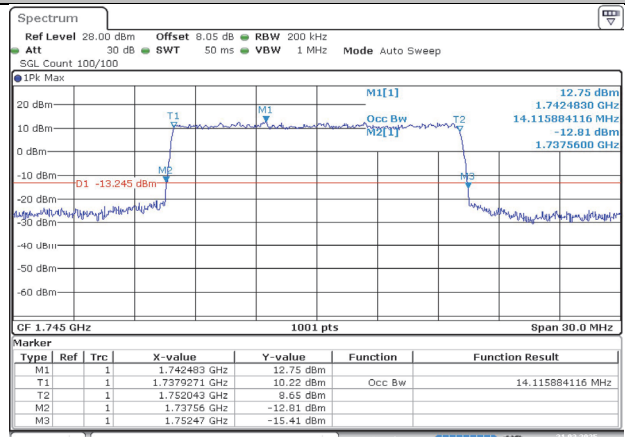
Date: 21.MAR.2025 16:24:44

MCH / CP-QPSK / Outer_Full



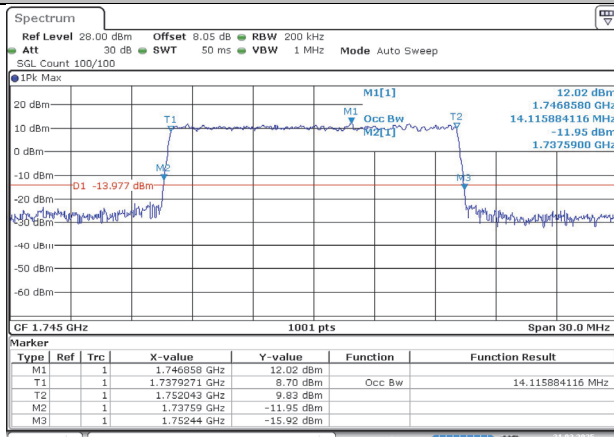
Date: 21.MAR.2025 16:25:23

MCH / CP-16QAM / Outer_Full



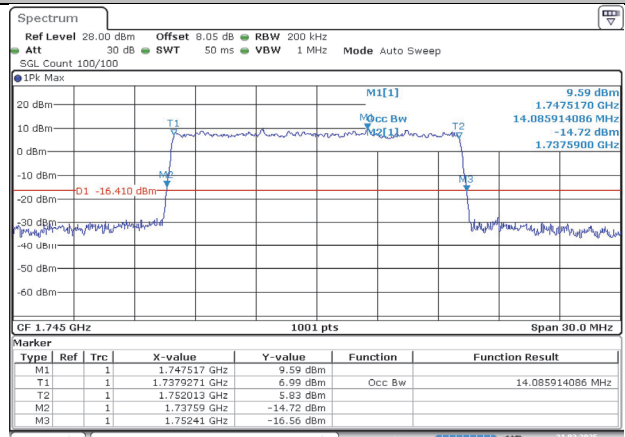
Date: 21.MAR.2025 16:25:32

MCH / CP-64QAM / Outer_Full



Date: 21.MAR.2025 16:25:40

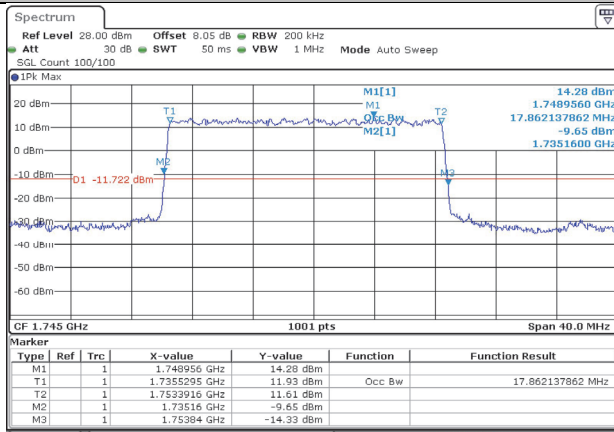
MCH / CP-256QAM / Outer_Full



Date: 21.MAR.2025 16:25:51

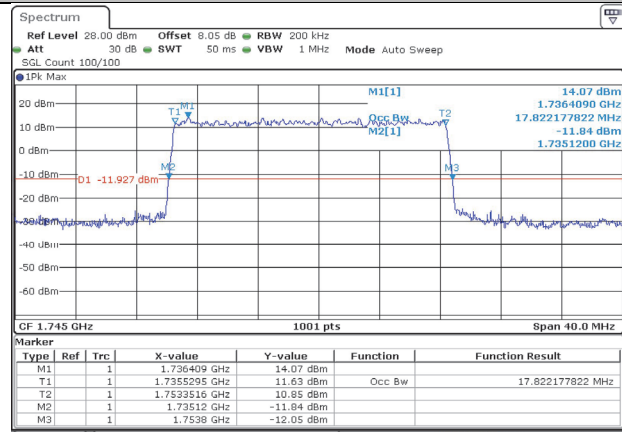
n66A / 15KHz / 20MHz

MCH / DFT-Pi2BPSK / Outer_Full



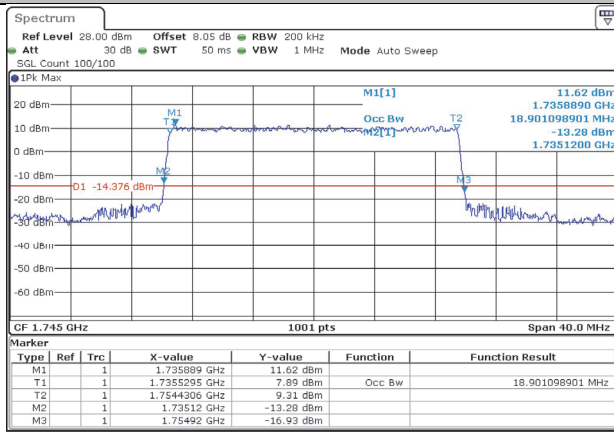
Date: 21.MAR.2025 16:26:13

MCH / DFT-QPSK / Outer_Full



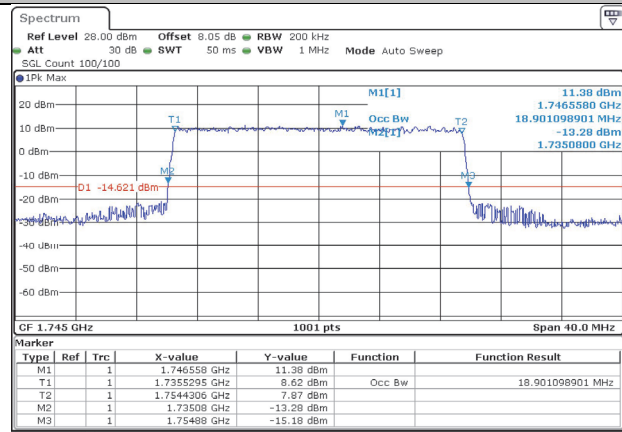
Date: 21.MAR.2025 16:26:41

MCH / CP-QPSK / Outer_Full



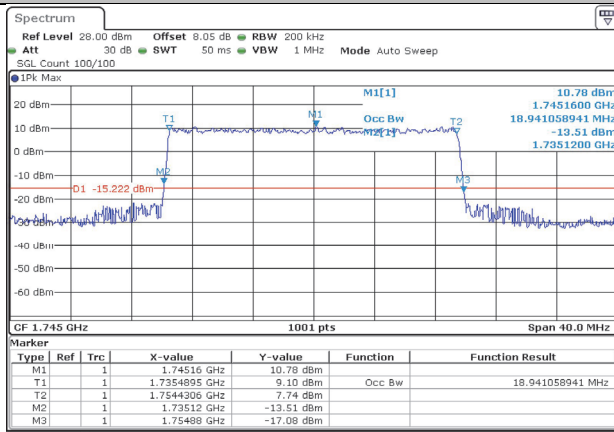
Date: 21.MAR.2025 16:27:24

MCH / CP-16QAM / Outer_Full



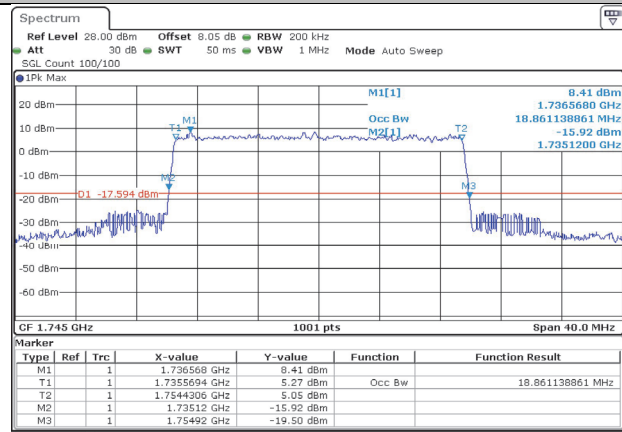
Date: 21.MAR.2025 16:27:33

MCH / CP-64QAM / Outer_Full



Date: 21.MAR.2025 16:27:42

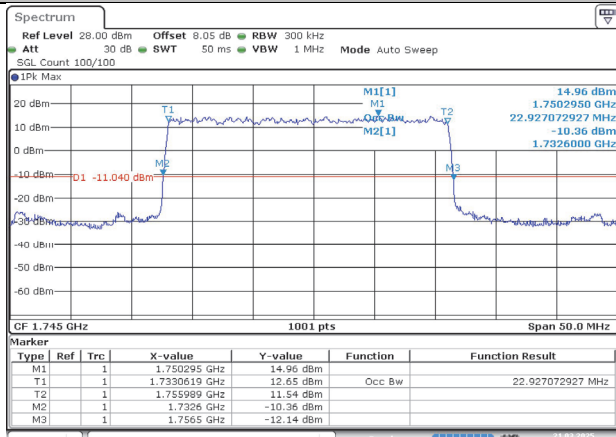
MCH / CP-256QAM / Outer_Full



Date: 21.MAR.2025 16:27:54

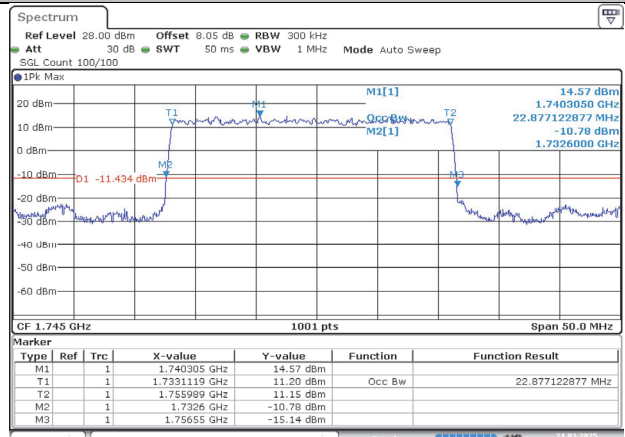
n66A / 15KHz / 25MHz

MCH / DFT-Pi2BPSK / Outer_Full



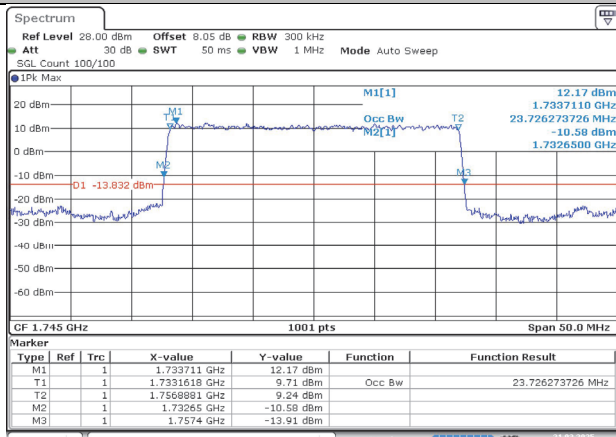
Date: 21.MAR.2025 16:28:14

MCH / DFT-QPSK / Outer_Full



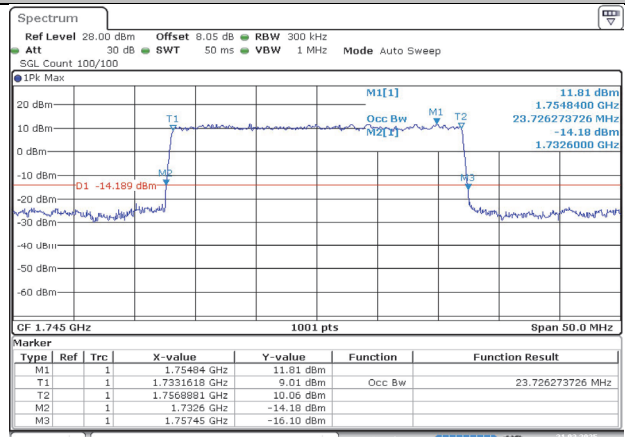
Date: 21.MAR.2025 16:28:30

MCH / CP-QPSK / Outer_Full



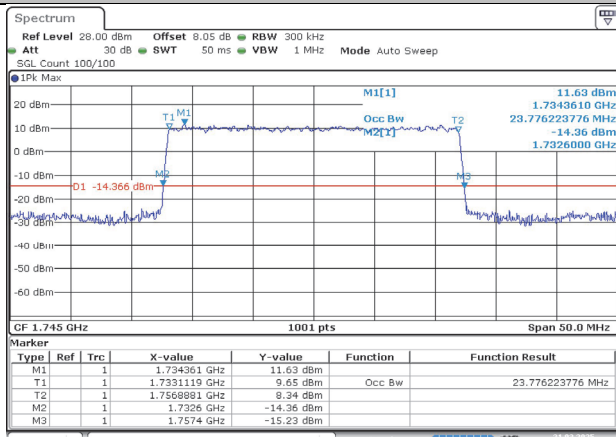
Date: 21.MAR.2025 16:29:04

MCH / CP-16QAM / Outer_Full



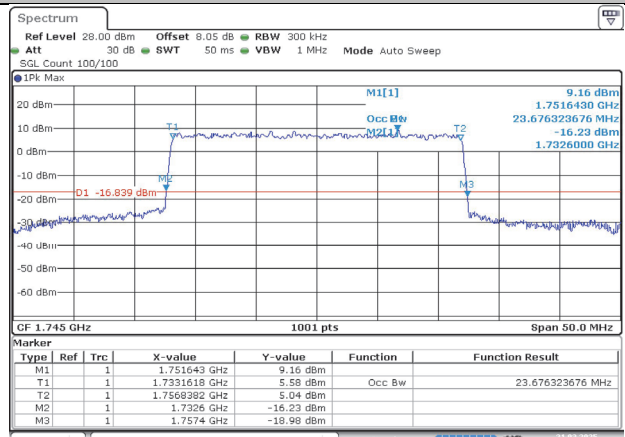
Date: 21.MAR.2025 16:29:12

MCH / CP-64QAM / Outer_Full



Date: 21.MAR.2025 16:29:19

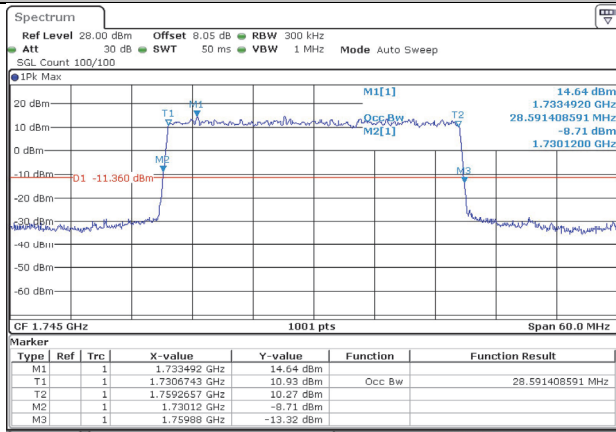
MCH / CP-256QAM / Outer_Full



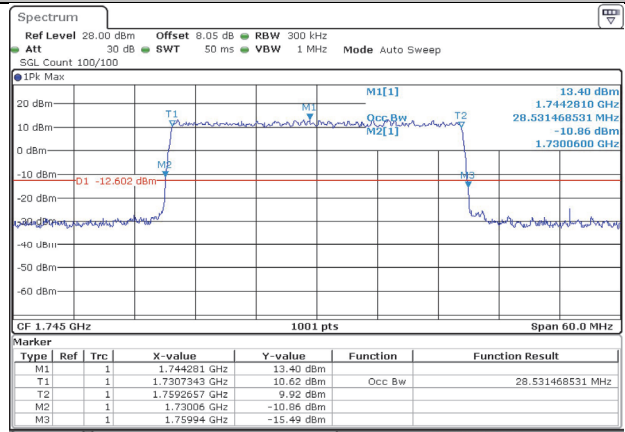
Date: 21.MAR.2025 16:29:28

n66A / 15KHz / 30MHz

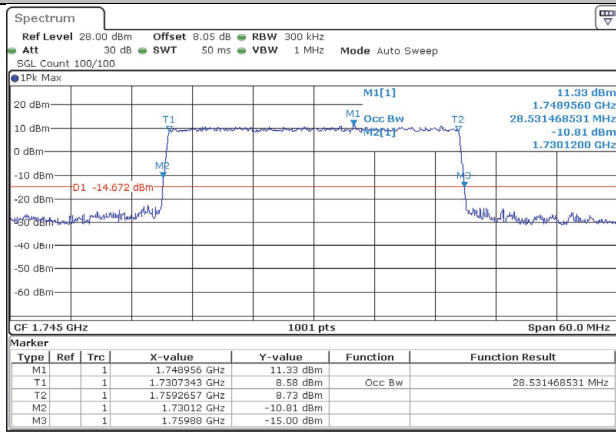
MCH / DFT-Pi2BPSK / Outer_Full



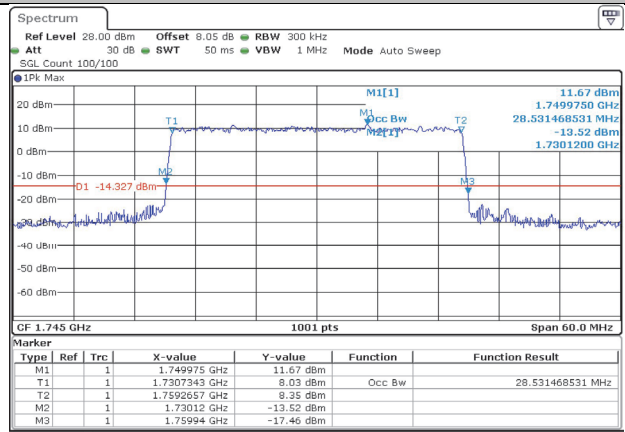
MCH / DFT-QPSK / Outer_Full



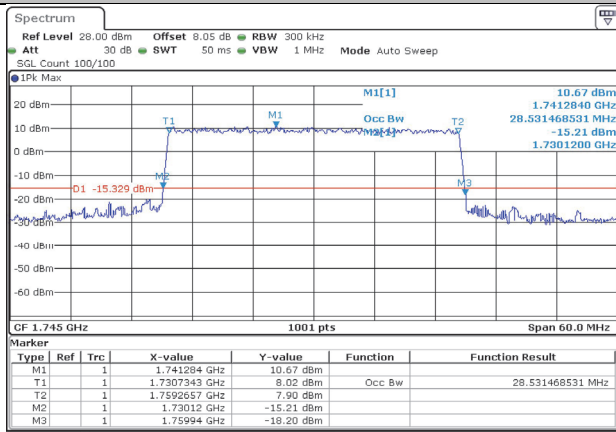
MCH / CP-QPSK / Outer_Full



MCH / CP-16QAM / Outer_Full



MCH / CP-64QAM / Outer_Full



MCH / CP-256QAM / Outer_Full

