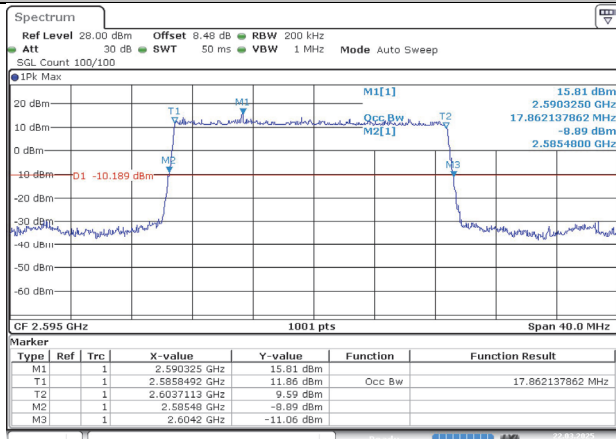


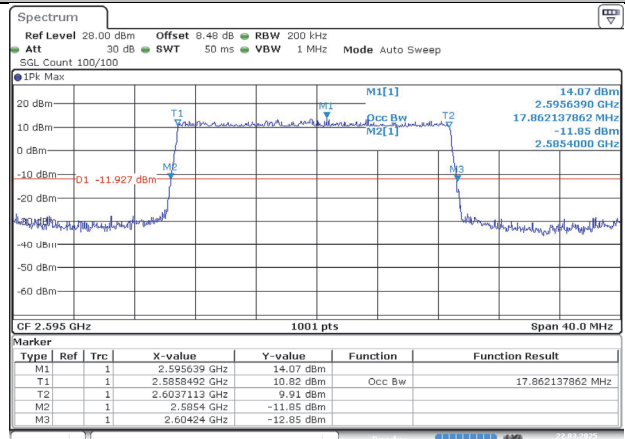
DC_66A_n38A / 30KHz / 20MHz

MCH / DFT-Pi2BPSK / Outer_Full



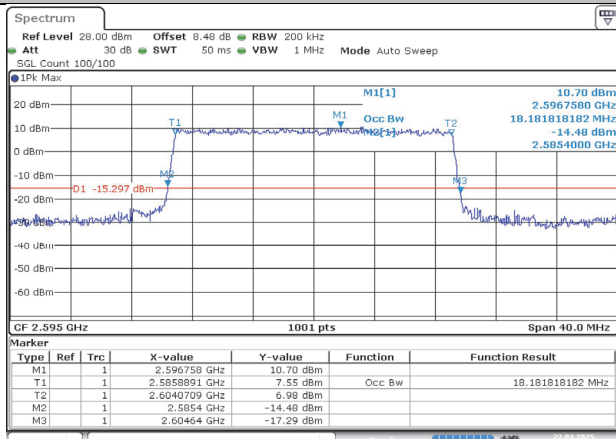
Date: 22 MAR 2025 04:18:10

MCH / DFT-QPSK / Outer_Full



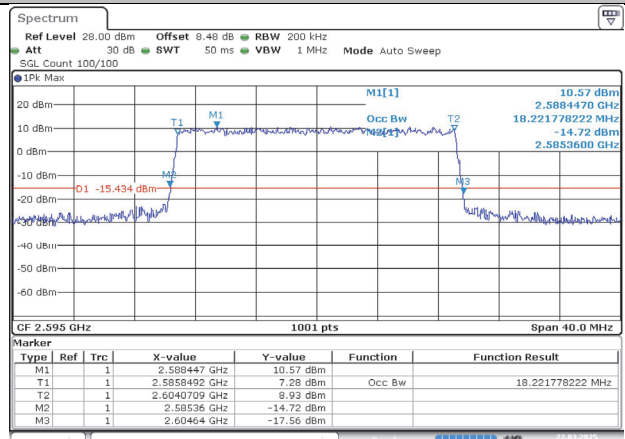
Date: 22 MAR 2025 04:18:21

MCH / CP-QPSK / Outer_Full



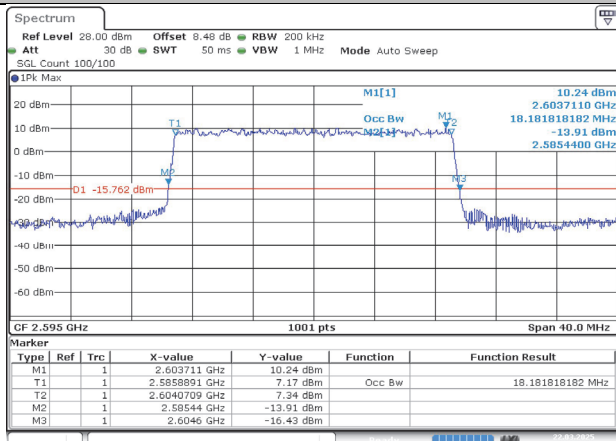
Date: 22 MAR 2025 04:19:01

MCH / CP-16QAM / Outer_Full



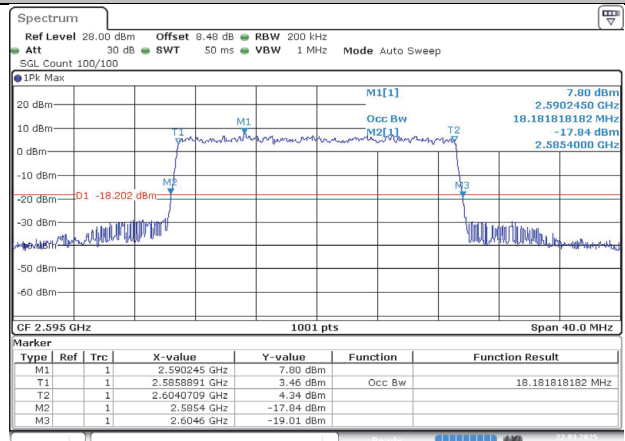
Date: 22 MAR 2025 04:19:10

MCH / CP-64QAM / Outer_Full



Date: 22 MAR 2025 04:19:19

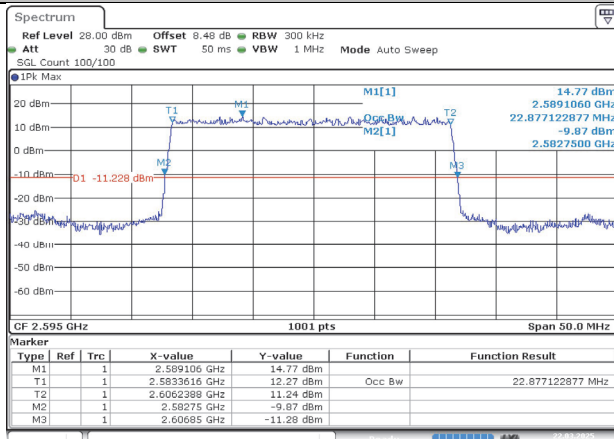
MCH / CP-256QAM / Outer_Full



Date: 22 MAR 2025 04:19:29

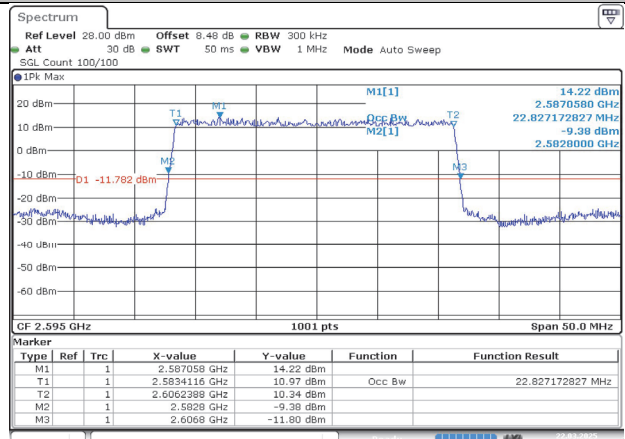
DC_66A_n38A / 30KHz / 25MHz

MCH / DFT-Pi2BPSK / Outer_Full



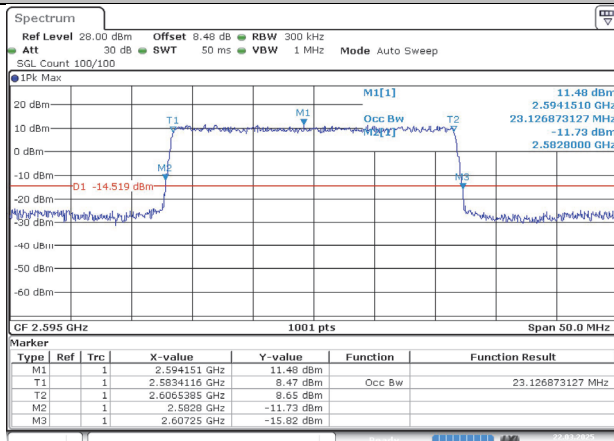
Date: 22 MAR 2025 04:19:41

MCH / DFT-QPSK / Outer_Full



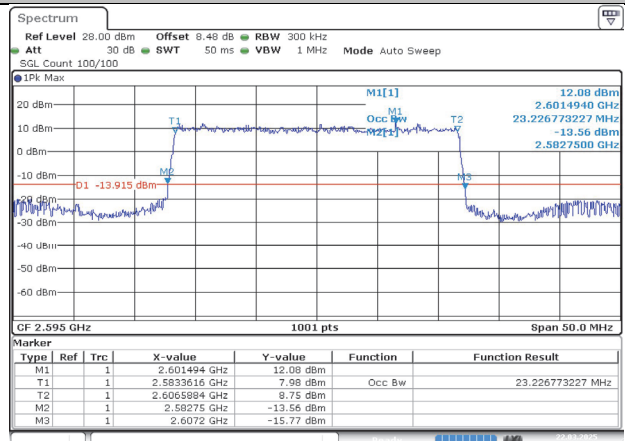
Date: 22 MAR 2025 04:19:52

MCH / CP-QPSK / Outer_Full



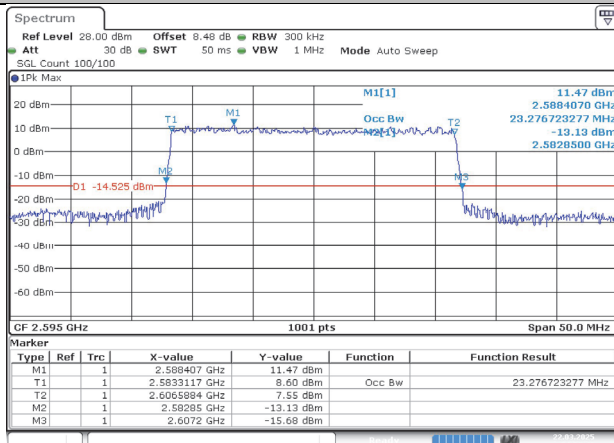
Date: 22 MAR 2025 04:20:25

MCH / CP-16QAM / Outer_Full



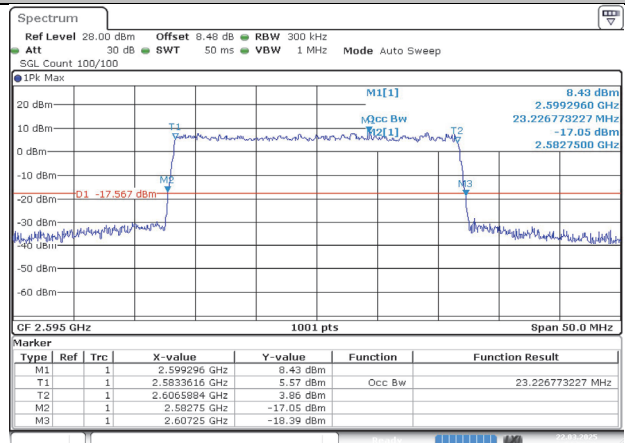
Date: 22 MAR 2025 04:20:33

MCH / CP-64QAM / Outer_Full



Date: 22 MAR 2025 04:20:41

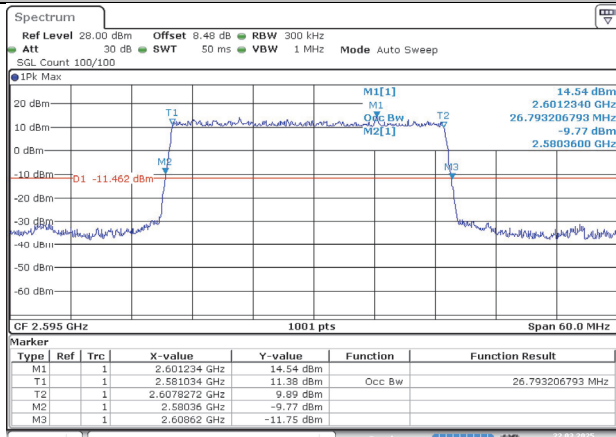
MCH / CP-256QAM / Outer_Full



Date: 22 MAR 2025 04:20:50

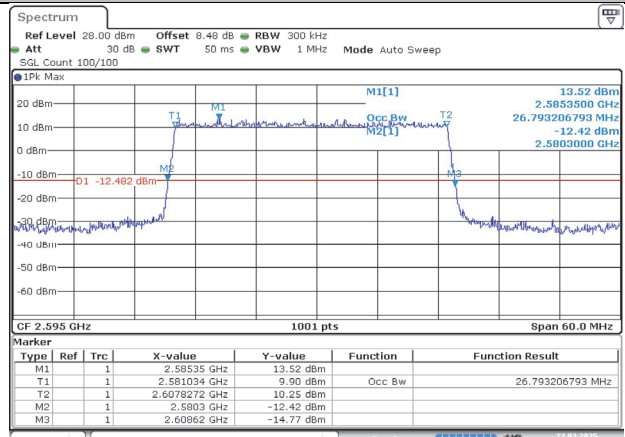
DC_66A_n38A / 30KHz / 30MHz

MCH / DFT-Pi2BPSK / Outer_Full



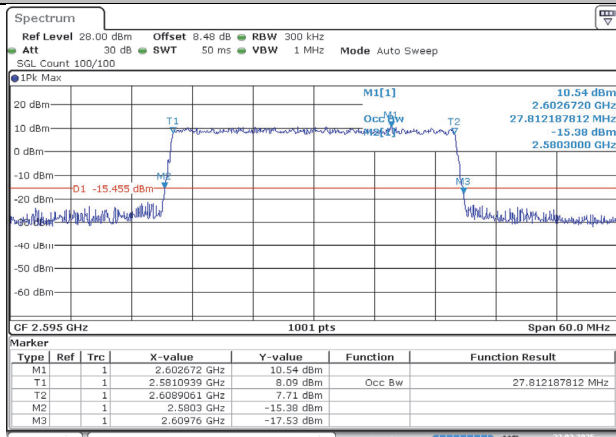
Date: 22 MAR 2025 04:21:02

MCH / DFT-QPSK / Outer_Full



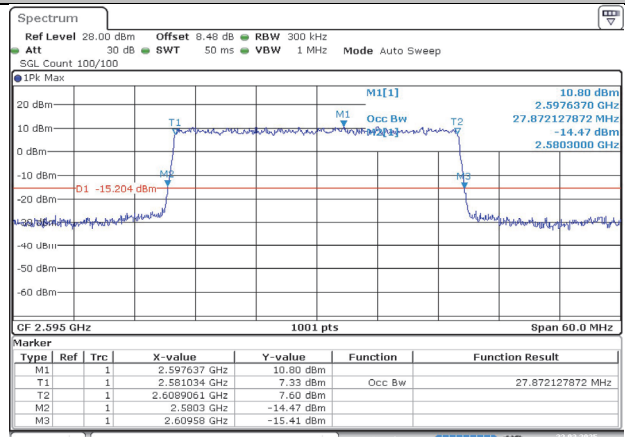
Date: 22 MAR 2025 04:21:12

MCH / CP-QPSK / Outer_Full



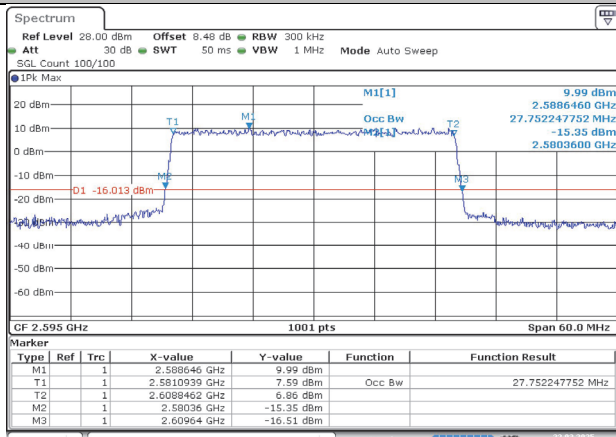
Date: 22 MAR 2025 04:21:46

MCH / CP-16QAM / Outer_Full



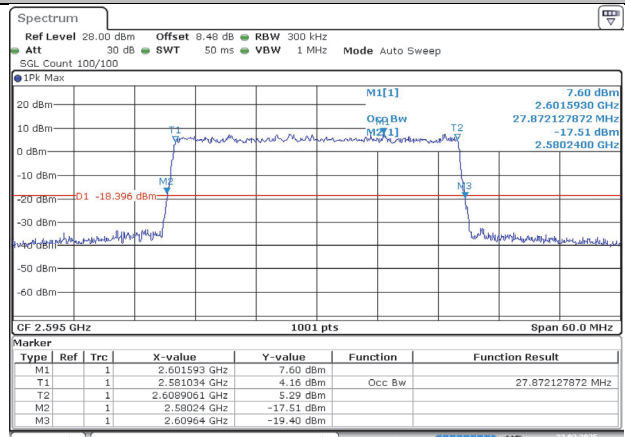
Date: 22 MAR 2025 04:21:54

MCH / CP-64QAM / Outer_Full



Date: 22 MAR 2025 04:22:02

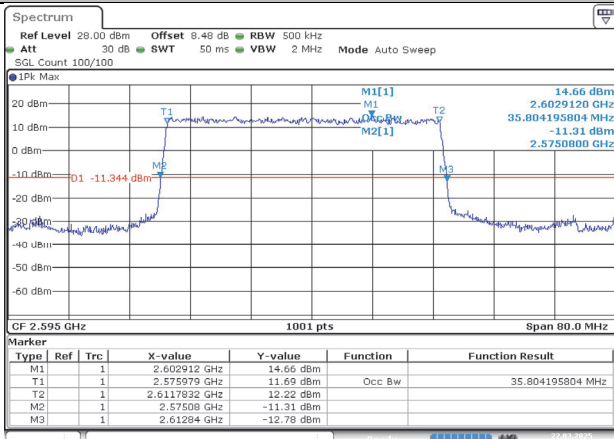
MCH / CP-256QAM / Outer_Full



Date: 22 MAR 2025 04:22:10

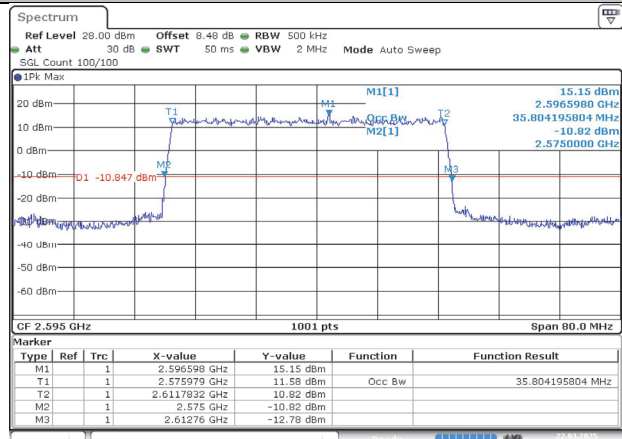
DC_66A_n38A / 30KHz / 40MHz

MCH / DFT-Pi2BPSK / Outer_Full



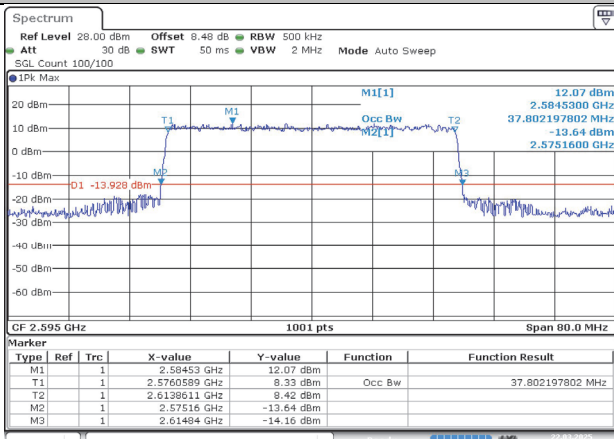
Date: 22 MAR 2025 04:22:23

MCH / DFT-QPSK / Outer_Full



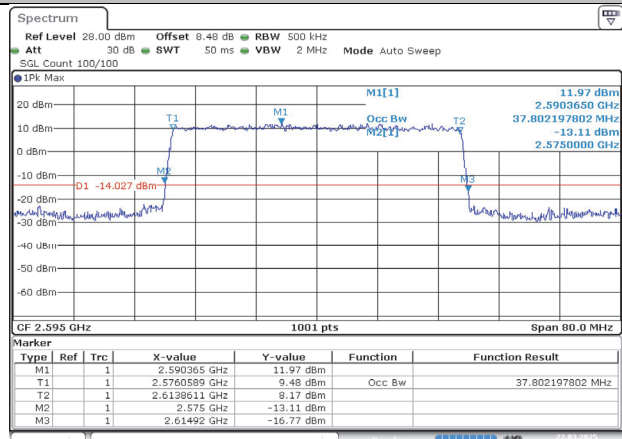
Date: 22 MAR 2025 04:22:33

MCH / CP-QPSK / Outer_Full



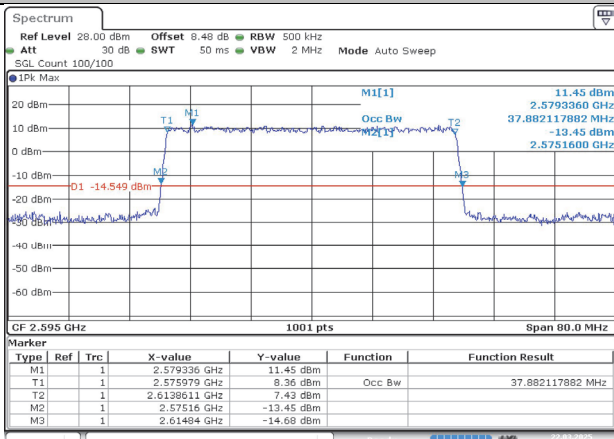
Date: 22 MAR 2025 04:23:07

MCH / CP-16QAM / Outer_Full



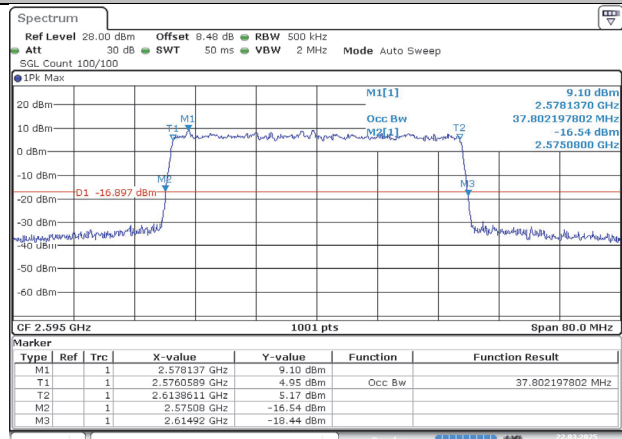
Date: 22 MAR 2025 04:23:15

MCH / CP-64QAM / Outer_Full



Date: 22 MAR 2025 04:23:23

MCH / CP-256QAM / Outer_Full



Date: 22 MAR 2025 04:23:32