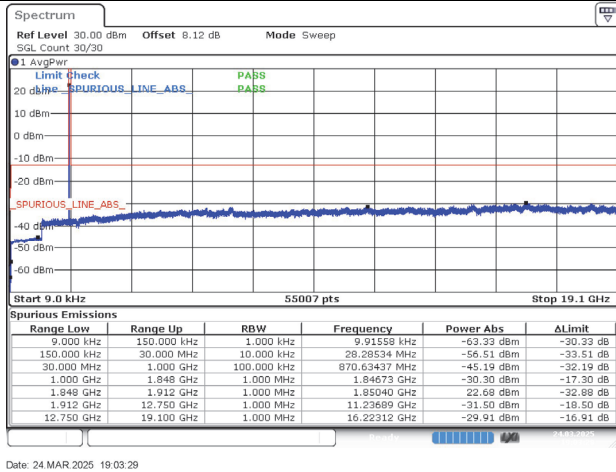
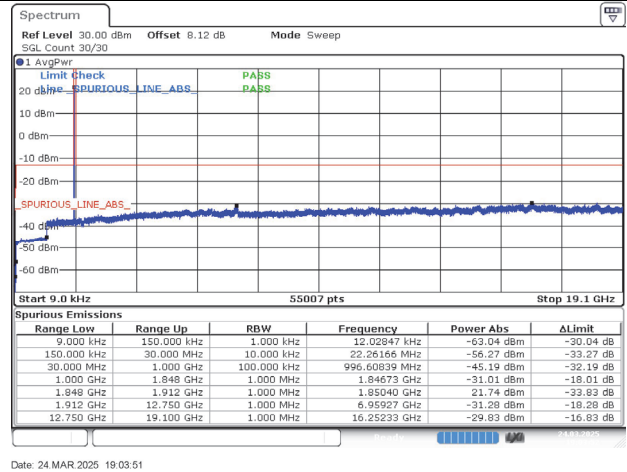


n2A / 15KHz / 15MHz

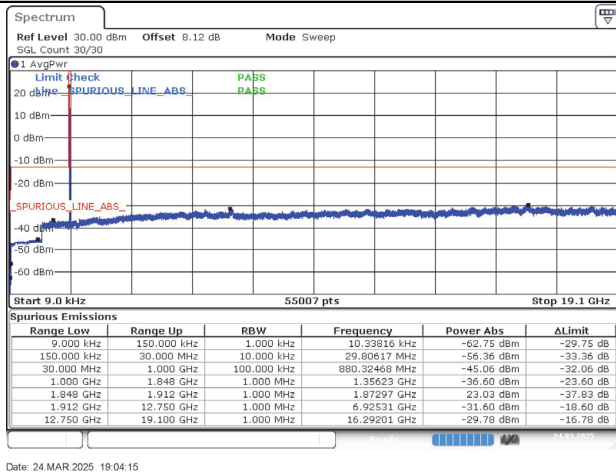
LCH / DFT-Pi2BPSK / Edge_1RB_Left



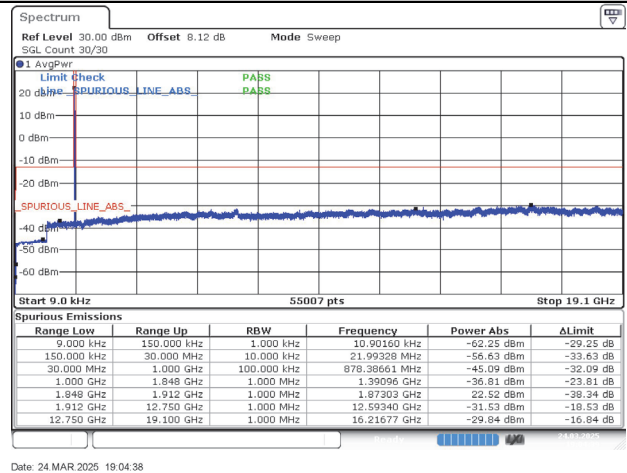
LCH / DFT-QPSK / Edge_1RB_Left



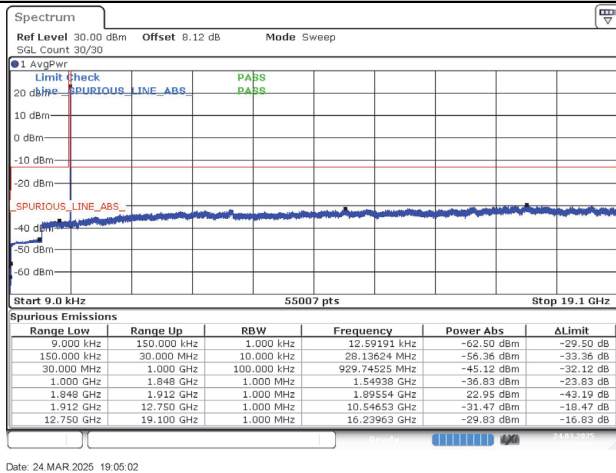
MCH / DFT-Pi2BPSK / Edge_1RB_Left



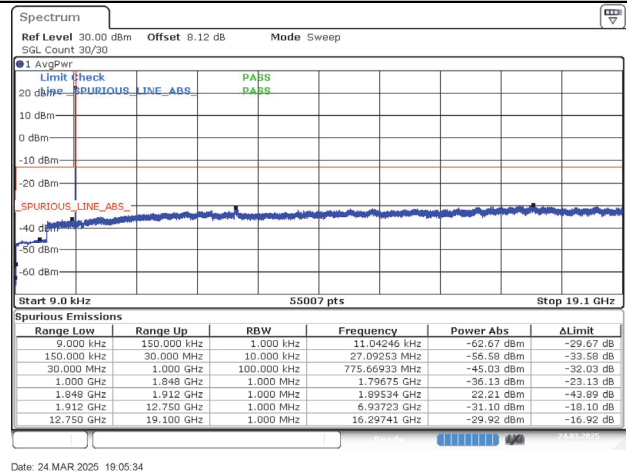
MCH / DFT-QPSK / Edge_1RB_Left



HCH / DFT-Pi2BPSK / Edge_1RB_Left

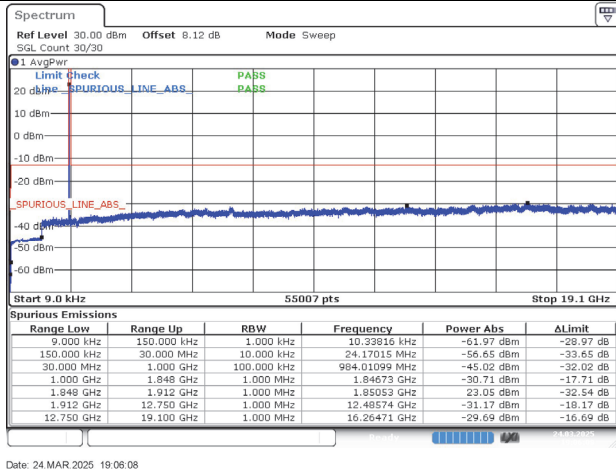


HCH / DFT-QPSK / Edge_1RB_Left

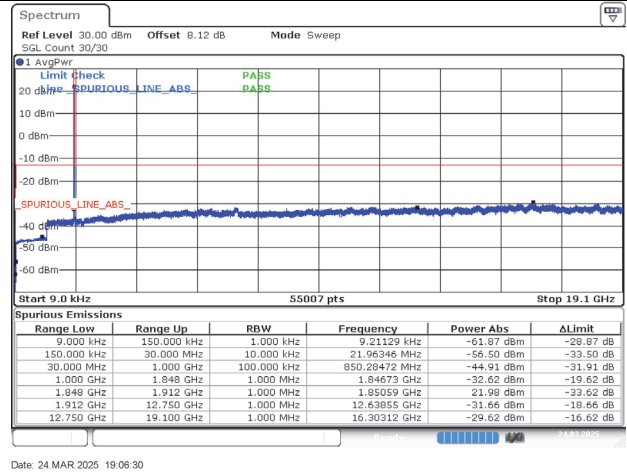


n2A / 15KHz / 20MHz

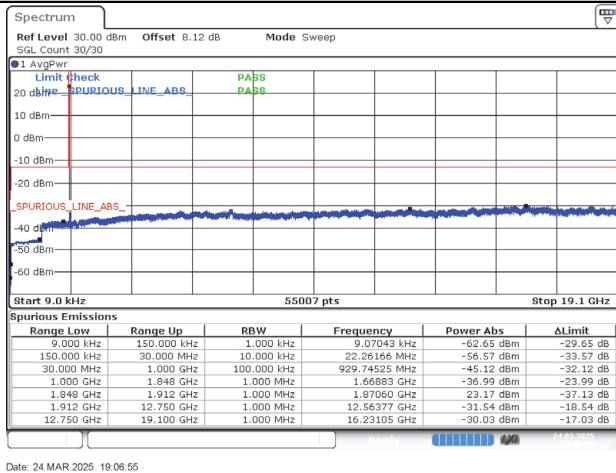
LCH / DFT-Pi2BPSK / Edge_1RB_Left



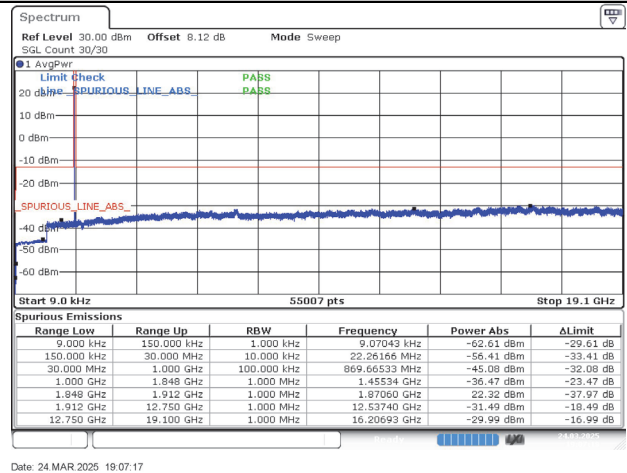
LCH / DFT-QPSK / Edge_1RB_Left



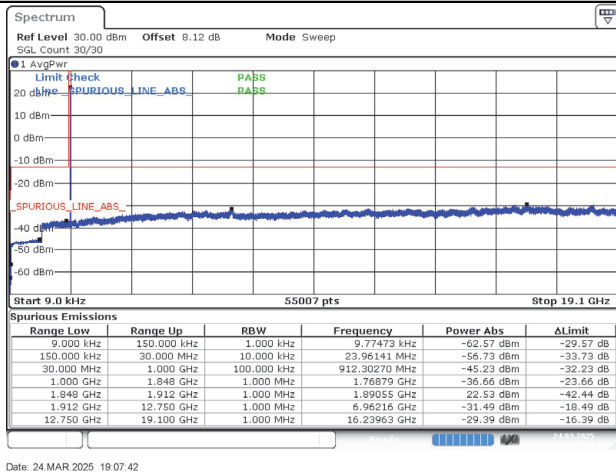
MCH / DFT-Pi2BPSK / Edge_1RB_Left



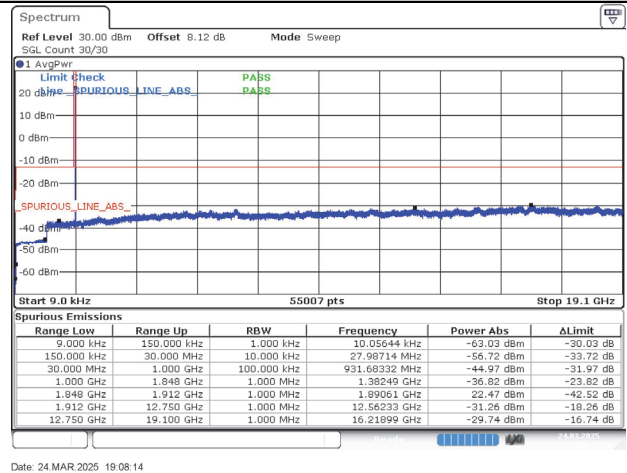
MCH / DFT-QPSK / Edge_1RB_Left



HCH / DFT-Pi2BPSK / Edge_1RB_Left

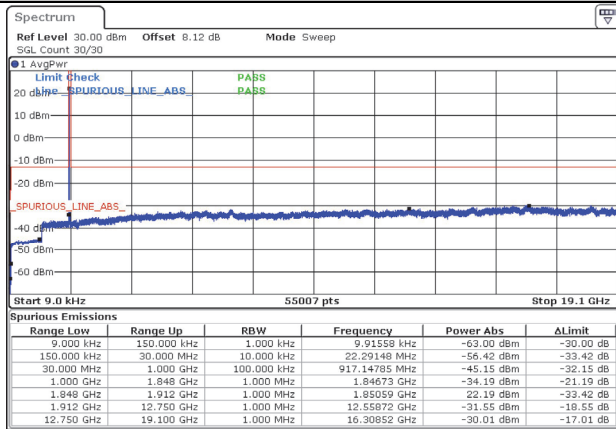


HCH / DFT-QPSK / Edge_1RB_Left



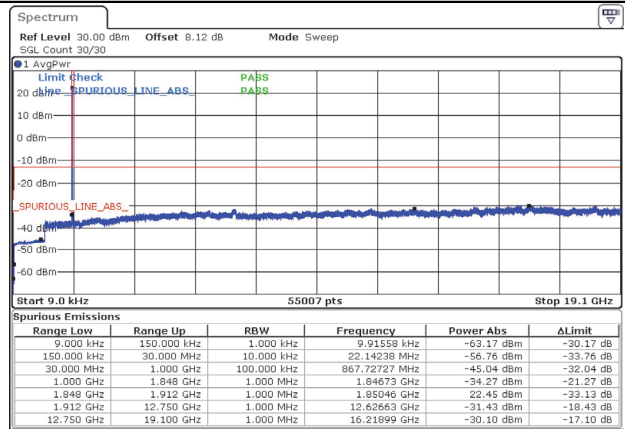
n2A / 15KHz / 25MHz

LCH / DFT-Pi2BPSK / Edge_1RB_Left



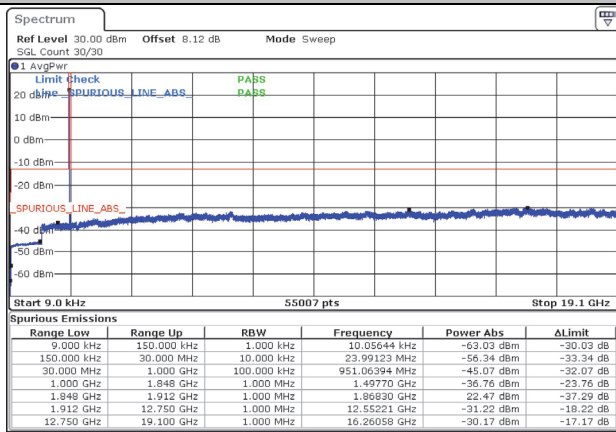
Date: 24 MAR 2025 19:09:04

LCH / DFT-QPSK / Edge_1RB_Left



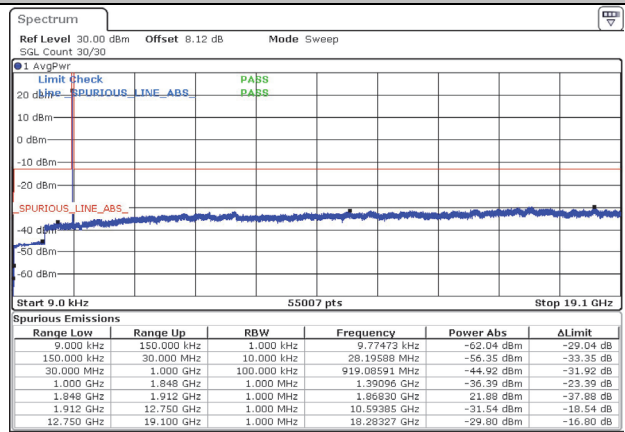
Date: 24 MAR 2025 19:09:27

MCH / DFT-Pi2BPSK / Edge_1RB_Left



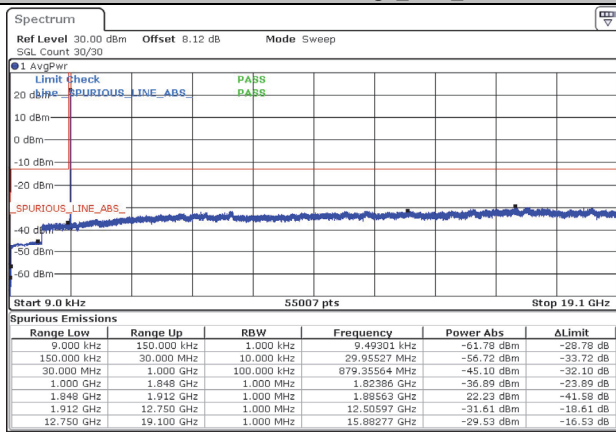
Date: 24 MAR 2025 19:09:51

MCH / DFT-QPSK / Edge_1RB_Left



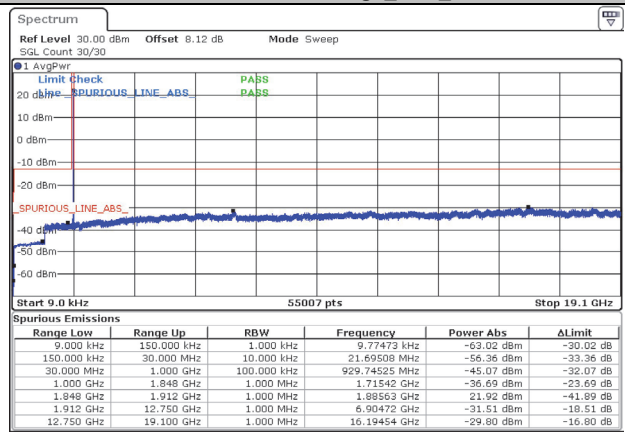
Date: 24 MAR 2025 19:10:14

HCH / DFT-Pi2BPSK / Edge_1RB_Left



Date: 24 MAR 2025 19:10:35

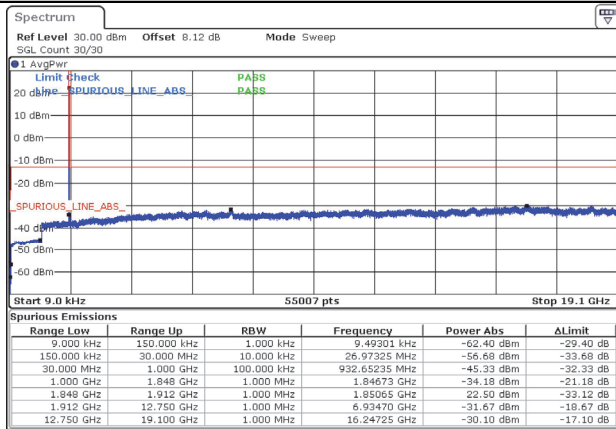
HCH / DFT-QPSK / Edge_1RB_Left



Date: 24 MAR 2025 19:11:07

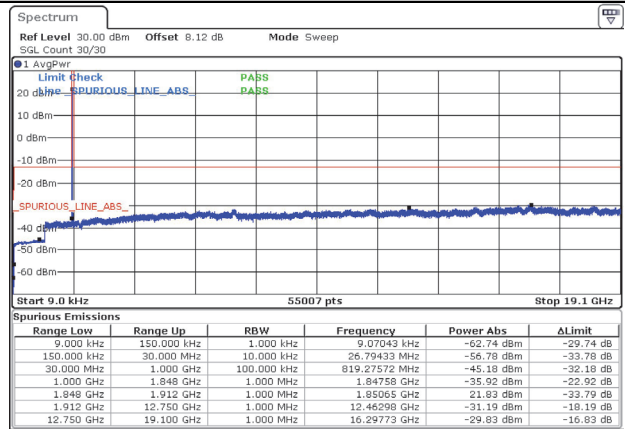
n2A / 15KHz / 30MHz

LCH / DFT-Pi2BPSK / Edge_1RB_Left



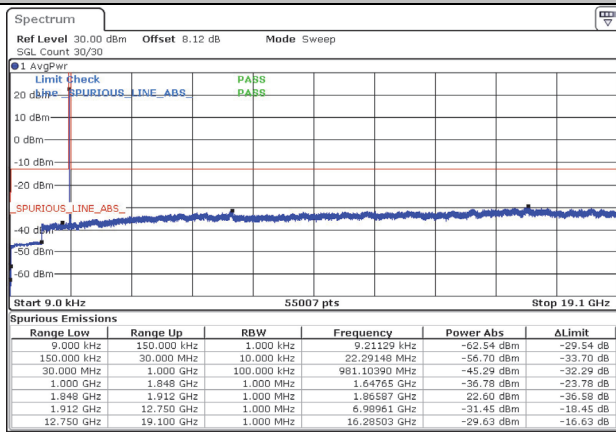
Date: 24 MAR 2025 19:11:41

LCH / DFT-QPSK / Edge_1RB_Left



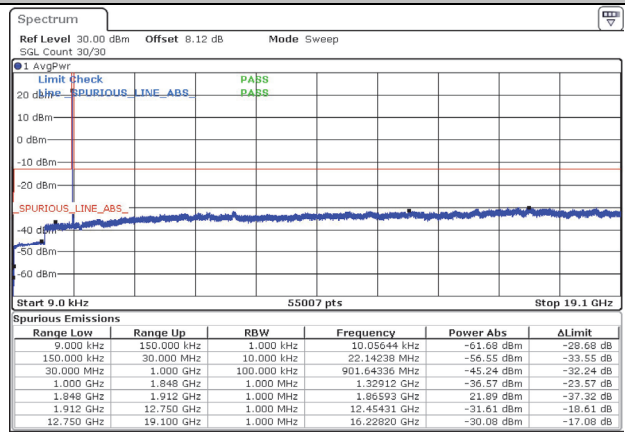
Date: 24 MAR 2025 19:12:04

MCH / DFT-Pi2BPSK / Edge_1RB_Left



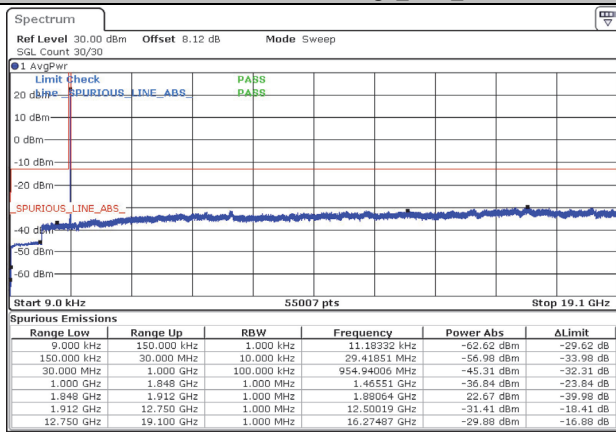
Date: 24 MAR 2025 19:12:37

MCH / DFT-QPSK / Edge_1RB_Left



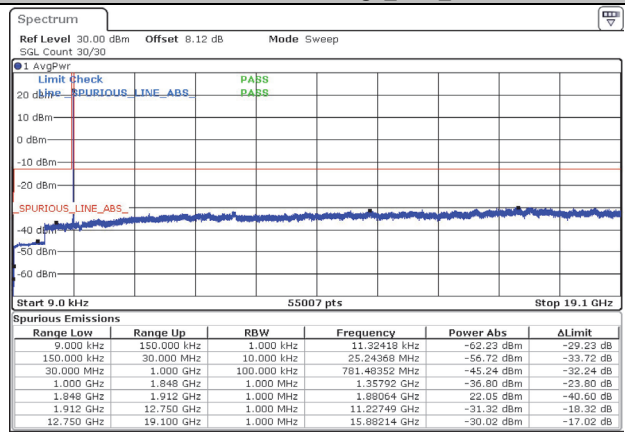
Date: 24 MAR 2025 19:13:10

HCH / DFT-Pi2BPSK / Edge_1RB_Left



Date: 24 MAR 2025 19:13:33

HCH / DFT-QPSK / Edge_1RB_Left



Date: 24 MAR 2025 19:13:56