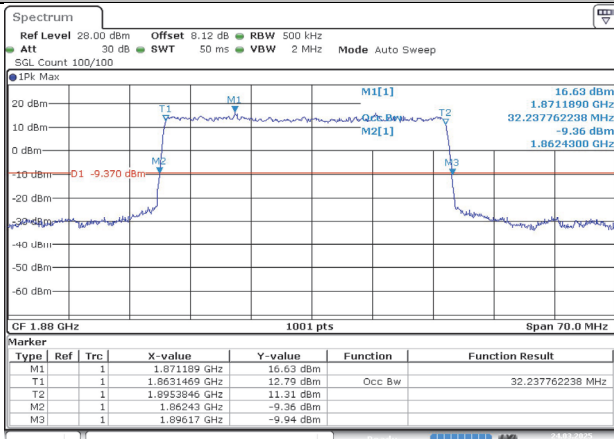


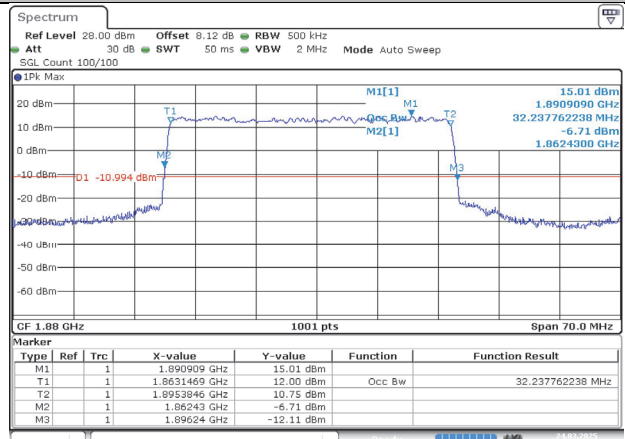
n2A / 15KHz / 35MHz

MCH / DFT-Pi2BPSK / Outer_Full



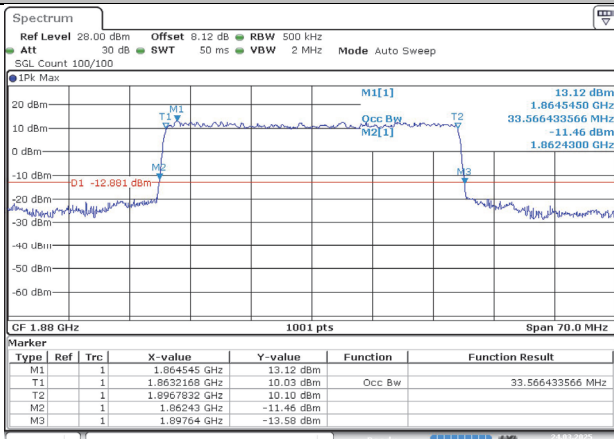
Date: 24 MAR 2025 18:16:42

MCH / DFT-QPSK / Outer_Full



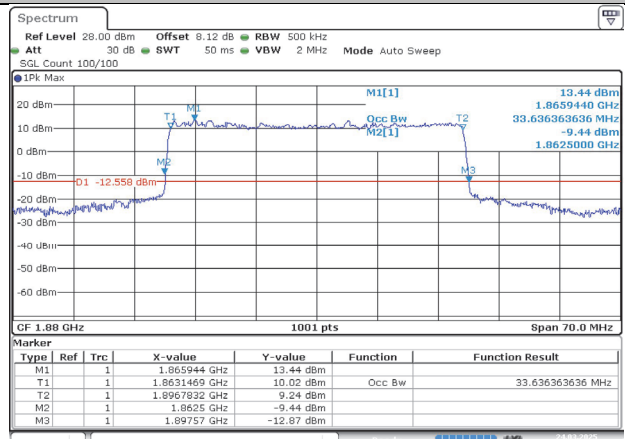
Date: 24 MAR 2025 18:16:58

MCH / CP-QPSK / Outer_Full



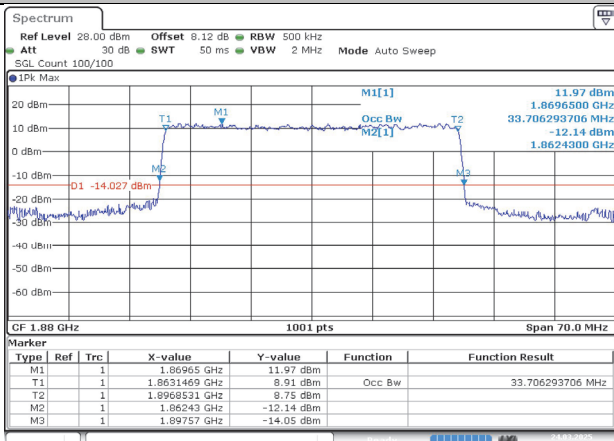
Date: 24 MAR 2025 18:17:34

MCH / CP-16QAM / Outer_Full



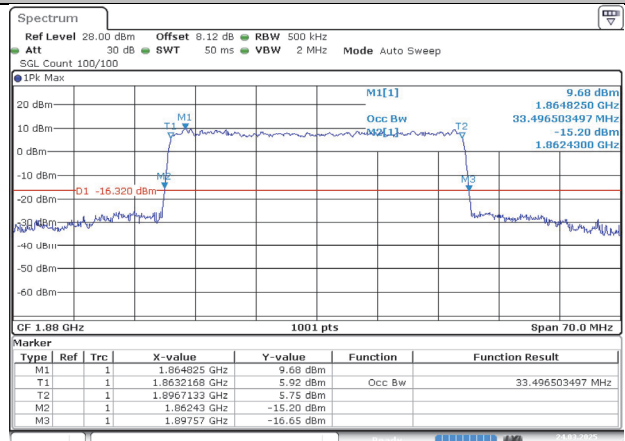
Date: 24 MAR 2025 18:17:42

MCH / CP-64QAM / Outer_Full



Date: 24 MAR 2025 18:17:49

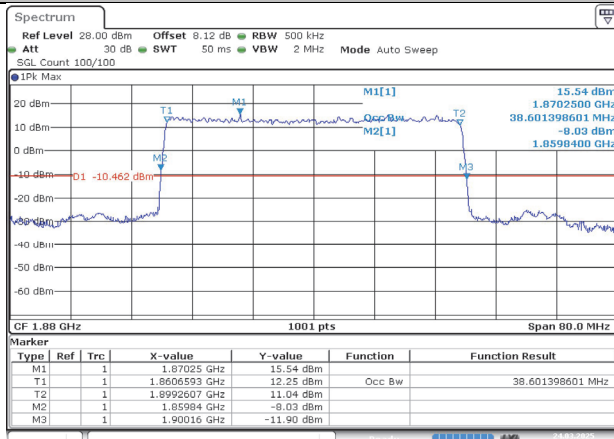
MCH / CP-256QAM / Outer_Full



Date: 24 MAR 2025 18:17:59

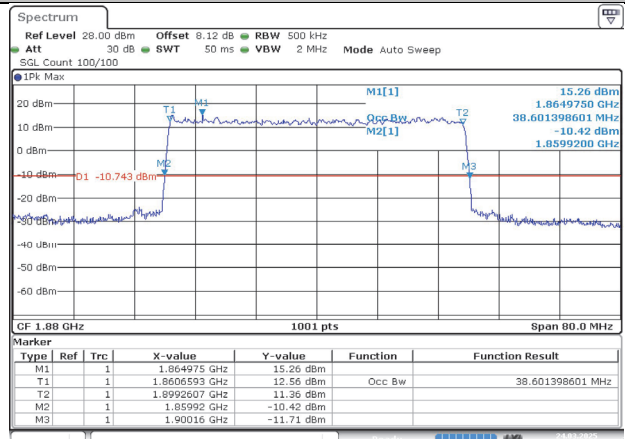
n2A / 15KHz / 40MHz

MCH / DFT-Pi2BPSK / Outer_Full



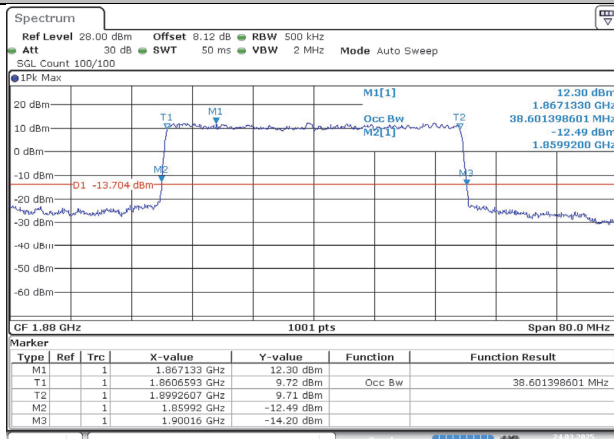
Date: 24 MAR 2025 18:18:19

MCH / DFT-QPSK / Outer_Full



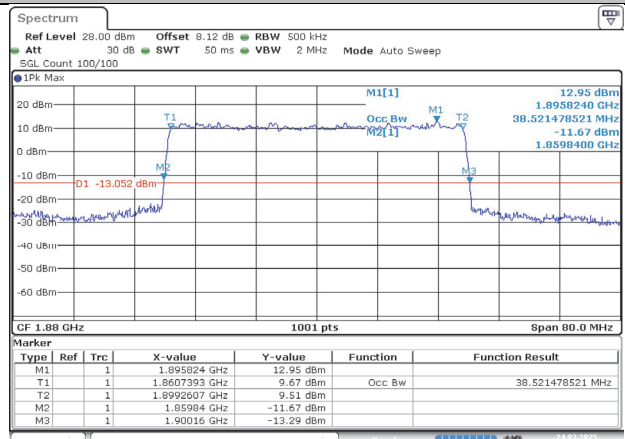
Date: 24 MAR 2025 18:18:45

MCH / CP-QPSK / Outer_Full



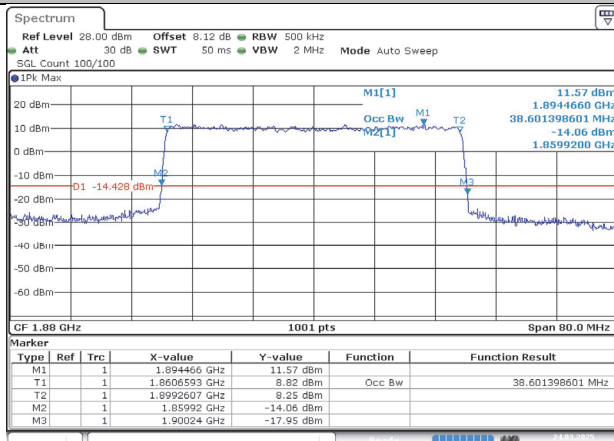
Date: 24 MAR 2025 18:19:21

MCH / CP-16QAM / Outer_Full



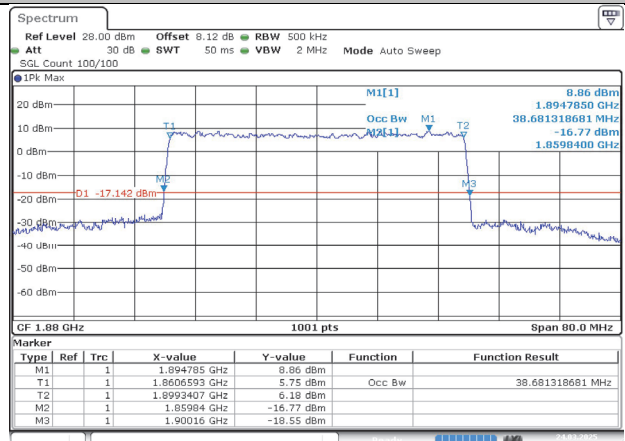
Date: 24 MAR 2025 18:19:28

MCH / CP-64QAM / Outer_Full



Date: 24 MAR 2025 18:19:36

MCH / CP-256QAM / Outer_Full



Date: 24 MAR 2025 18:19:46

5. Conducted Band Edges

5.1. Test Plots for SCS=15KHz

