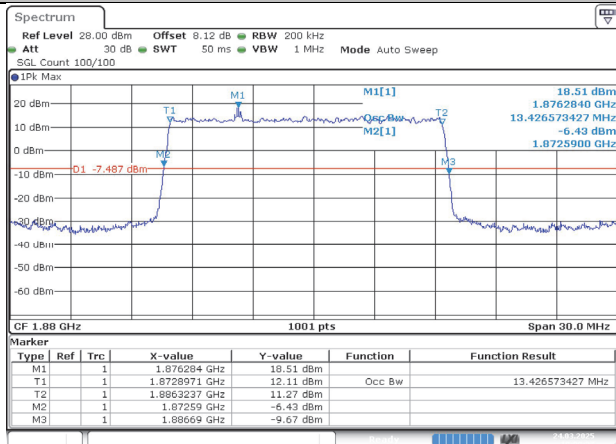


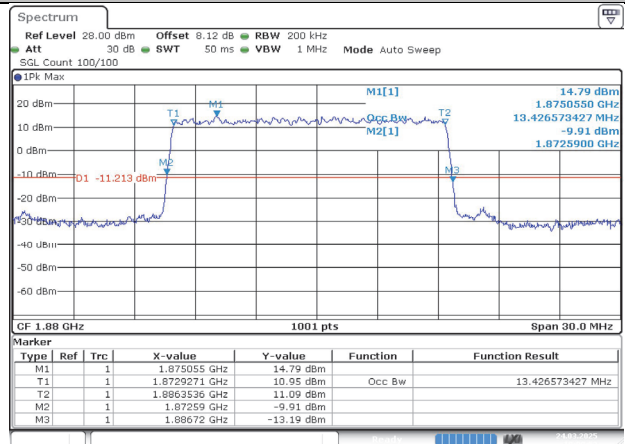
n2A / 15KHz / 15MHz

MCH / DFT-Pi2BPSK / Outer_Full



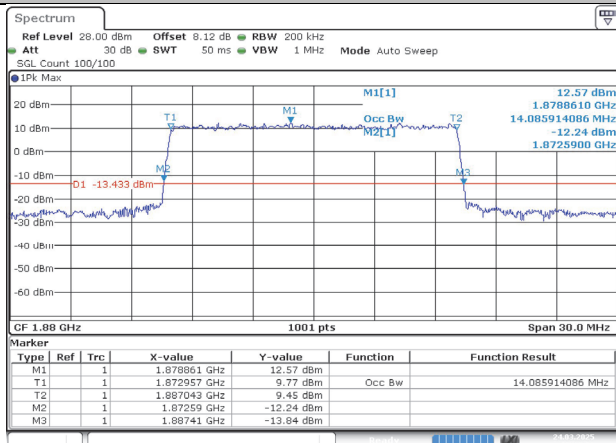
Date: 24 MAR 2025 18:09:37

MCH / DFT-QPSK / Outer_Full



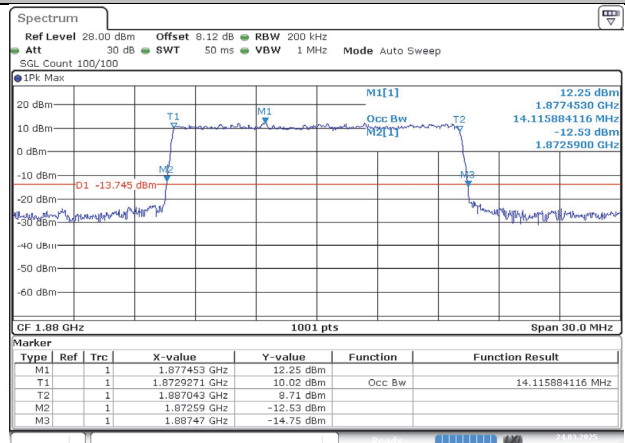
Date: 24 MAR 2025 18:09:54

MCH / CP-QPSK / Outer_Full



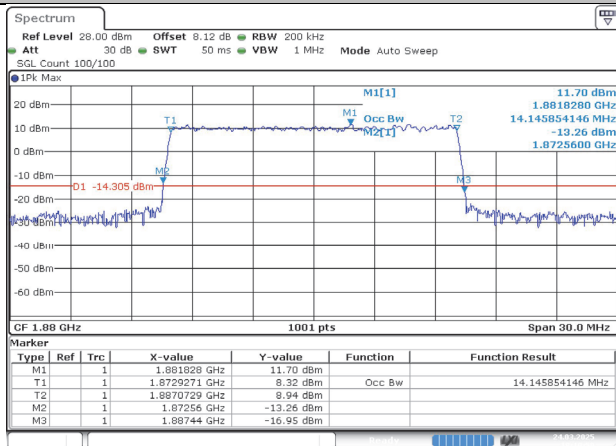
Date: 24 MAR 2025 18:10:33

MCH / CP-16QAM / Outer_Full



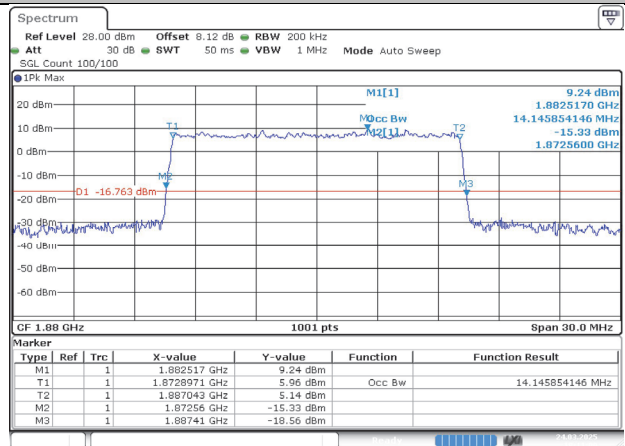
Date: 24 MAR 2025 18:10:42

MCH / CP-64QAM / Outer_Full



Date: 24 MAR 2025 18:10:50

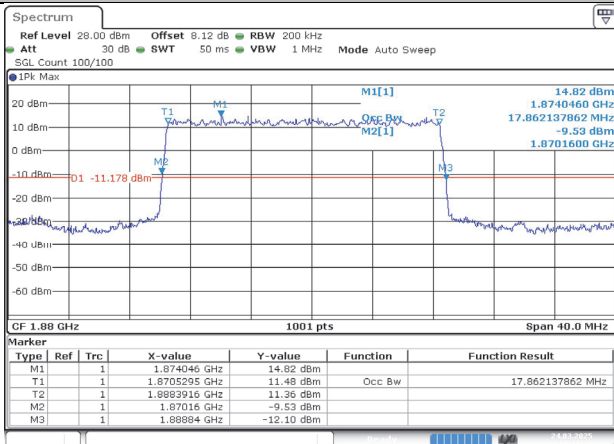
MCH / CP-256QAM / Outer_Full



Date: 24 MAR 2025 18:11:00

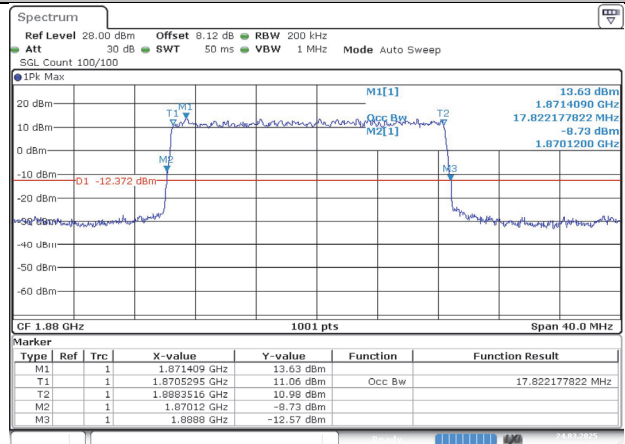
n2A / 15KHz / 20MHz

MCH / DFT-Pi2BPSK / Outer_Full



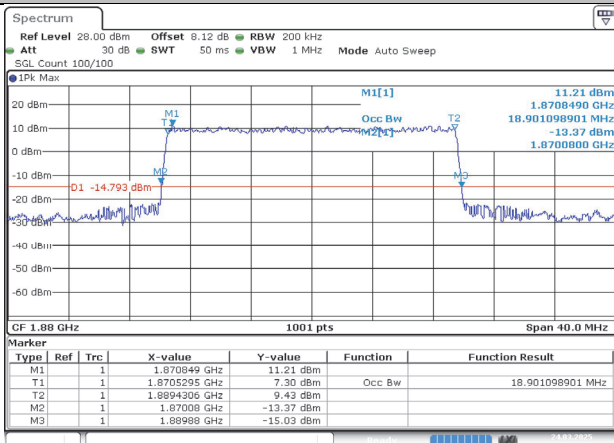
Date: 24 MAR 2025 18:11:22

MCH / DFT-QPSK / Outer_Full



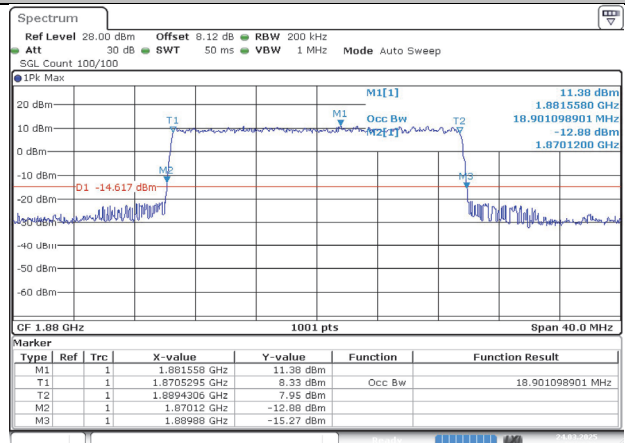
Date: 24 MAR 2025 18:11:49

MCH / CP-QPSK / Outer_Full



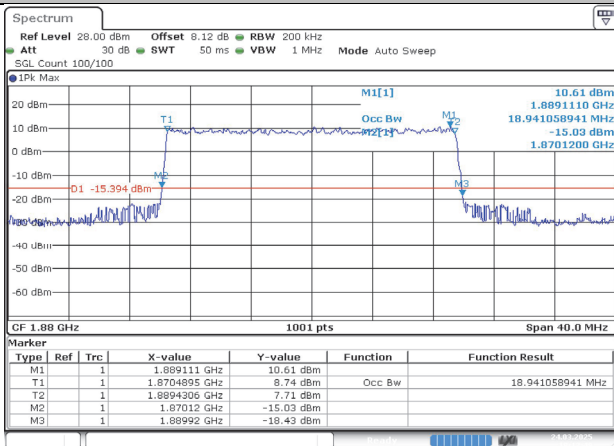
Date: 24 MAR 2025 18:12:30

MCH / CP-16QAM / Outer_Full



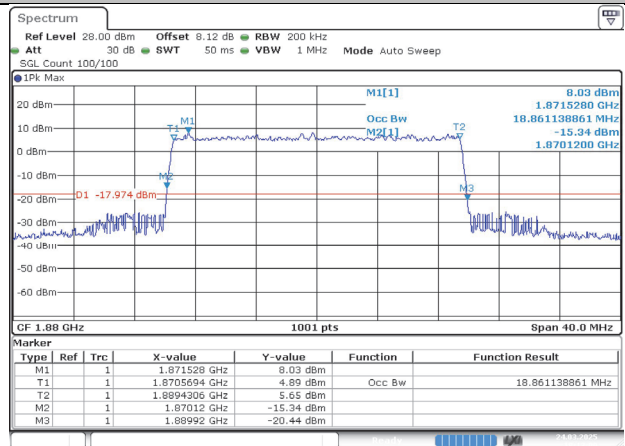
Date: 24 MAR 2025 18:12:39

MCH / CP-64QAM / Outer_Full



Date: 24 MAR 2025 18:12:48

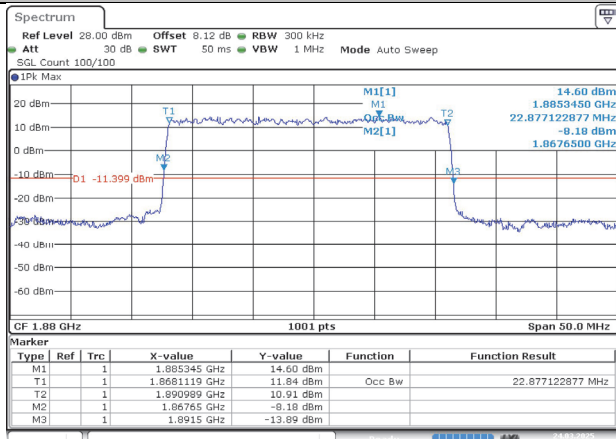
MCH / CP-256QAM / Outer_Full



Date: 24 MAR 2025 18:12:59

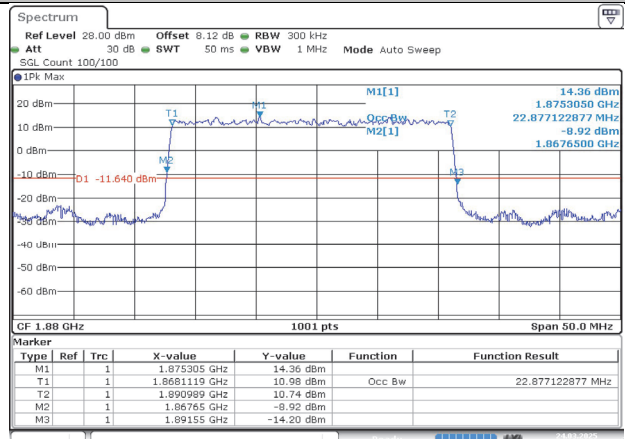
n2A / 15KHz / 25MHz

MCH / DFT-Pi2BPSK / Outer_Full



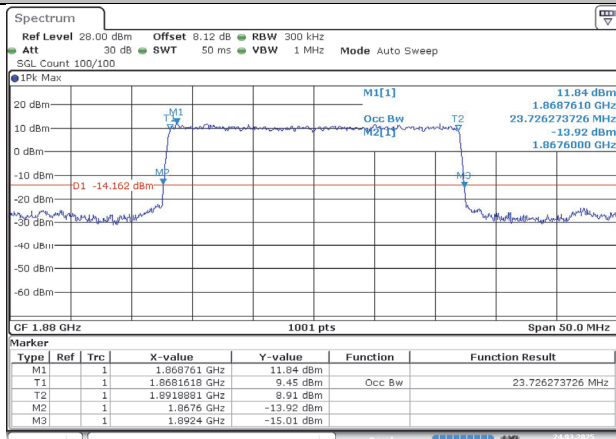
Date: 24 MAR 2025 18:13:19

MCH / DFT-QPSK / Outer_Full



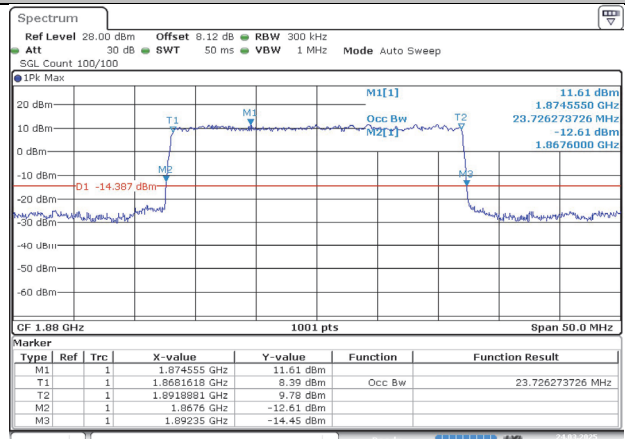
Date: 24 MAR 2025 18:13:35

MCH / CP-QPSK / Outer_Full



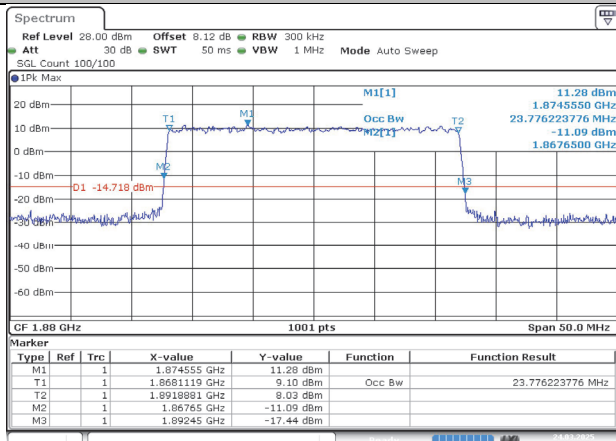
Date: 24 MAR 2025 18:14:11

MCH / CP-16QAM / Outer_Full



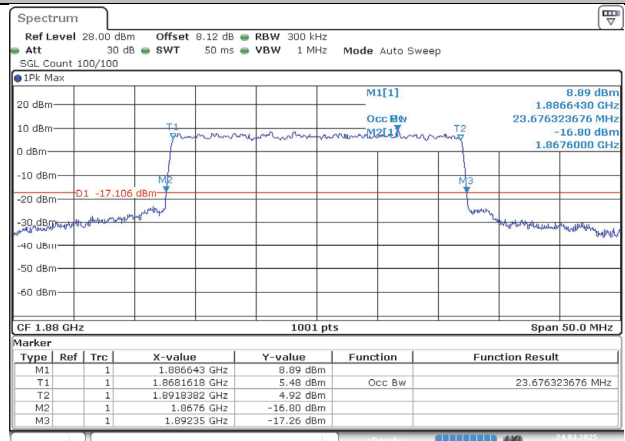
Date: 24 MAR 2025 18:14:18

MCH / CP-64QAM / Outer_Full



Date: 24 MAR 2025 18:14:26

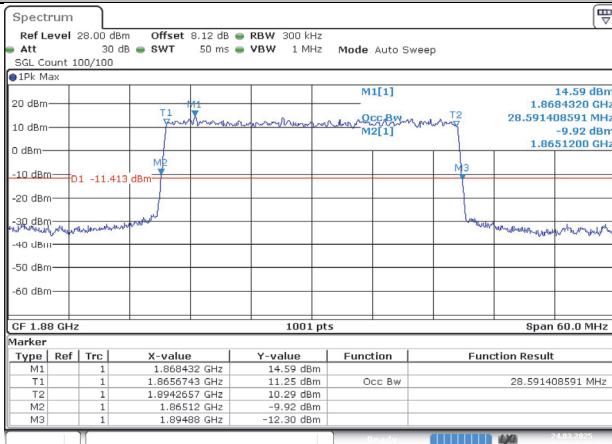
MCH / CP-256QAM / Outer_Full



Date: 24 MAR 2025 18:14:36

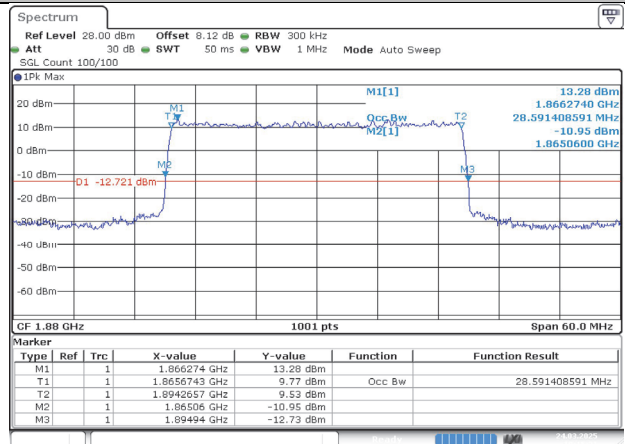
n2A / 15KHz / 30MHz

MCH / DFT-Pi2BPSK / Outer_Full



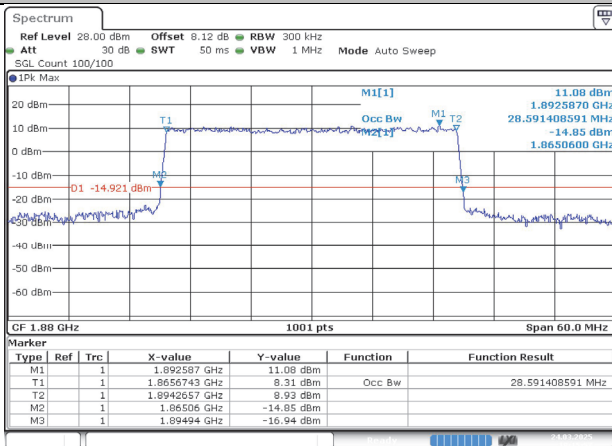
Date: 24 MAR 2025 18:15:05

MCH / DFT-QPSK / Outer_Full



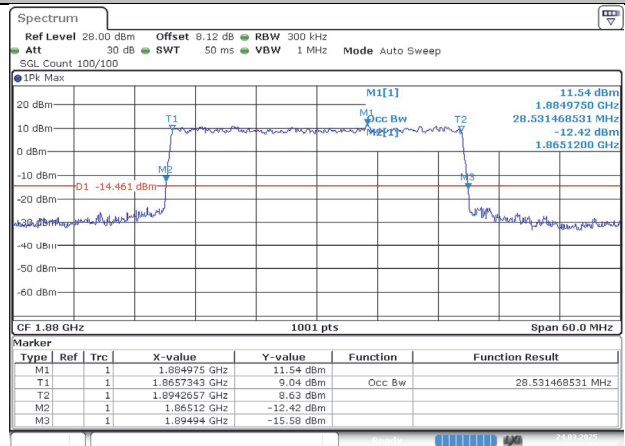
Date: 24 MAR 2025 18:15:21

MCH / CP-QPSK / Outer_Full



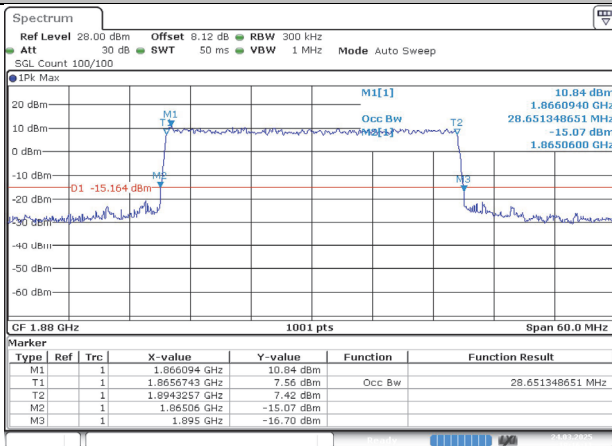
Date: 24 MAR 2025 18:15:57

MCH / CP-16QAM / Outer_Full



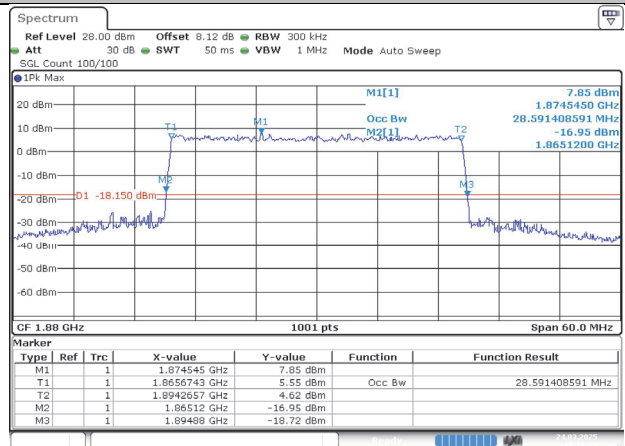
Date: 24 MAR 2025 18:16:04

MCH / CP-64QAM / Outer_Full



Date: 24 MAR 2025 18:16:12

MCH / CP-256QAM / Outer_Full



Date: 24 MAR 2025 18:16:21