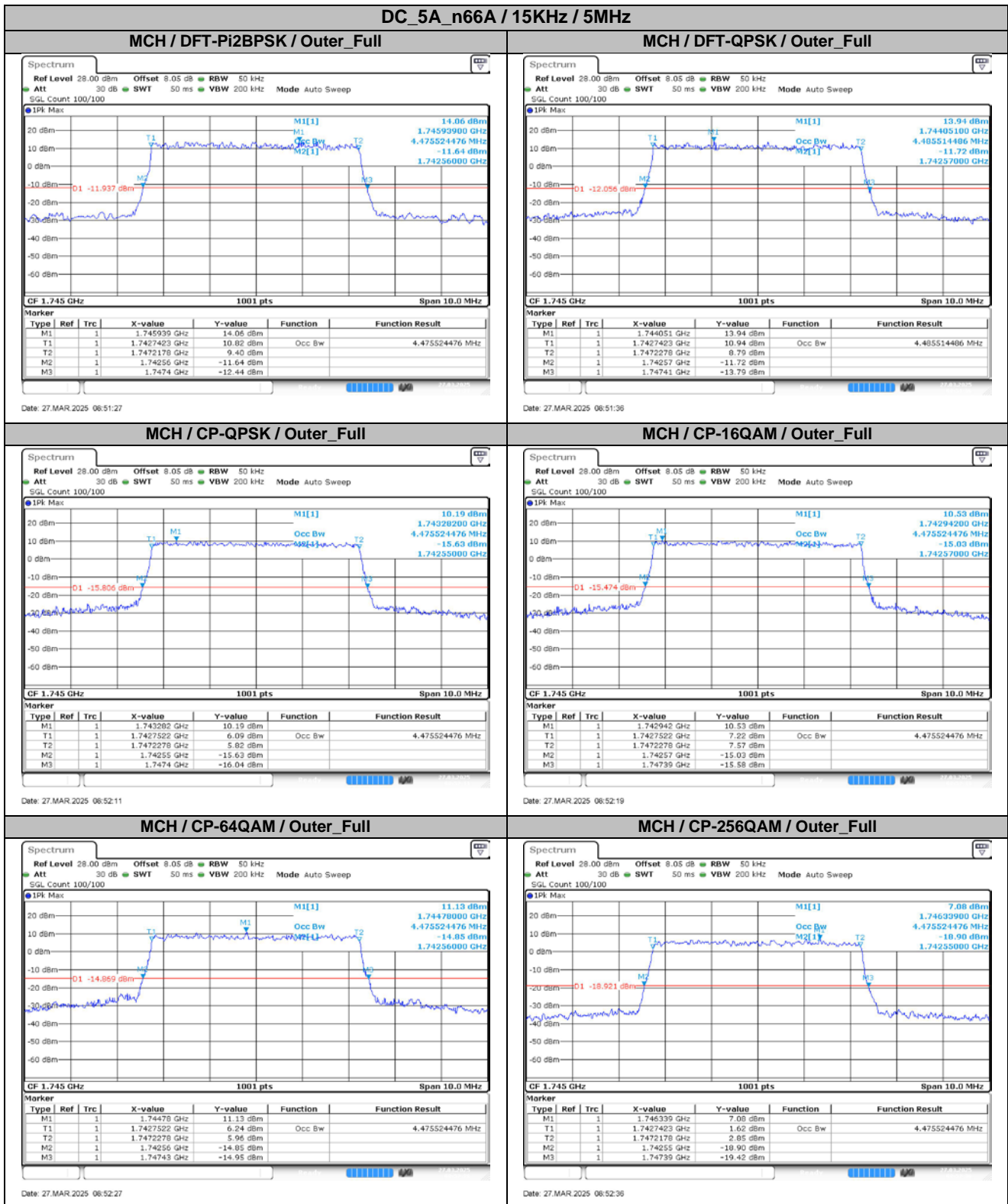
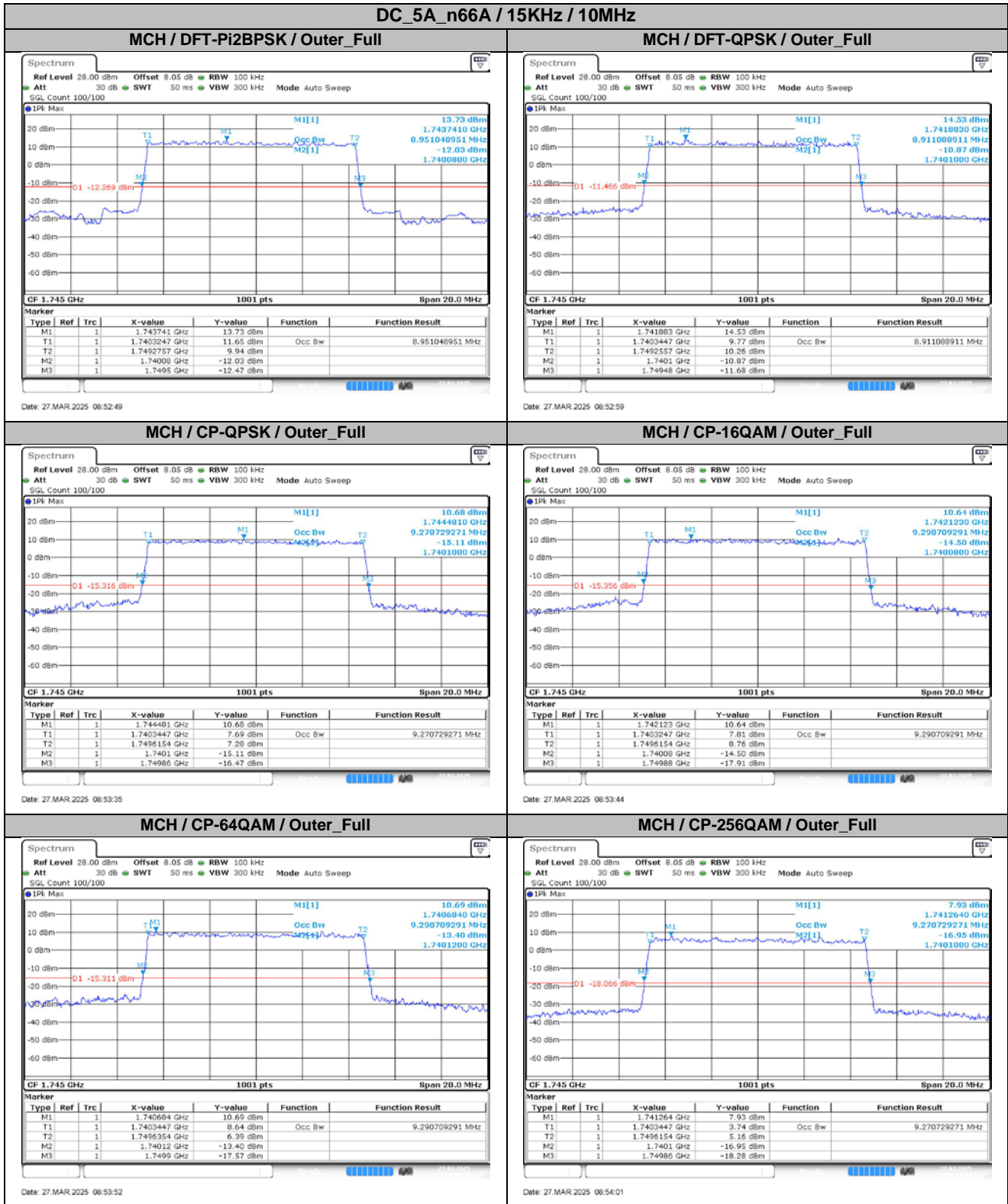


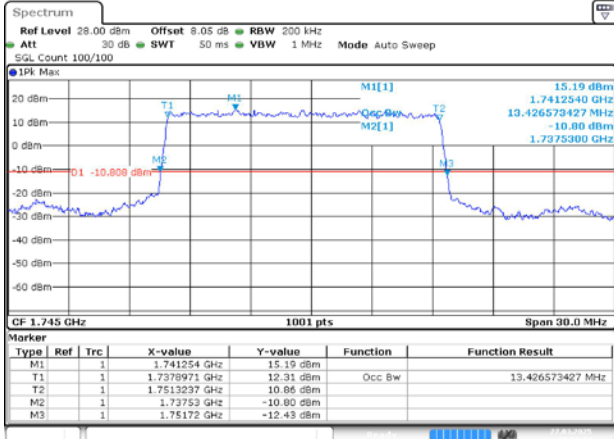
4.2. Test Plots for SCS=15KHz





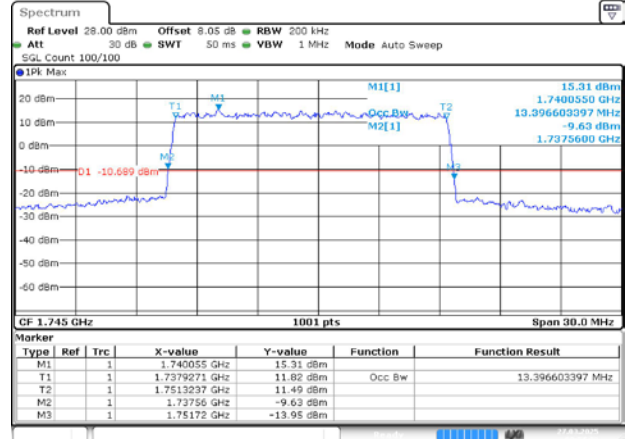
DC_5A_n66A / 15KHz / 15MHz

MCH / DFT-Pi2BPSK / Outer_Full



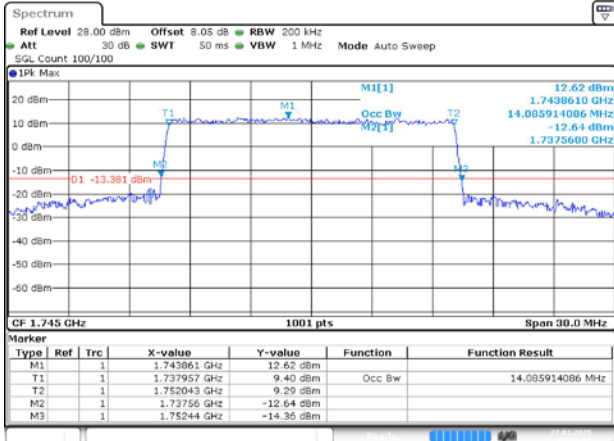
Date: 27.MAR.2025 08:54:14

MCH / DFT-QPSK / Outer_Full



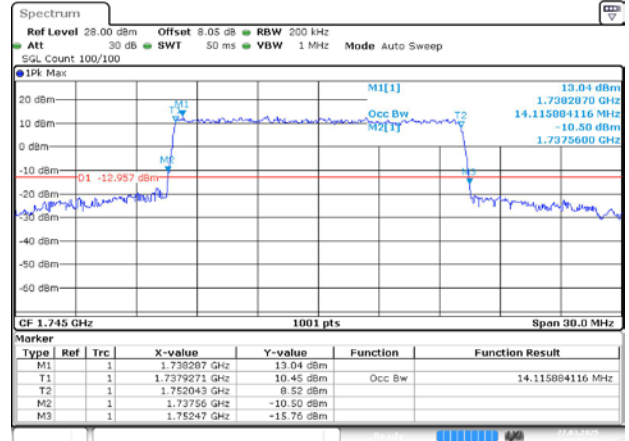
Date: 27.MAR.2025 08:54:24

MCH / CP-QPSK / Outer_Full



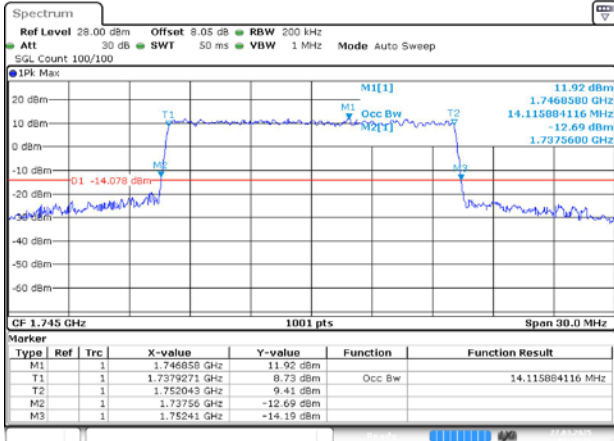
Date: 27.MAR.2025 08:55:02

MCH / CP-16QAM / Outer_Full



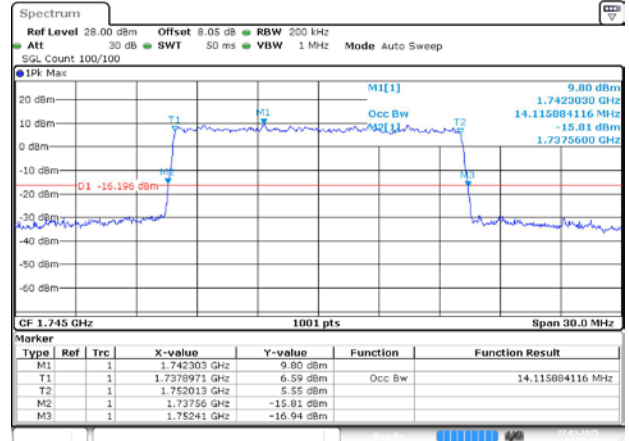
Date: 27.MAR.2025 08:55:11

MCH / CP-64QAM / Outer_Full

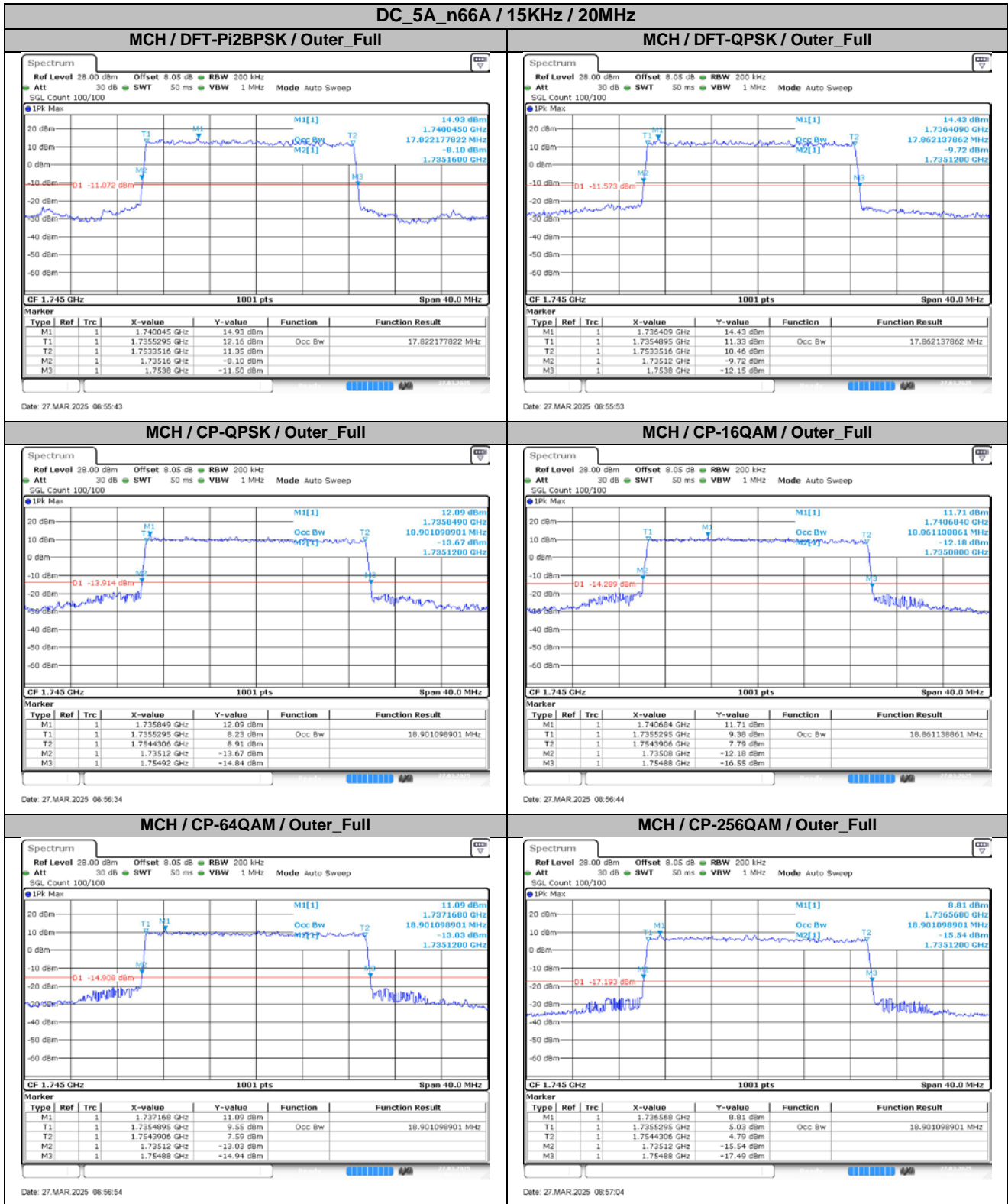


Date: 27.MAR.2025 08:55:20

MCH / CP-256QAM / Outer_Full

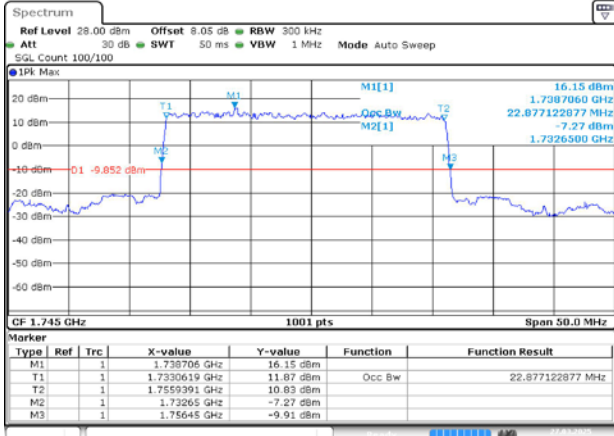


Date: 27.MAR.2025 08:55:29



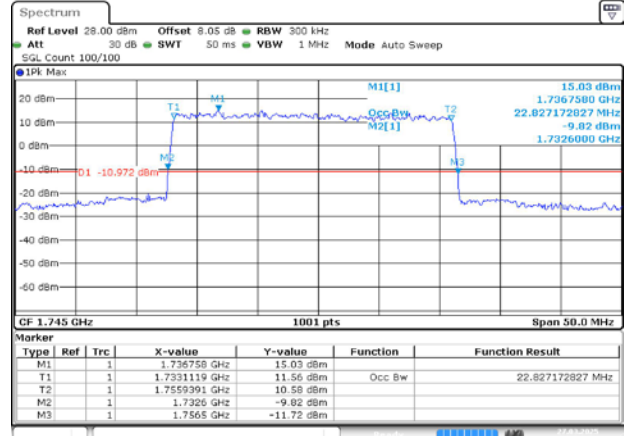
DC_5A_n66A / 15KHz / 25MHz

MCH / DFT-Pi2BPSK / Outer_Full



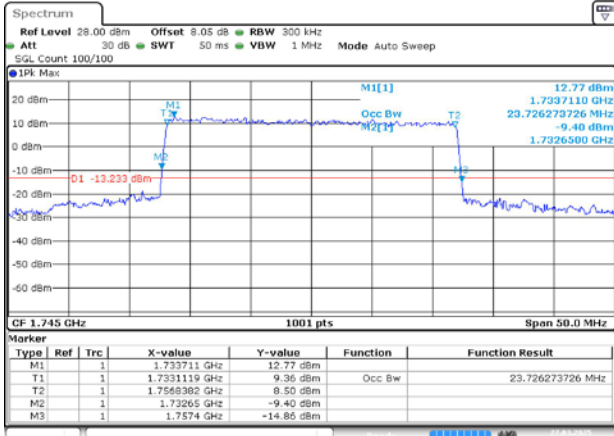
Date: 27.MAR.2025 08:57:16

MCH / DFT-QPSK / Outer_Full



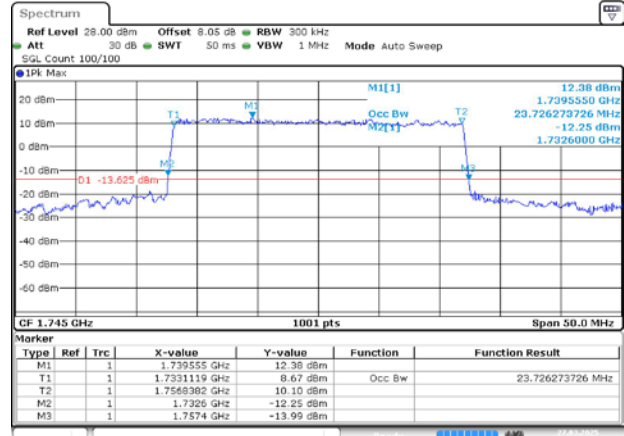
Date: 27.MAR.2025 08:57:25

MCH / CP-QPSK / Outer_Full



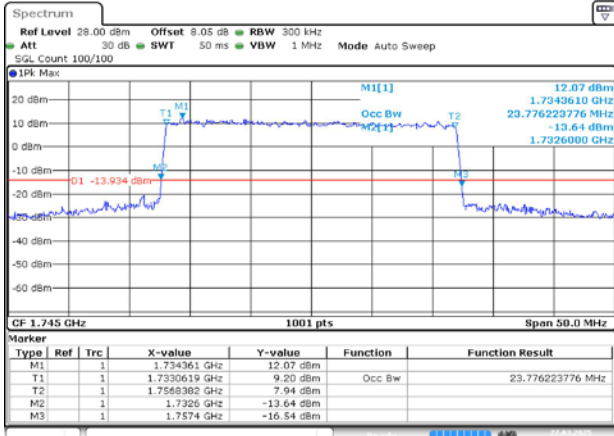
Date: 27.MAR.2025 08:57:58

MCH / CP-16QAM / Outer_Full



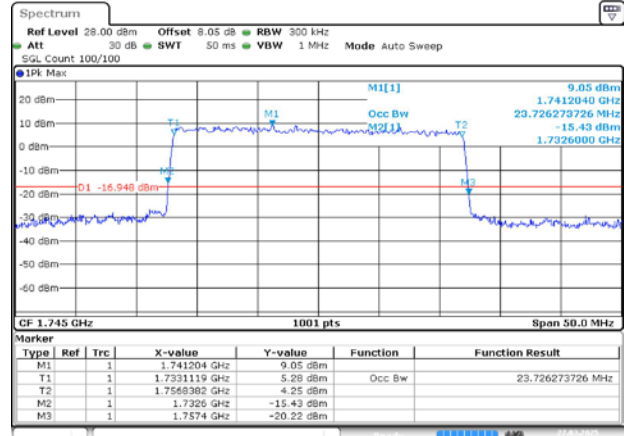
Date: 27.MAR.2025 08:58:06

MCH / CP-64QAM / Outer_Full



Date: 27.MAR.2025 08:58:14

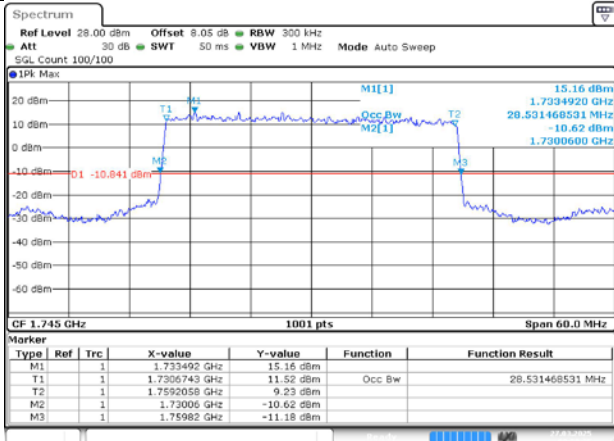
MCH / CP-256QAM / Outer_Full



Date: 27.MAR.2025 08:58:23

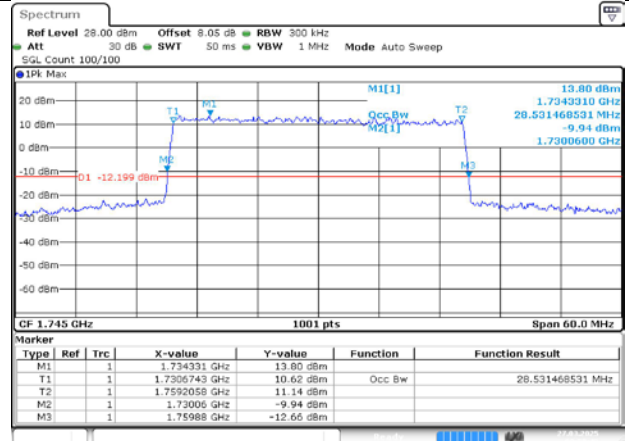
DC_5A_n66A / 15KHz / 30MHz

MCH / DFT-Pi2BPSK / Outer_Full



Date: 27.MAR.2025 08:58:35

MCH / DFT-QPSK / Outer_Full



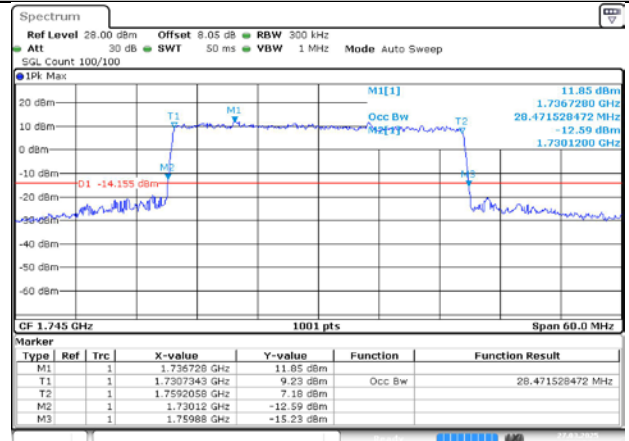
Date: 27.MAR.2025 08:58:43

MCH / CP-QPSK / Outer_Full



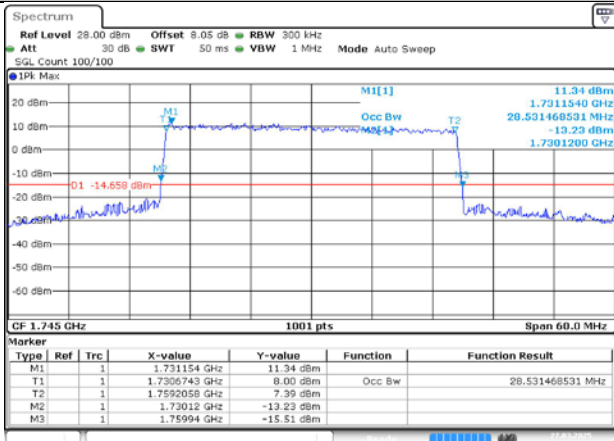
Date: 27.MAR.2025 08:59:17

MCH / CP-16QAM / Outer_Full



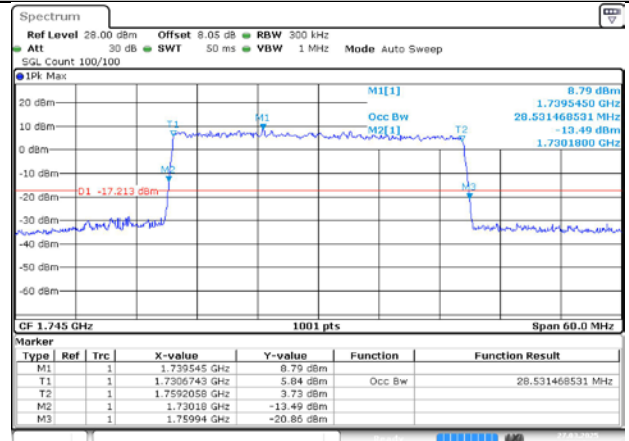
Date: 27.MAR.2025 08:59:25

MCH / CP-64QAM / Outer_Full



Date: 27.MAR.2025 08:59:33

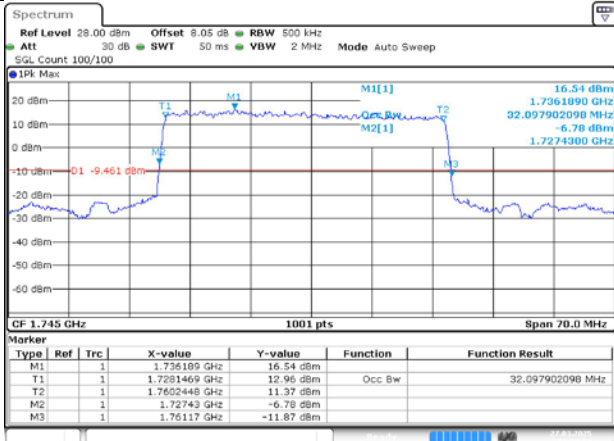
MCH / CP-256QAM / Outer_Full



Date: 27.MAR.2025 08:59:42

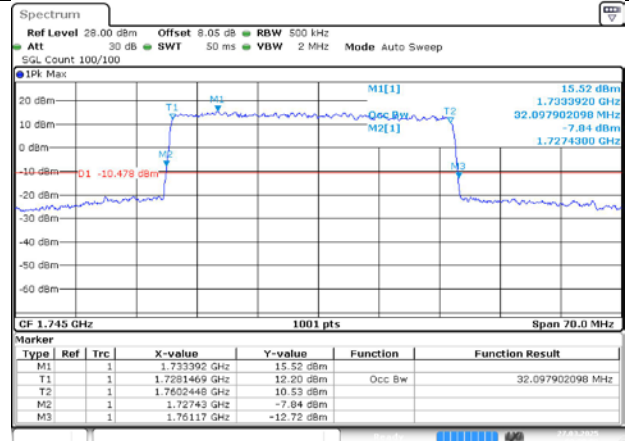
DC_5A_n66A / 15KHz / 35MHz

MCH / DFT-Pi2BPSK / Outer_Full



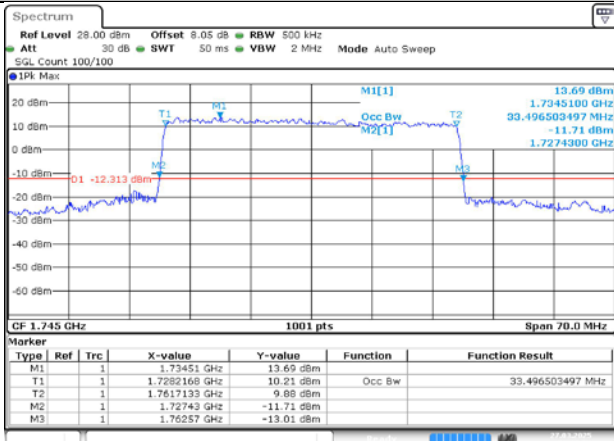
Date: 27.MAR.2025 06:59:54

MCH / DFT-QPSK / Outer_Full



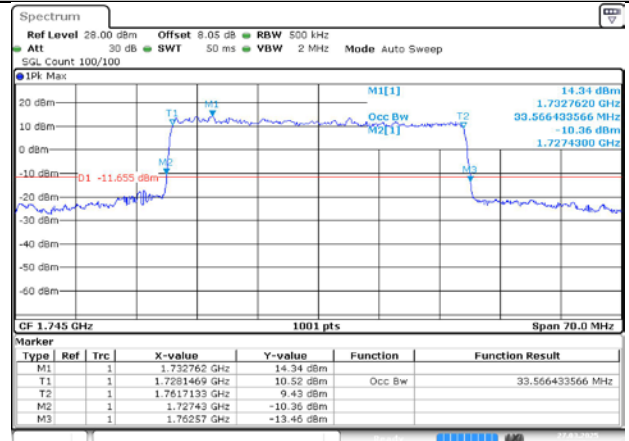
Date: 27.MAR.2025 09:00:03

MCH / CP-QPSK / Outer_Full



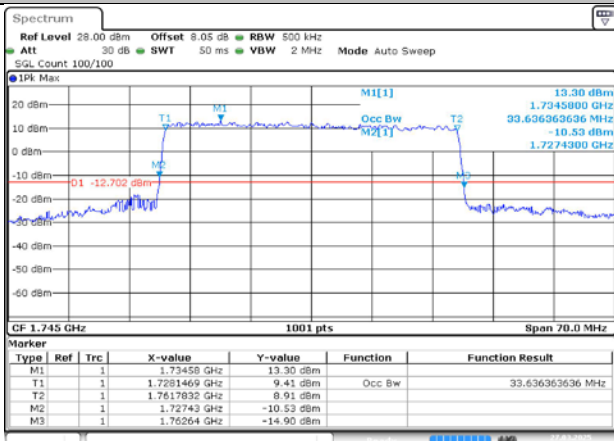
Date: 27.MAR.2025 09:00:38

MCH / CP-16QAM / Outer_Full



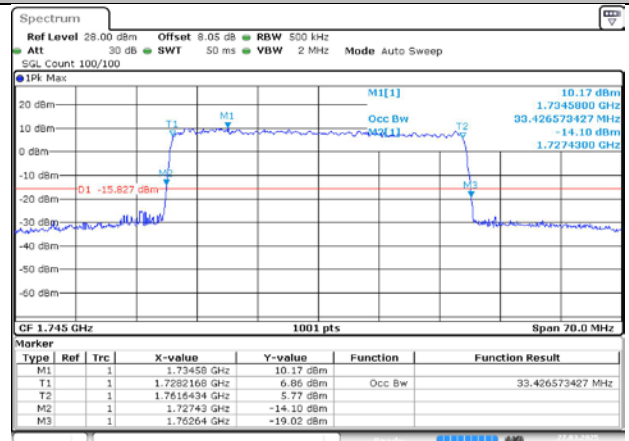
Date: 27.MAR.2025 09:00:46

MCH / CP-64QAM / Outer_Full



Date: 27.MAR.2025 09:00:55

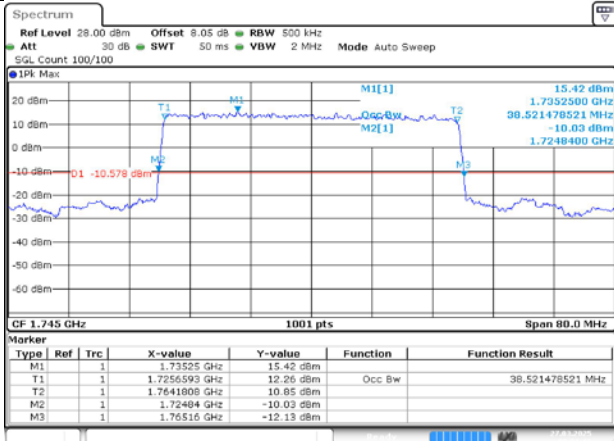
MCH / CP-256QAM / Outer_Full



Date: 27.MAR.2025 09:01:04

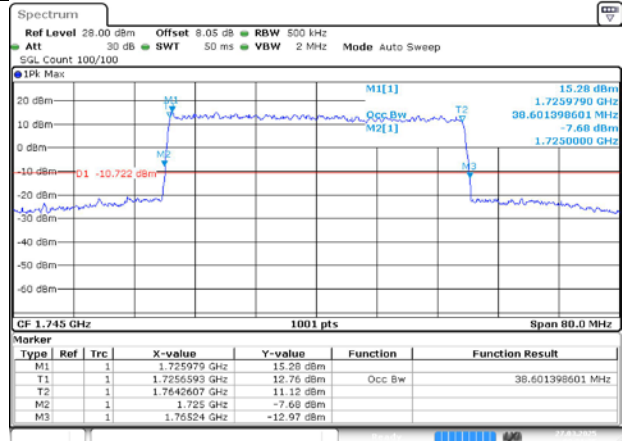
DC_5A_n66A / 15KHz / 40MHz

MCH / DFT-Pi2BPSK / Outer_Full



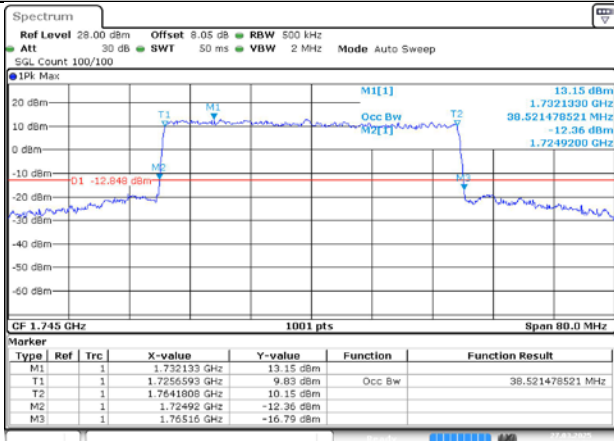
Date: 27.MAR.2025 09:01:16

MCH / DFT-QPSK / Outer_Full



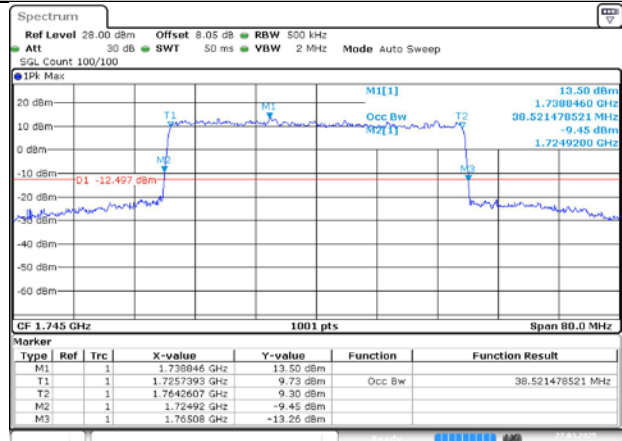
Date: 27.MAR.2025 09:01:25

MCH / CP-QPSK / Outer_Full



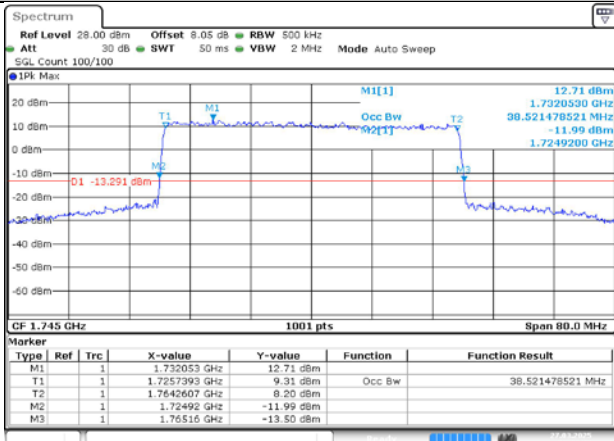
Date: 27.MAR.2025 09:02:00

MCH / CP-16QAM / Outer_Full



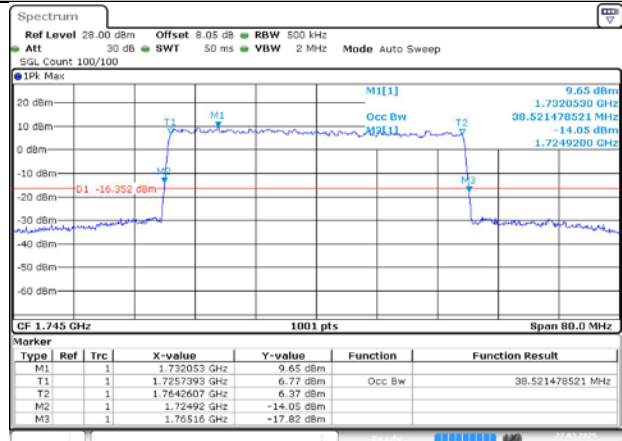
Date: 27.MAR.2025 09:02:08

MCH / CP-64QAM / Outer_Full



Date: 27.MAR.2025 09:02:17

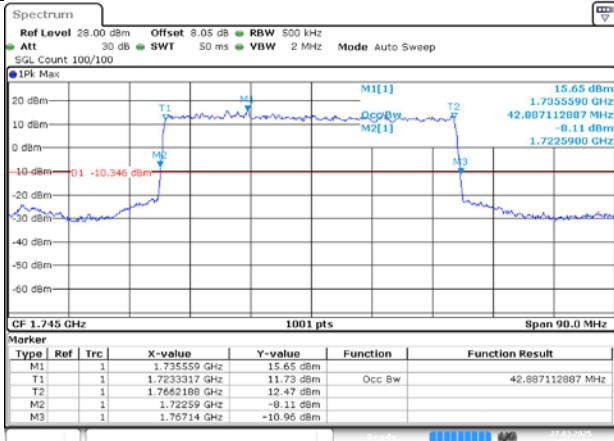
MCH / CP-256QAM / Outer_Full



Date: 27.MAR.2025 09:02:26

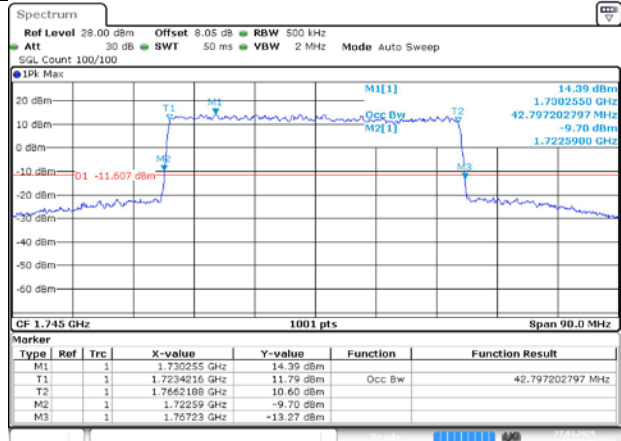
DC_5A_n66A / 15KHz / 45MHz

MCH / DFT-Pi2BPSK / Outer_Full



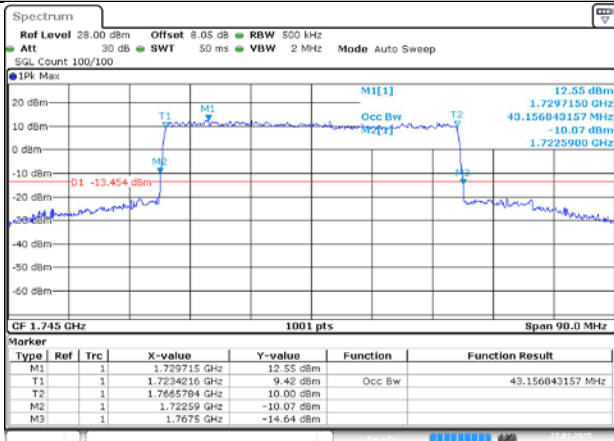
Date: 27.MAR.2025 09:02:38

MCH / DFT-QPSK / Outer_Full



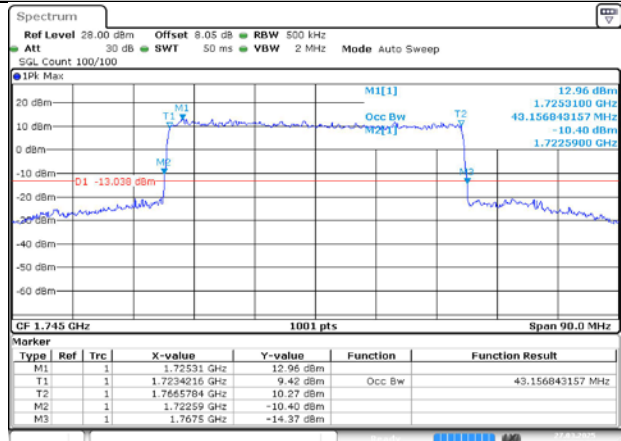
Date: 27.MAR.2025 09:02:47

MCH / CP-QPSK / Outer_Full



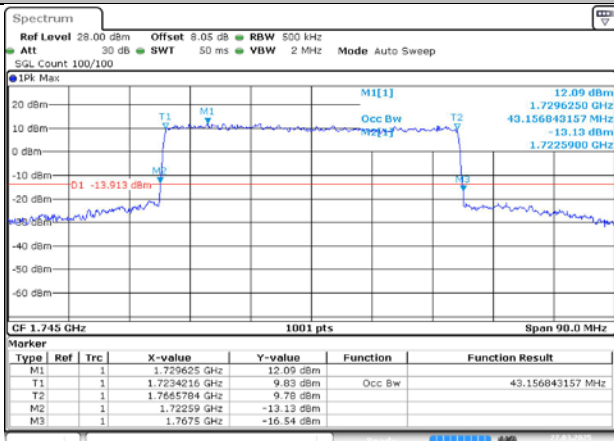
Date: 27.MAR.2025 09:03:23

MCH / CP-16QAM / Outer_Full



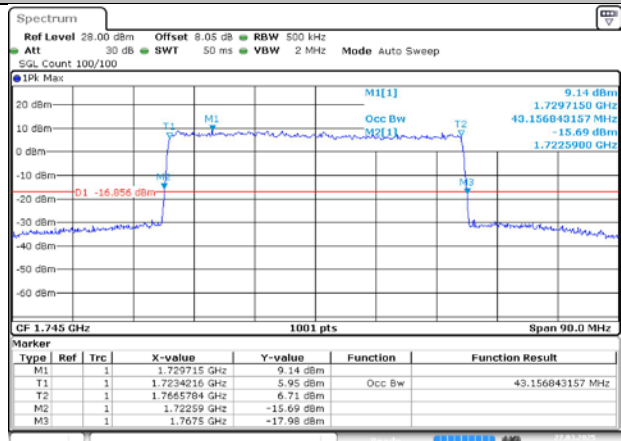
Date: 27.MAR.2025 09:03:31

MCH / CP-64QAM / Outer_Full



Date: 27.MAR.2025 09:03:39

MCH / CP-256QAM / Outer_Full



Date: 27.MAR.2025 09:03:48

5. Conducted Band Edges

5.1. Test Plots for SCS=15KHz

