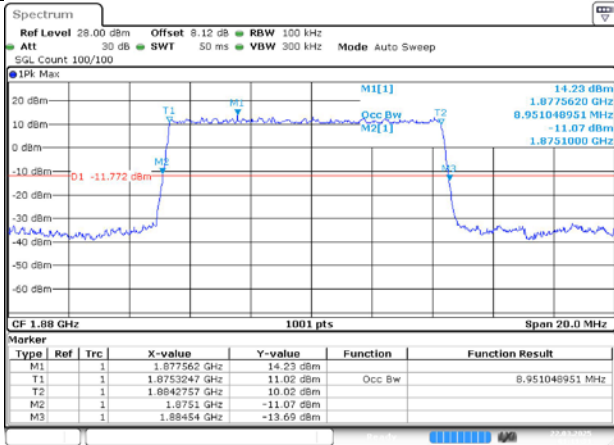


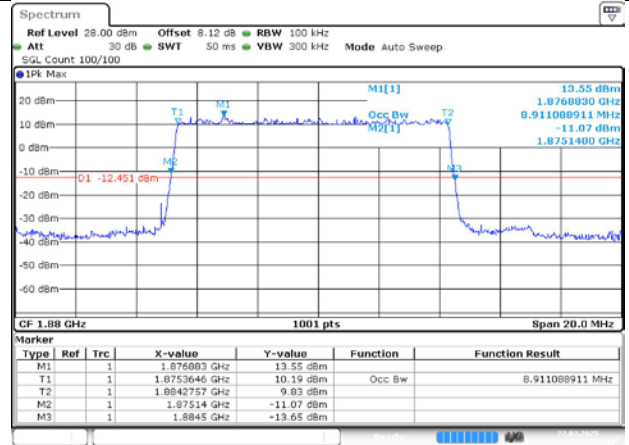
DC\_4A\_n2A / 15KHz / 10MHz

MCH / DFT-Pi2BPSK / Outer\_Full



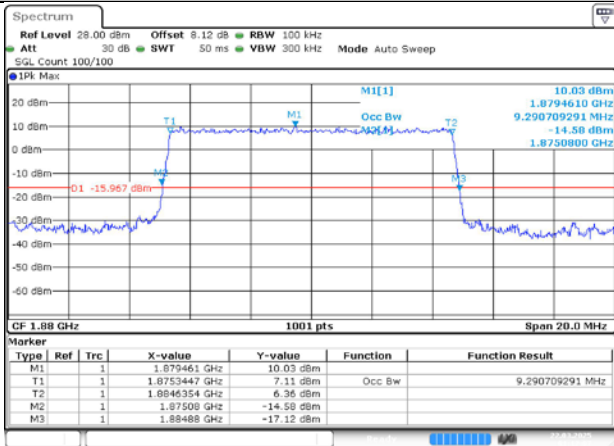
Date: 22.MAR.2025 01:11:21

MCH / DFT-QPSK / Outer\_Full



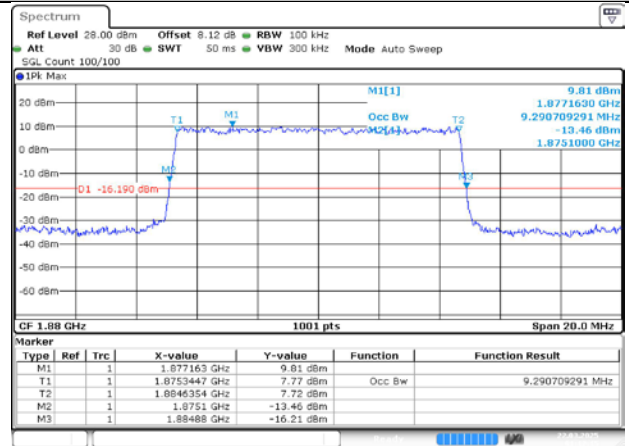
Date: 22.MAR.2025 01:11:30

MCH / CP-QPSK / Outer\_Full



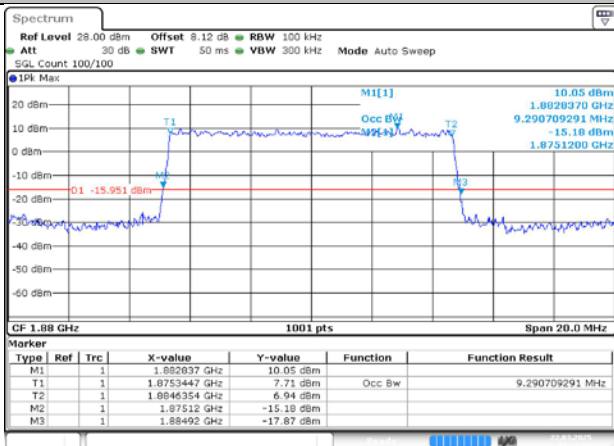
Date: 22.MAR.2025 01:12:07

MCH / CP-16QAM / Outer\_Full



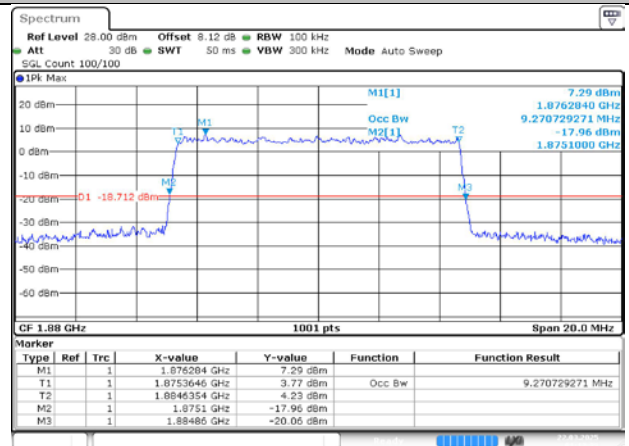
Date: 22.MAR.2025 01:12:15

MCH / CP-64QAM / Outer\_Full

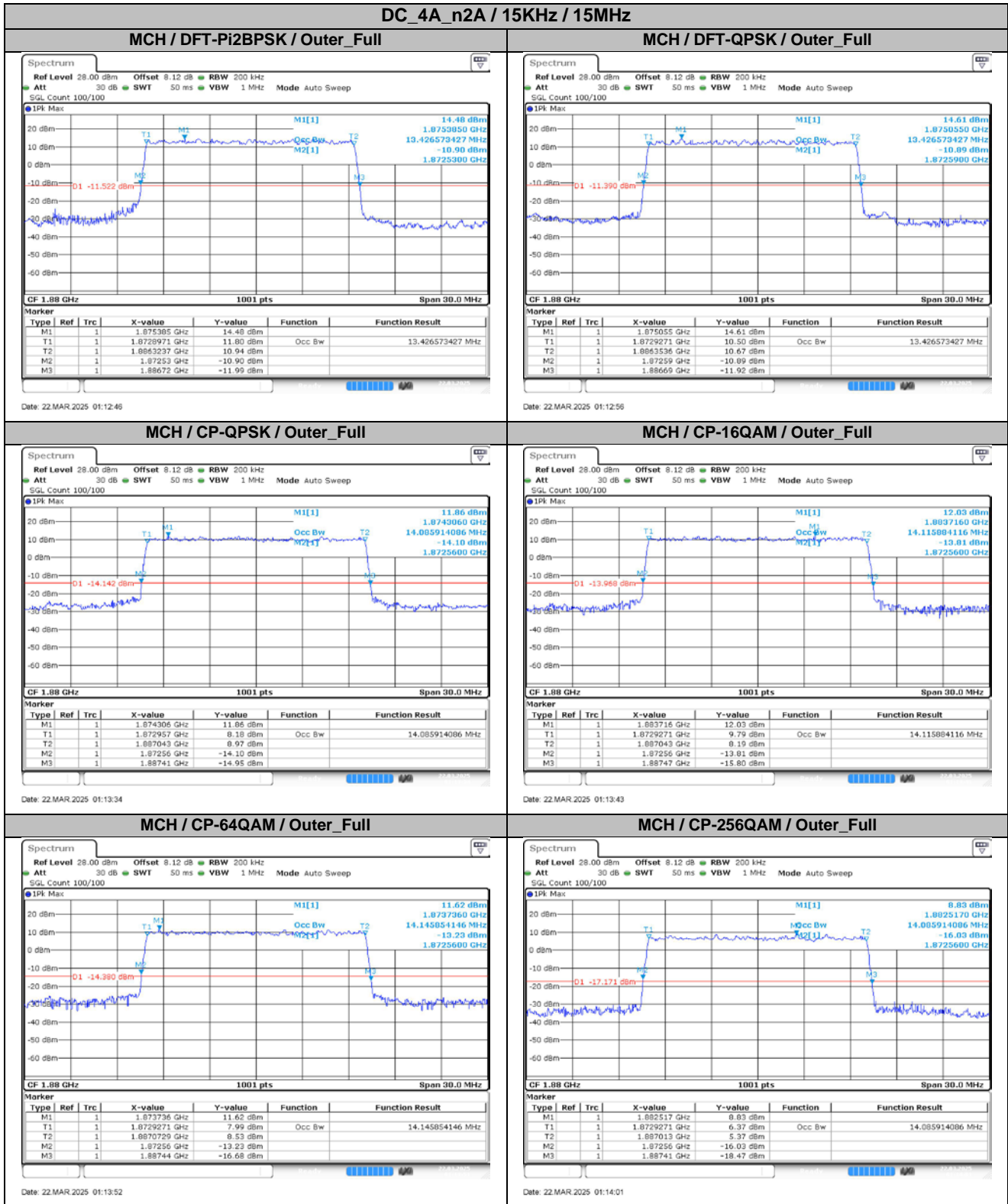


Date: 22.MAR.2025 01:12:24

MCH / CP-256QAM / Outer\_Full

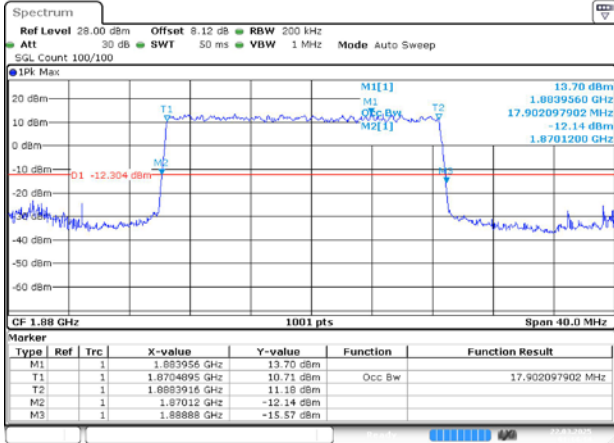


Date: 22.MAR.2025 01:12:33



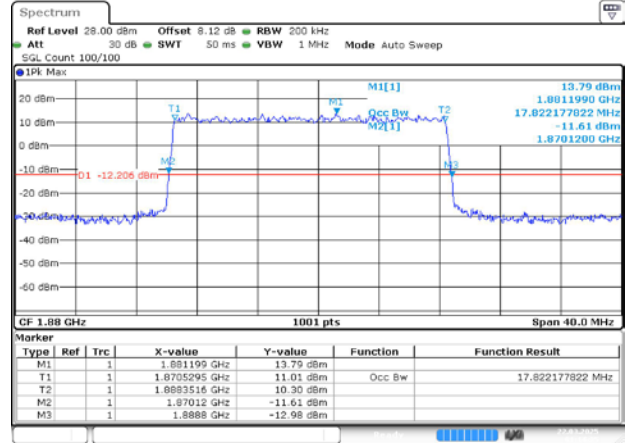
DC\_4A\_n2A / 15KHz / 20MHz

MCH / DFT-Pi2BPSK / Outer\_Full



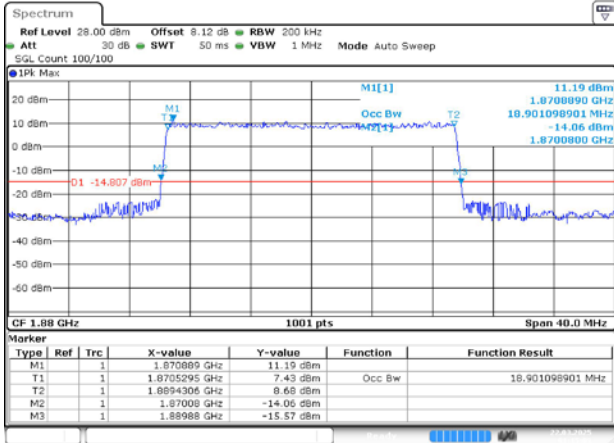
Date: 22.MAR.2025 01:14:15

MCH / DFT-QPSK / Outer\_Full



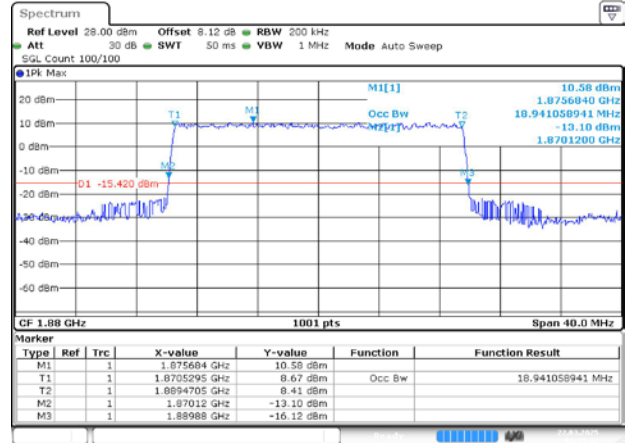
Date: 22.MAR.2025 01:14:25

MCH / CP-QPSK / Outer\_Full



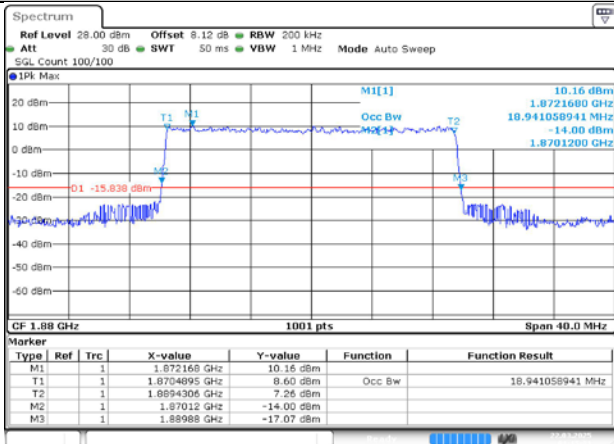
Date: 22.MAR.2025 01:15:03

MCH / CP-16QAM / Outer\_Full



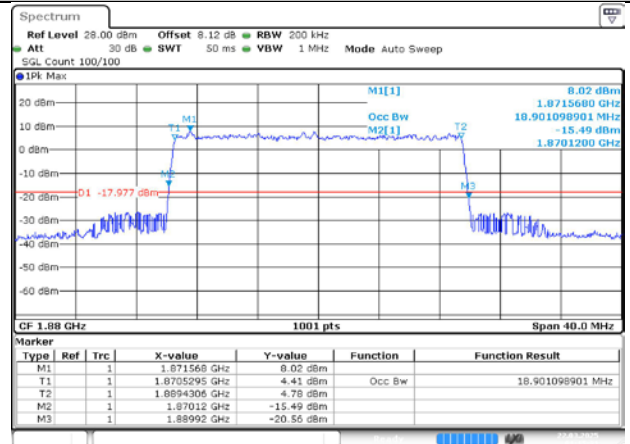
Date: 22.MAR.2025 01:15:13

MCH / CP-64QAM / Outer\_Full



Date: 22.MAR.2025 01:15:22

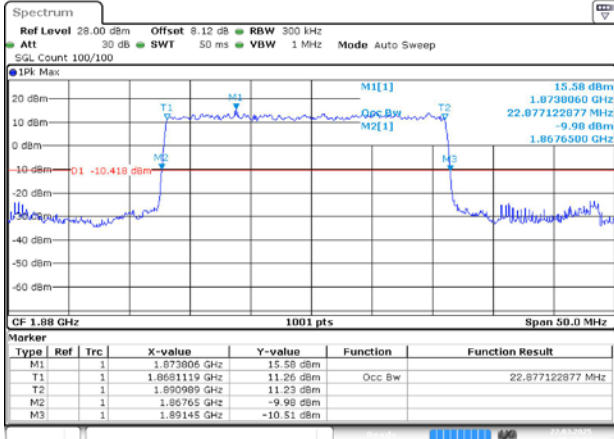
MCH / CP-256QAM / Outer\_Full



Date: 22.MAR.2025 01:15:31

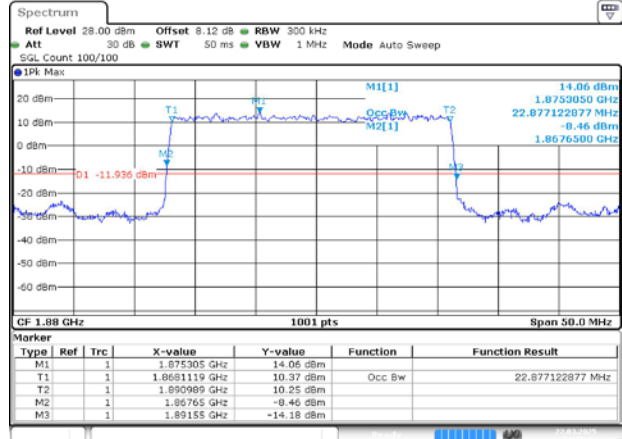
DC\_4A\_n2A / 15KHz / 25MHz

MCH / DFT-Pi2BPSK / Outer\_Full



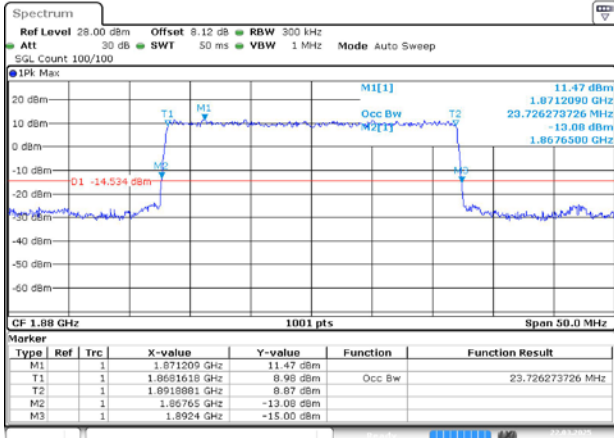
Date: 22.MAR.2025 01:15:43

MCH / DFT-QPSK / Outer\_Full



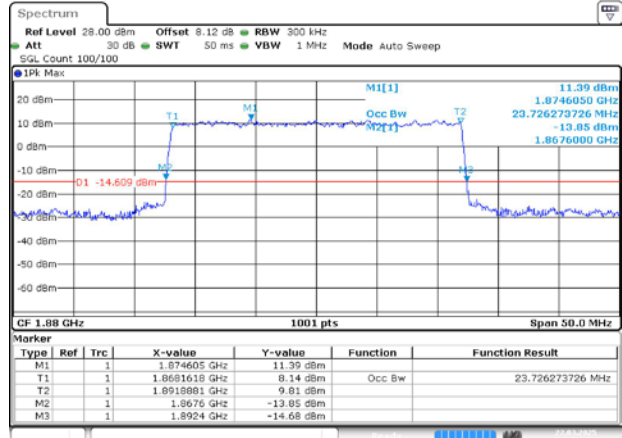
Date: 22.MAR.2025 01:15:52

MCH / CP-QPSK / Outer\_Full



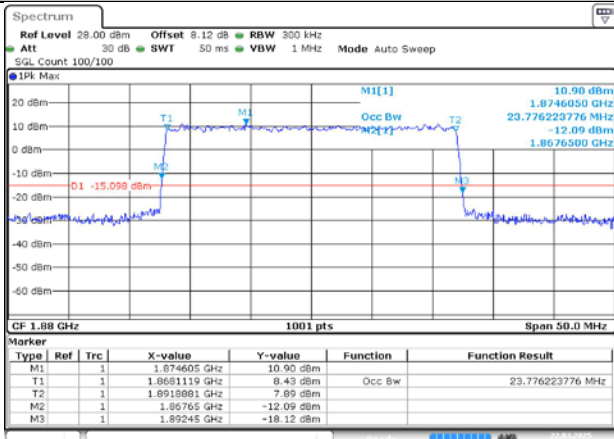
Date: 22.MAR.2025 01:16:25

MCH / CP-16QAM / Outer\_Full



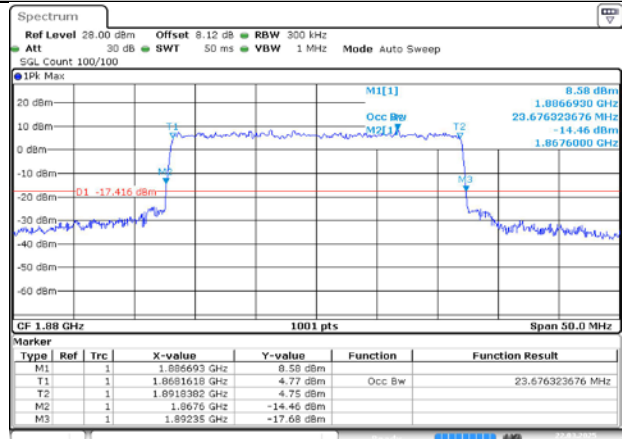
Date: 22.MAR.2025 01:16:33

MCH / CP-64QAM / Outer\_Full

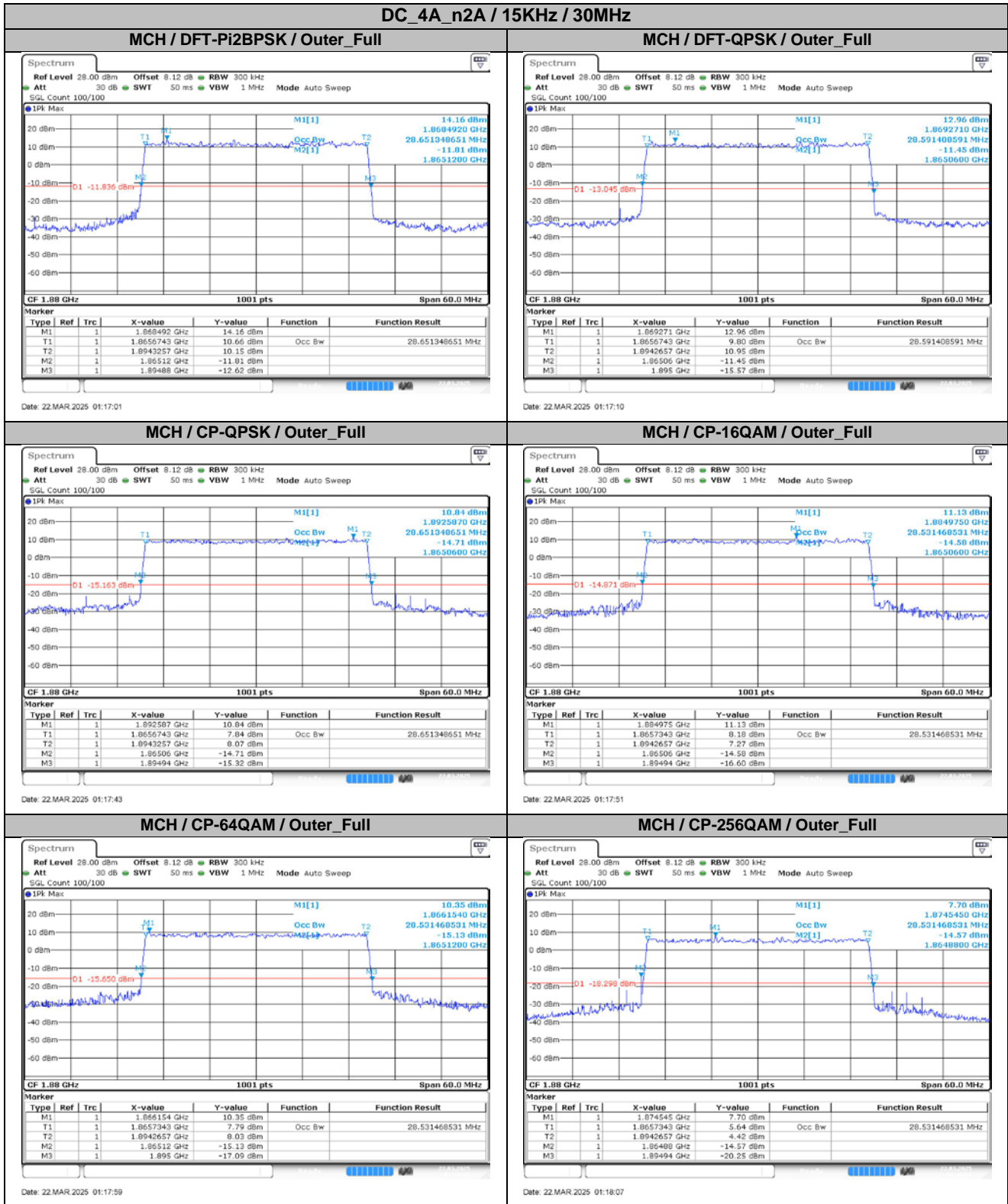


Date: 22.MAR.2025 01:16:41

MCH / CP-256QAM / Outer\_Full

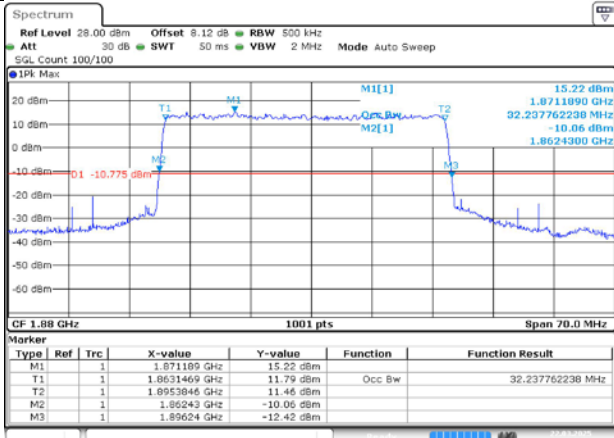


Date: 22.MAR.2025 01:16:49



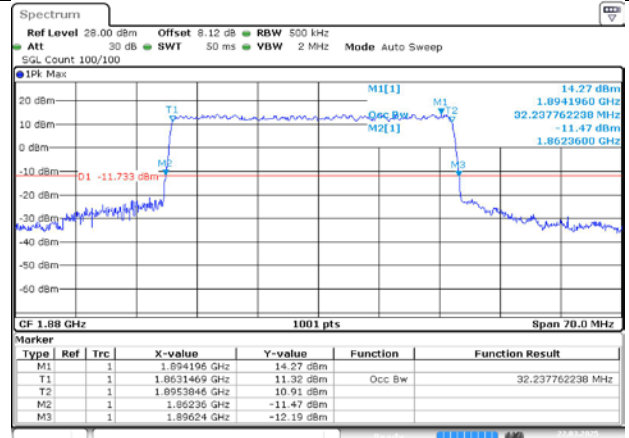
DC\_4A\_n2A / 15KHz / 35MHz

MCH / DFT-Pi2BPSK / Outer\_Full



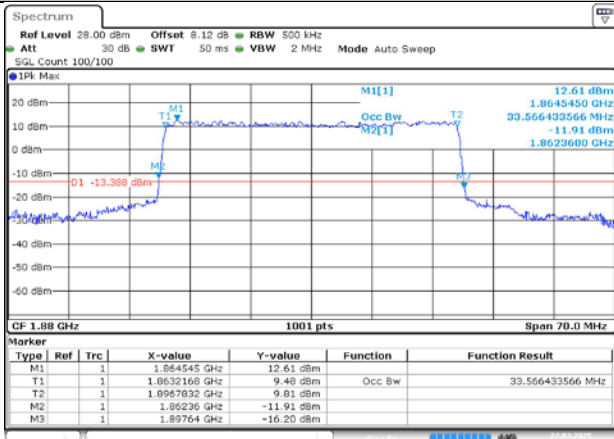
Date: 22.MAR.2025 01:18:19

MCH / DFT-QPSK / Outer\_Full



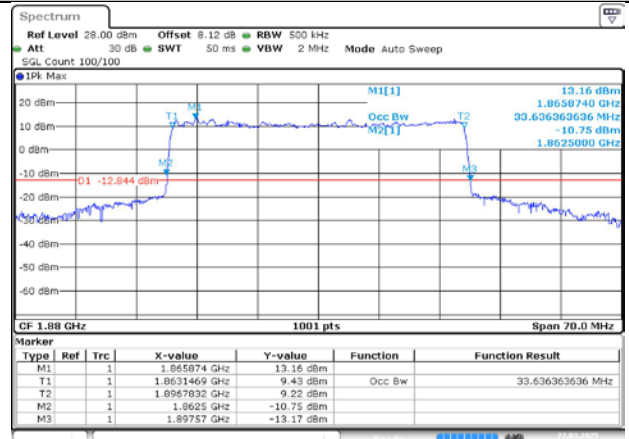
Date: 22.MAR.2025 01:18:28

MCH / CP-QPSK / Outer\_Full



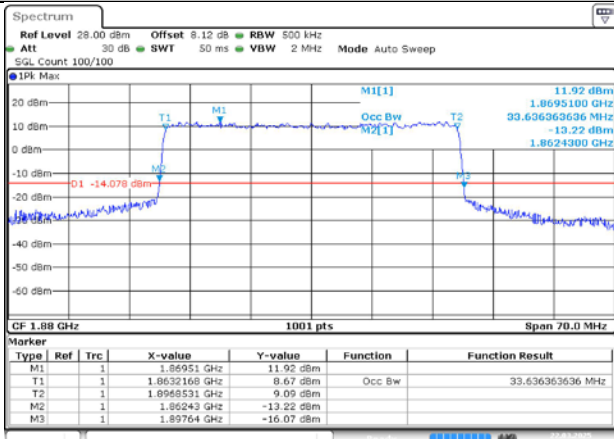
Date: 22.MAR.2025 01:19:02

MCH / CP-16QAM / Outer\_Full



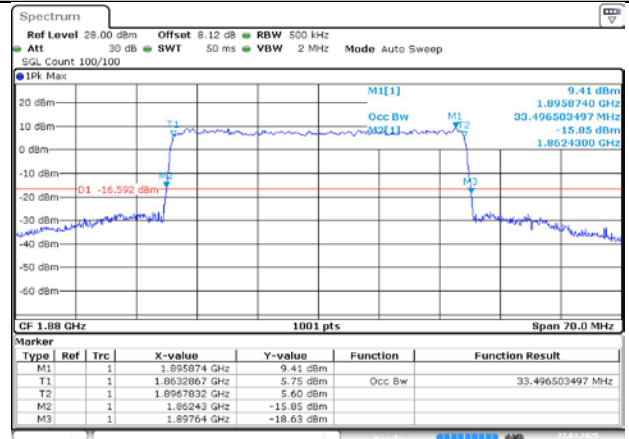
Date: 22.MAR.2025 01:19:09

MCH / CP-64QAM / Outer\_Full



Date: 22.MAR.2025 01:19:17

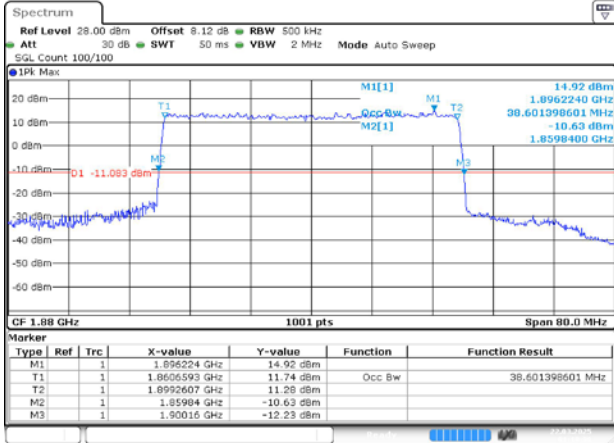
MCH / CP-256QAM / Outer\_Full



Date: 22.MAR.2025 01:19:26

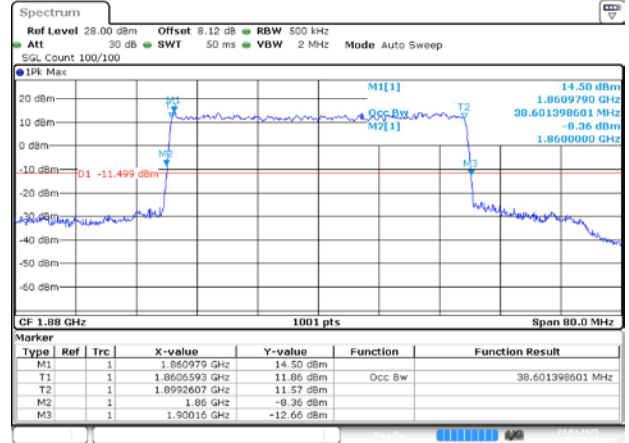
DC\_4A\_n2A / 15KHz / 40MHz

MCH / DFT-Pi2BPSK / Outer\_Full



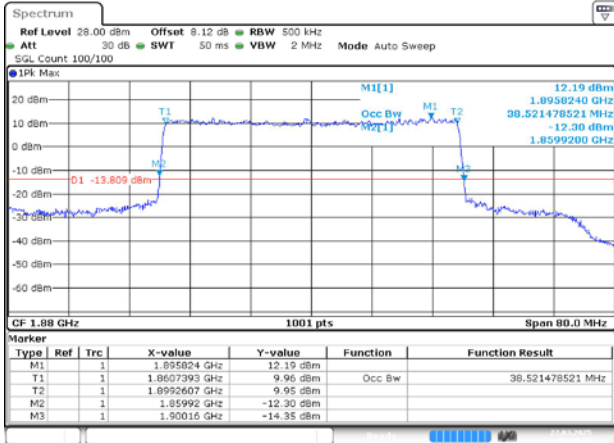
Date: 22.MAR.2025 01:19:38

MCH / DFT-QPSK / Outer\_Full



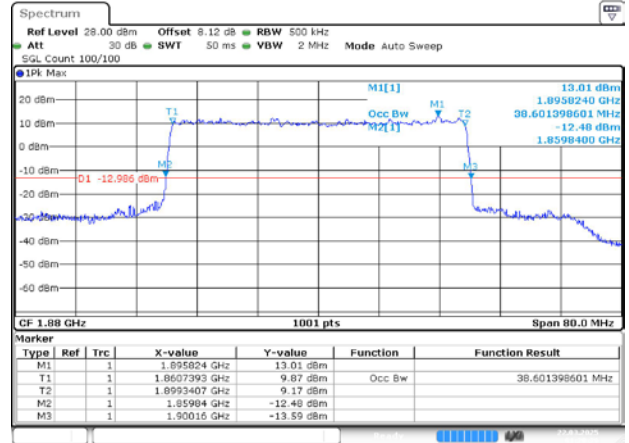
Date: 22.MAR.2025 01:19:47

MCH / CP-QPSK / Outer\_Full



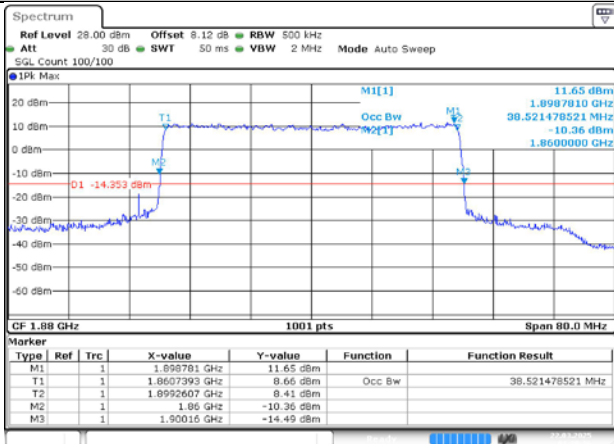
Date: 22.MAR.2025 01:20:20

MCH / CP-16QAM / Outer\_Full



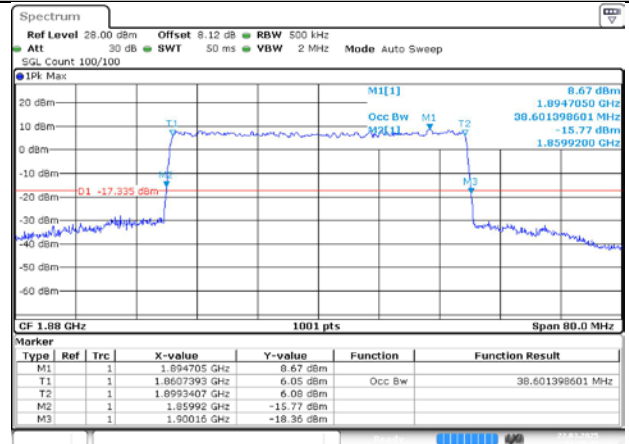
Date: 22.MAR.2025 01:20:28

MCH / CP-64QAM / Outer\_Full



Date: 22.MAR.2025 01:20:36

MCH / CP-256QAM / Outer\_Full



Date: 22.MAR.2025 01:20:45

### 5. Conducted Band Edges

#### 5.1. Test Plots for SCS=15KHz

