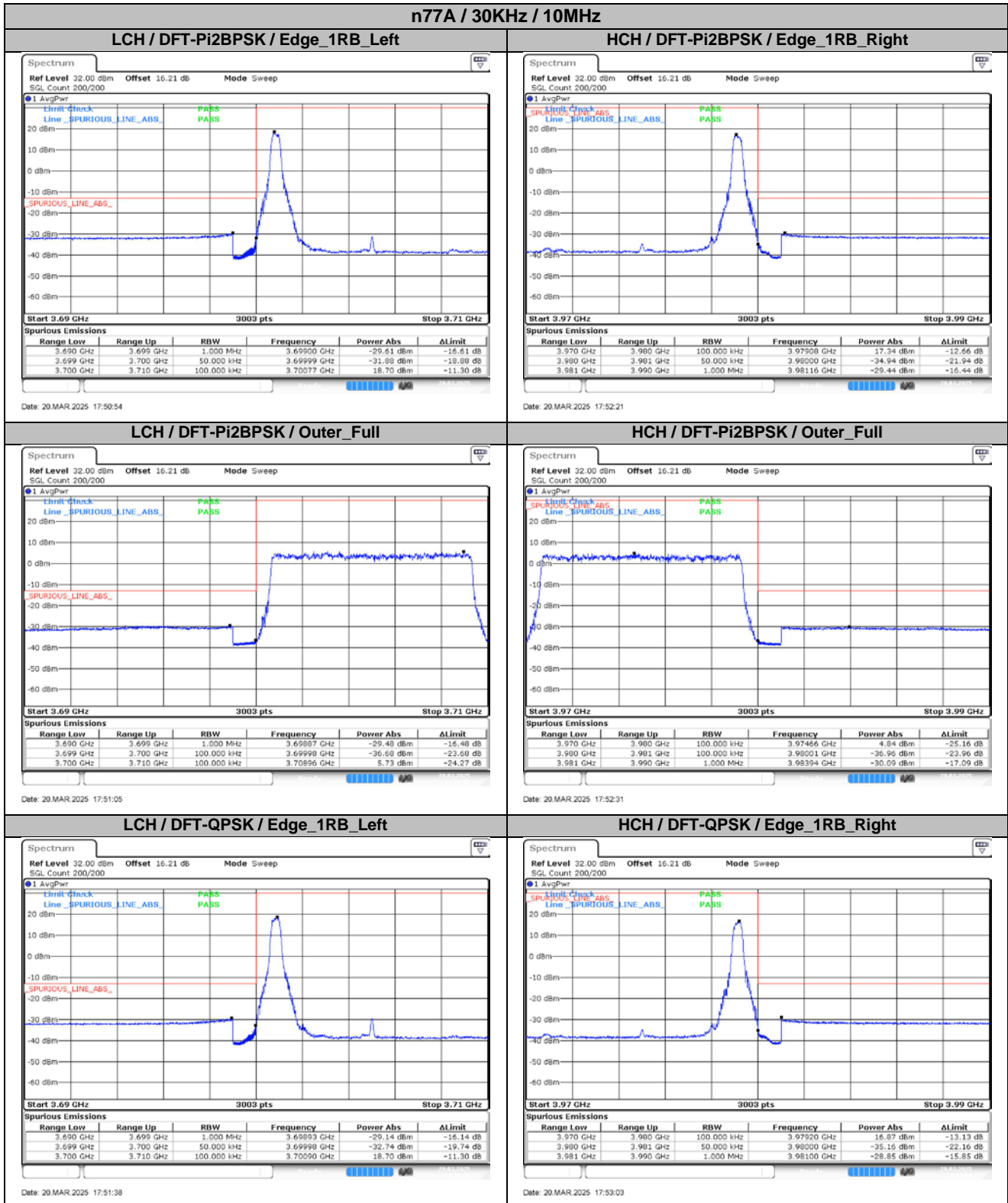
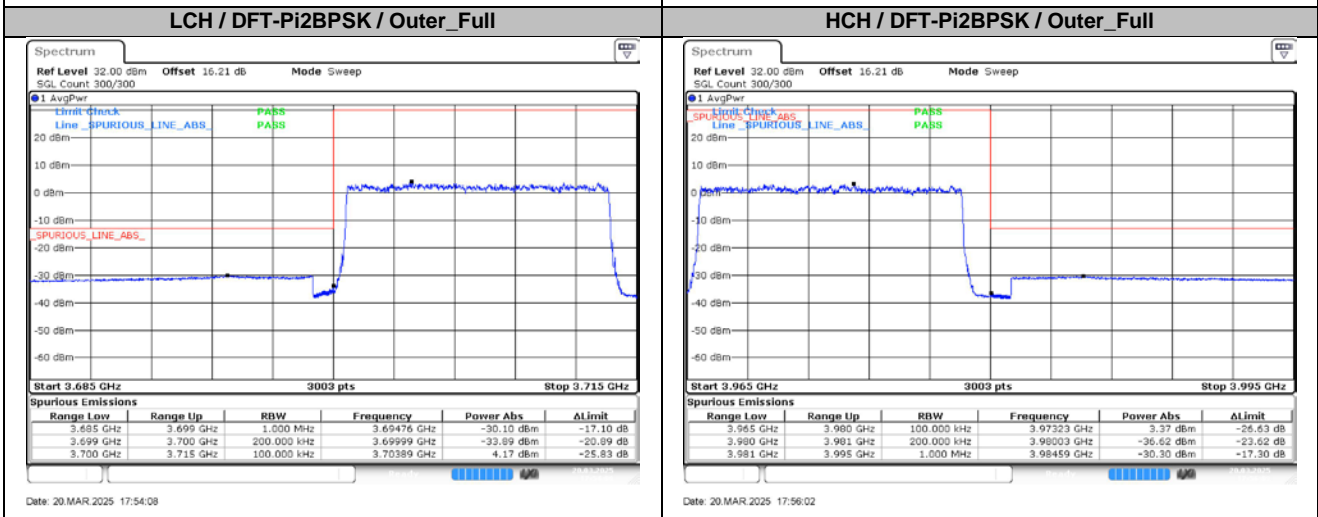
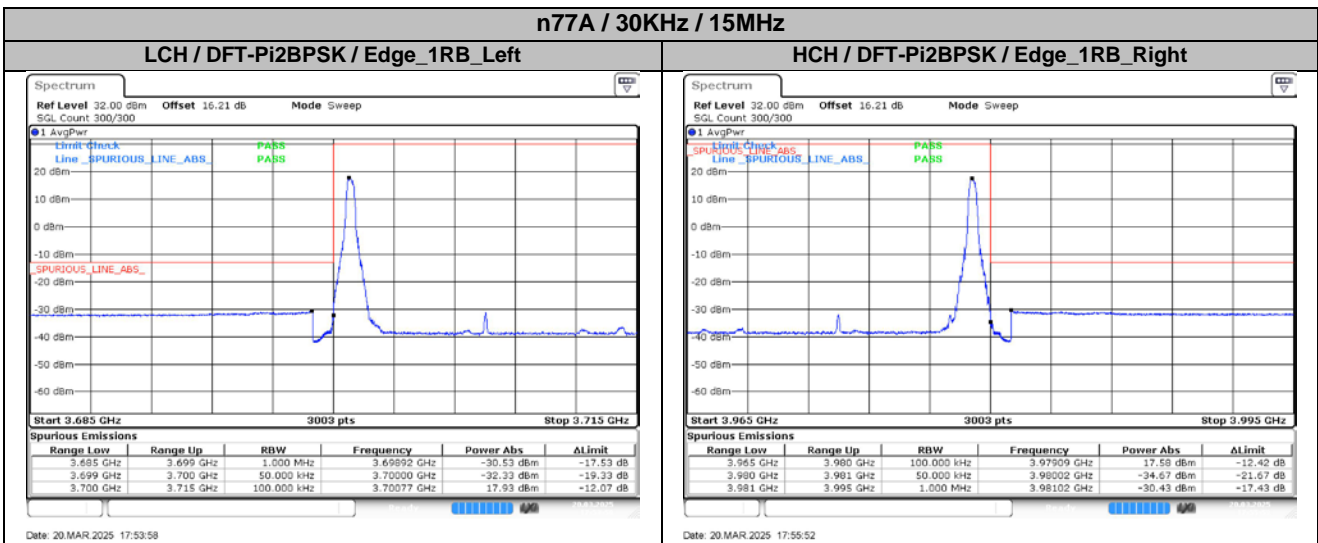
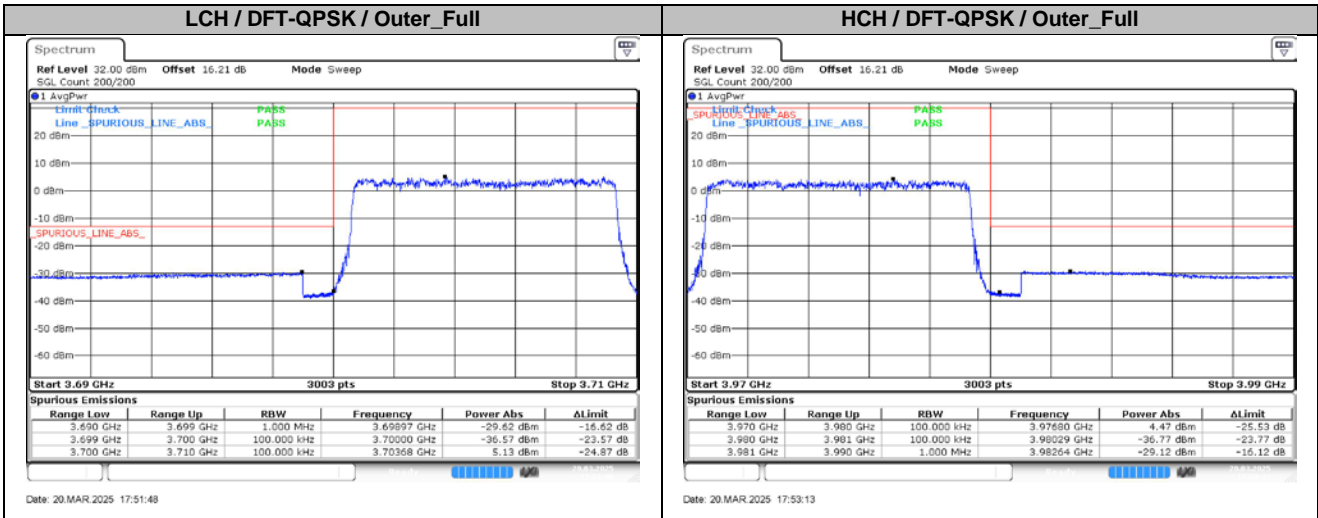
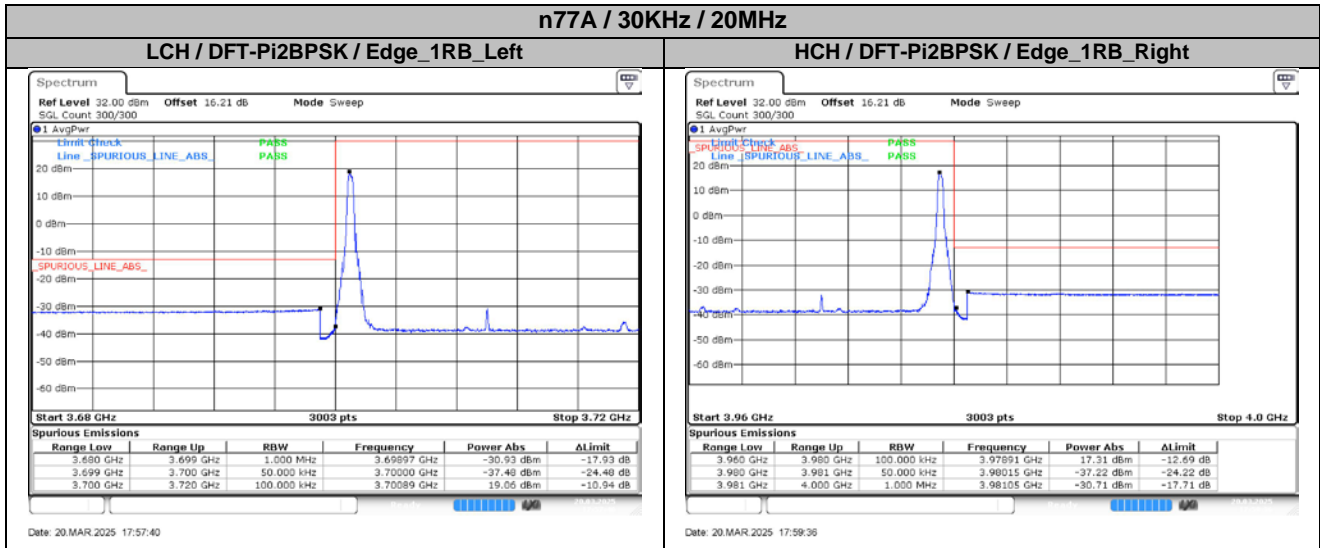
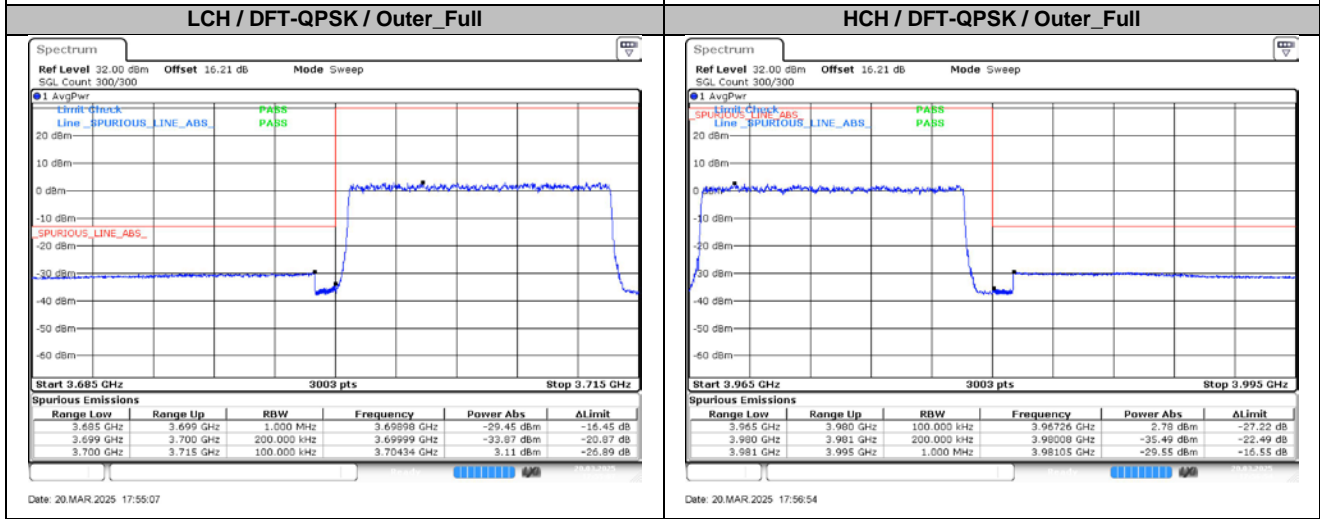
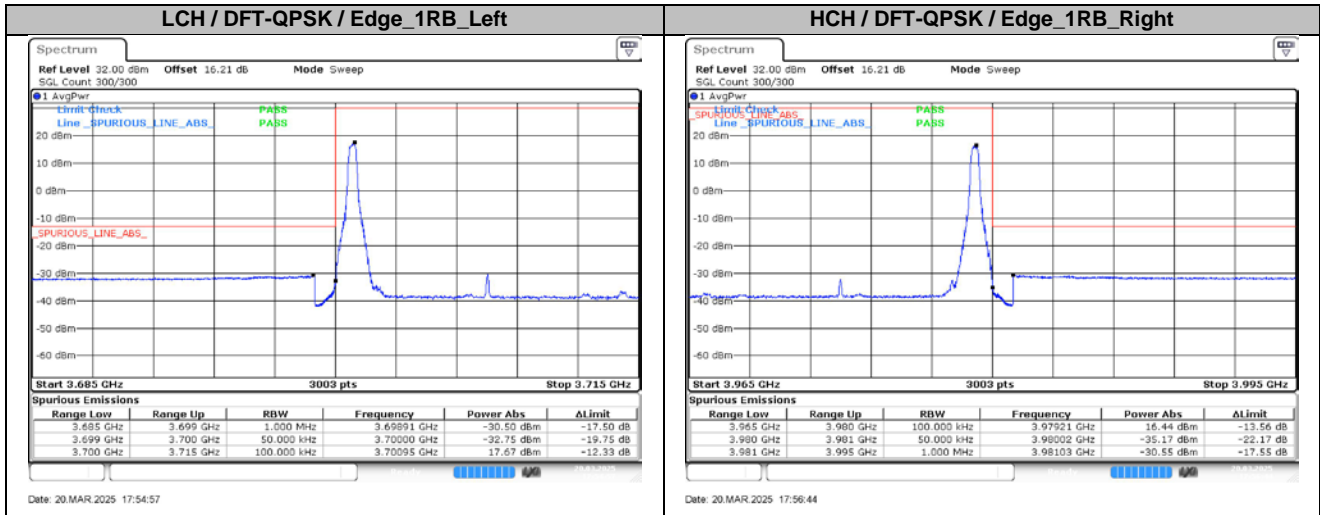


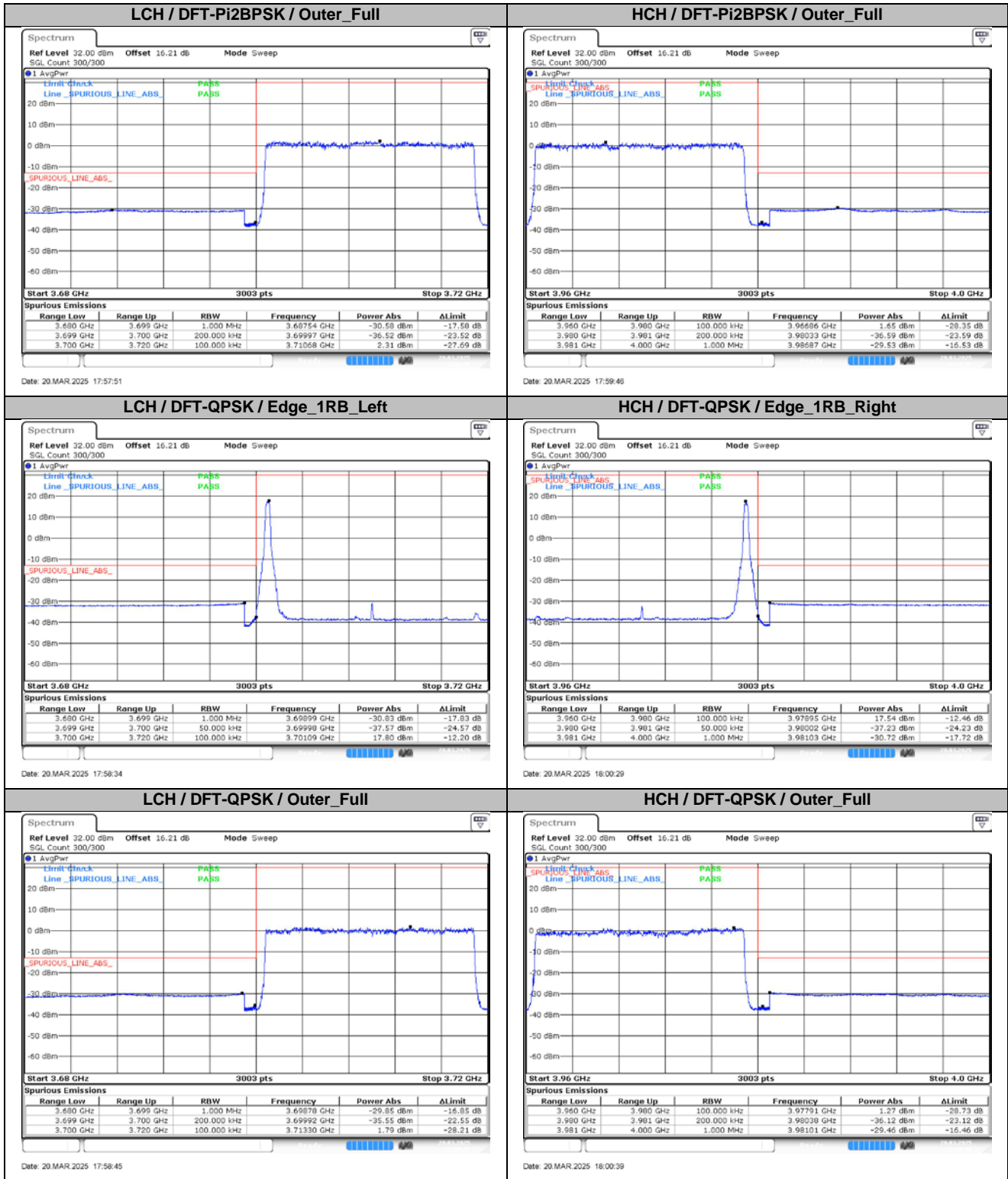
### 5. Conducted Band Edges

#### 5.1. Test Plots for SCS=30KHz



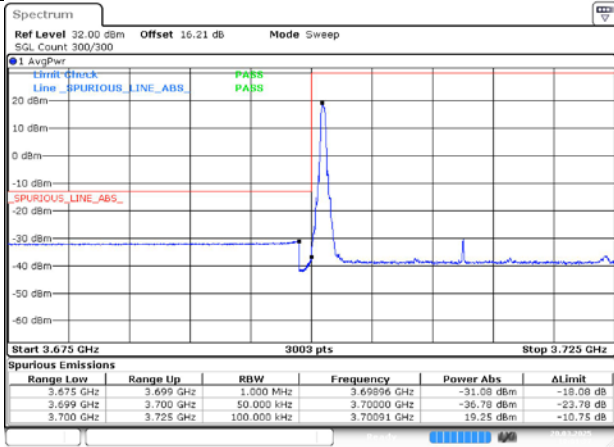






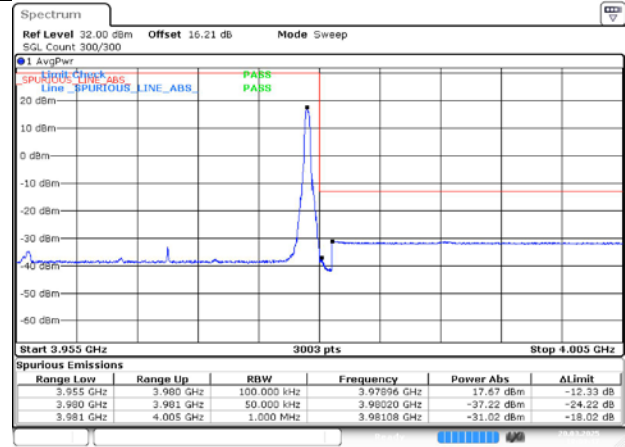
n77A / 30KHz / 25MHz

LCH / DFT-Pi2BPSK / Edge\_1RB\_Left



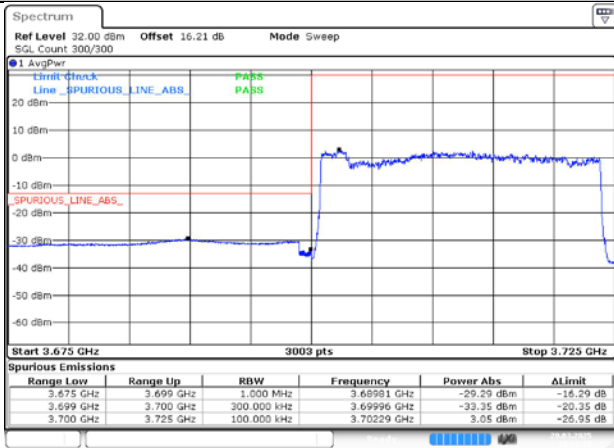
Date: 20.MAR.2025 18:01:28

HCH / DFT-Pi2BPSK / Edge\_1RB\_Right



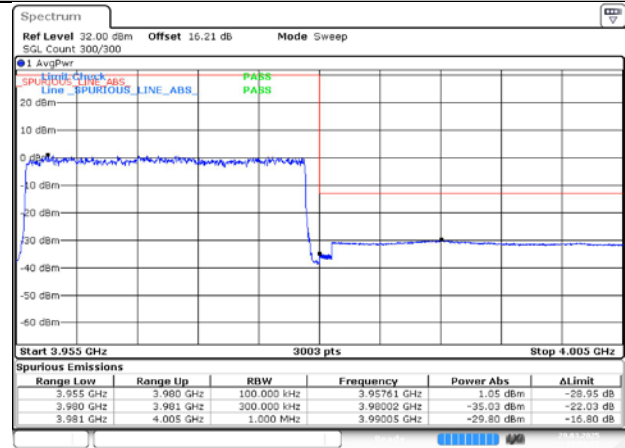
Date: 20.MAR.2025 18:03:17

LCH / DFT-Pi2BPSK / Outer\_Full



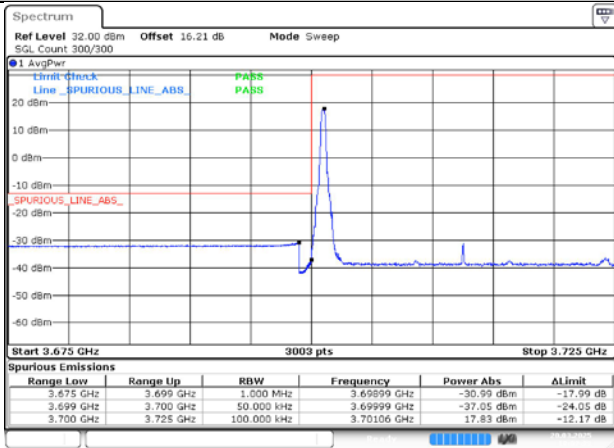
Date: 20.MAR.2025 18:01:38

HCH / DFT-Pi2BPSK / Outer\_Full



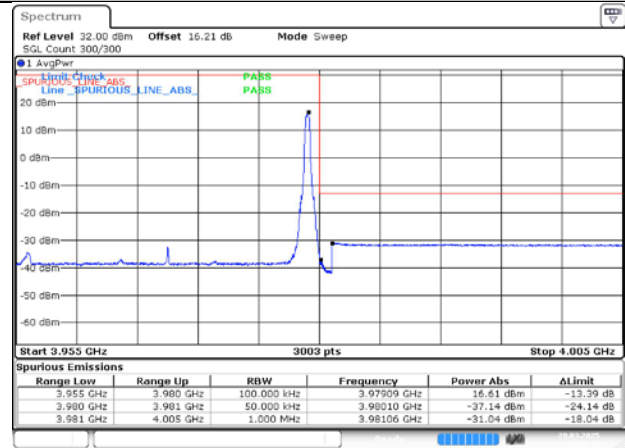
Date: 20.MAR.2025 18:03:27

LCH / DFT-QPSK / Edge\_1RB\_Left

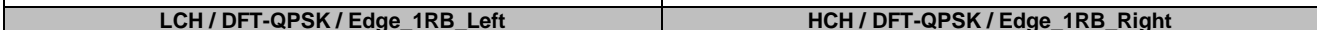
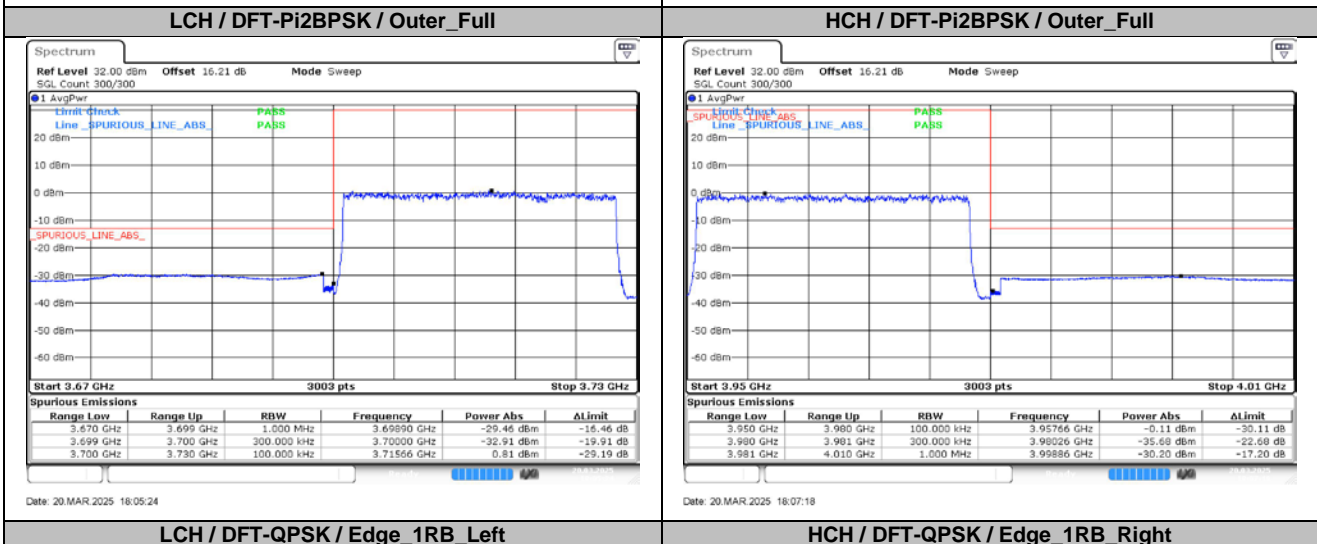
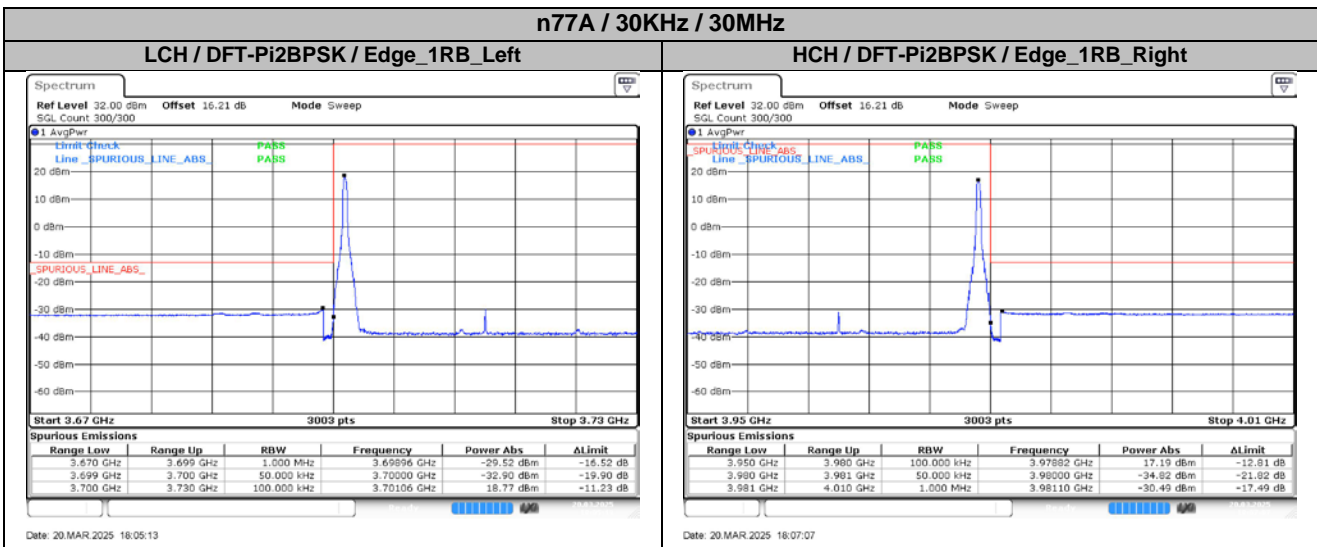
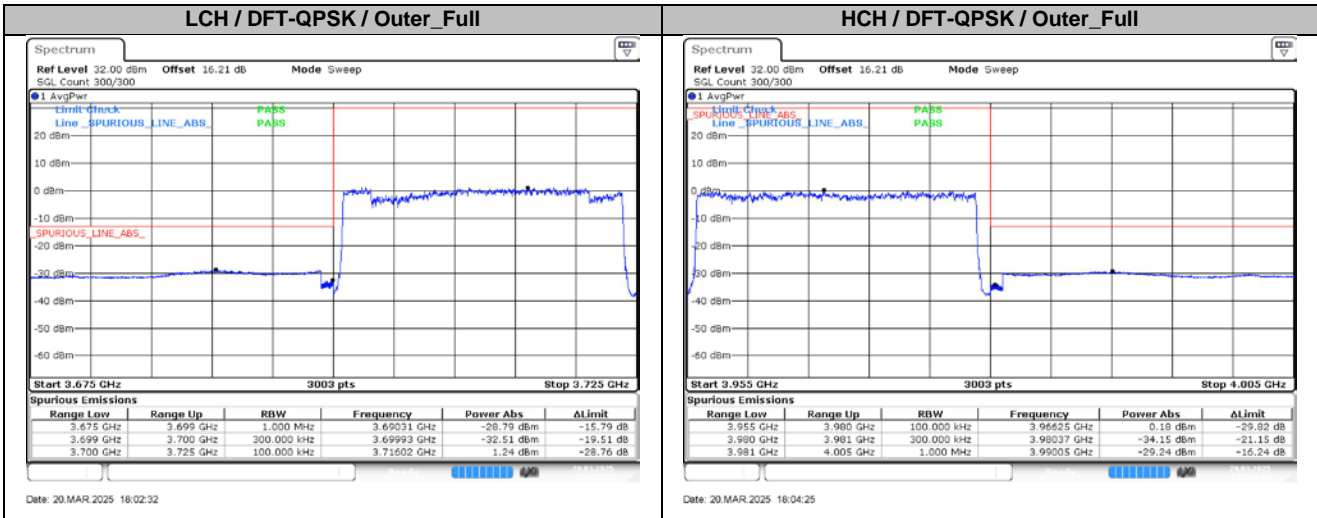


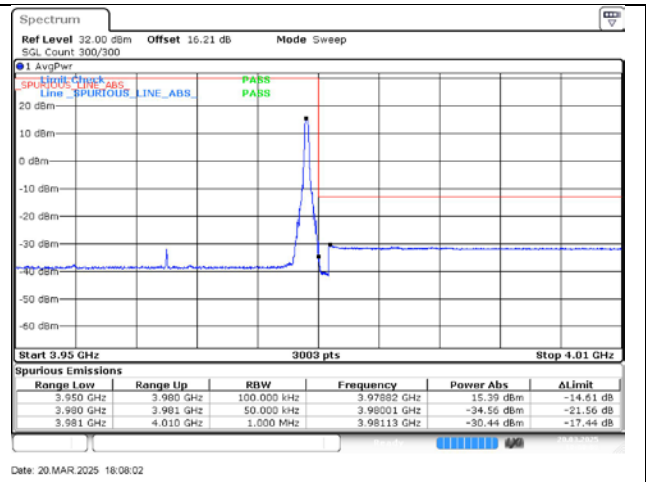
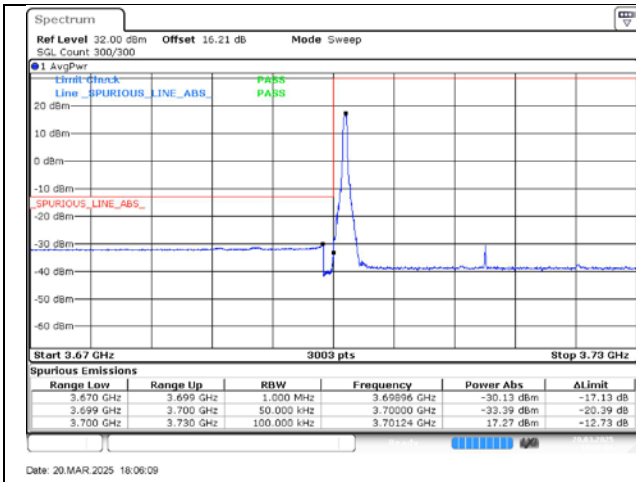
Date: 20.MAR.2025 18:02:22

HCH / DFT-QPSK / Edge\_1RB\_Right



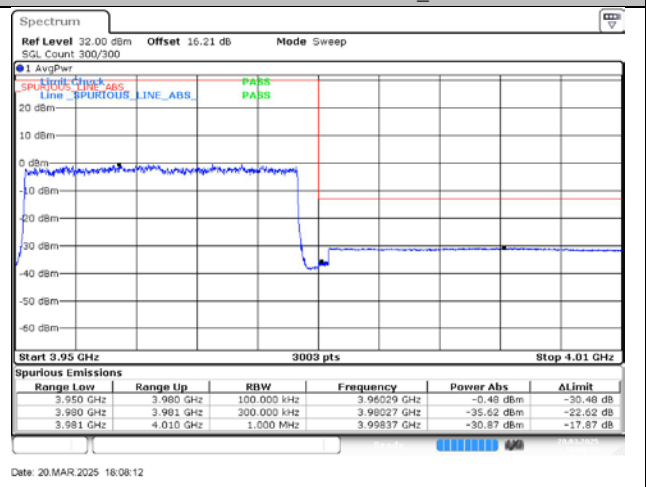
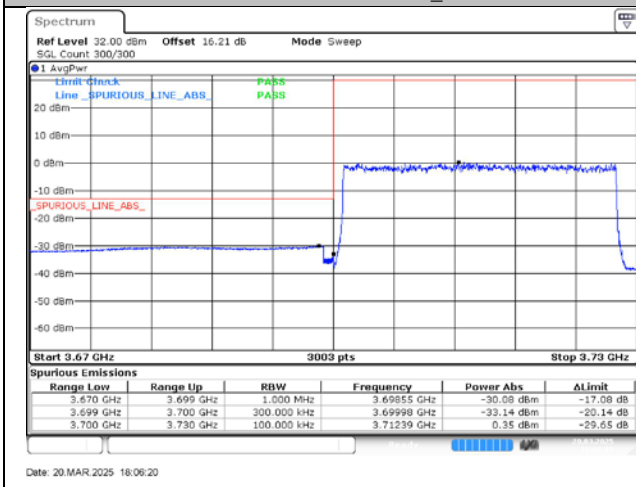
Date: 20.MAR.2025 18:04:16





LCH / DFT-QPSK / Outer\_Full

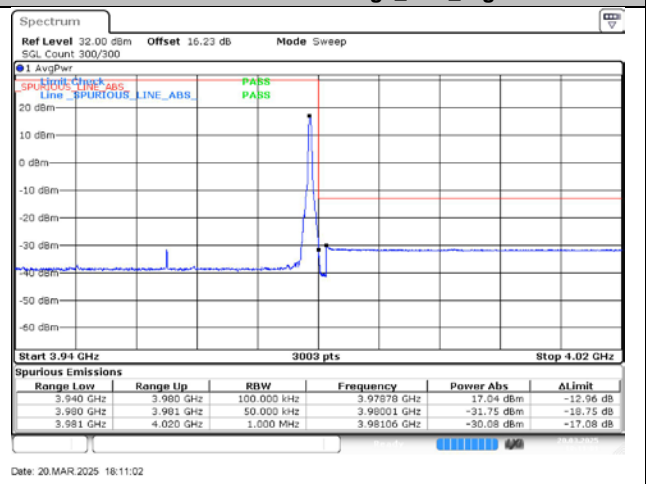
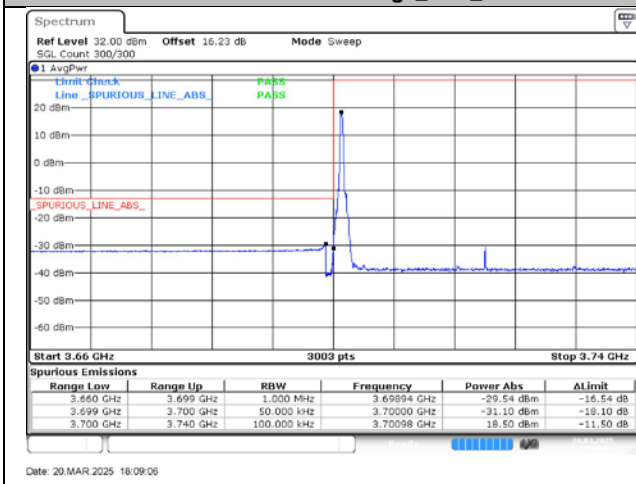
HCH / DFT-QPSK / Outer\_Full

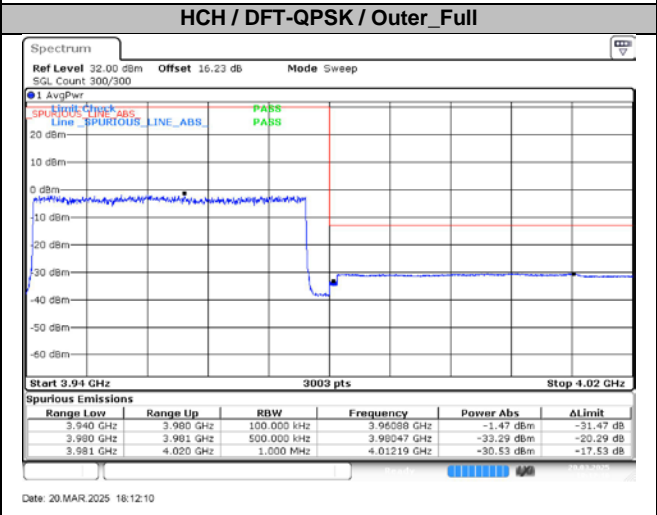
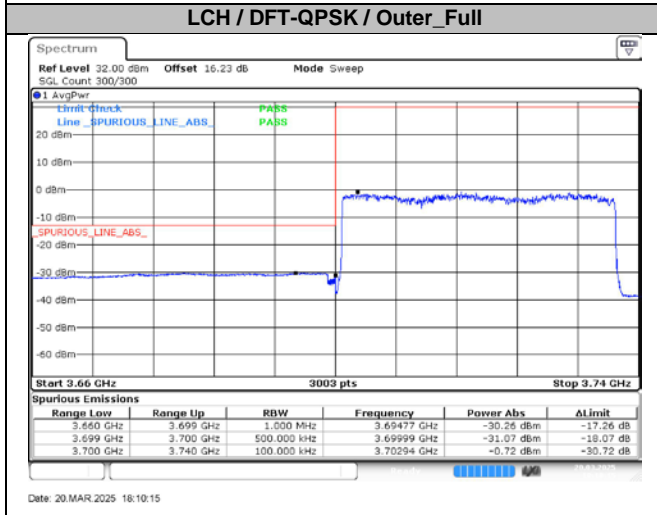
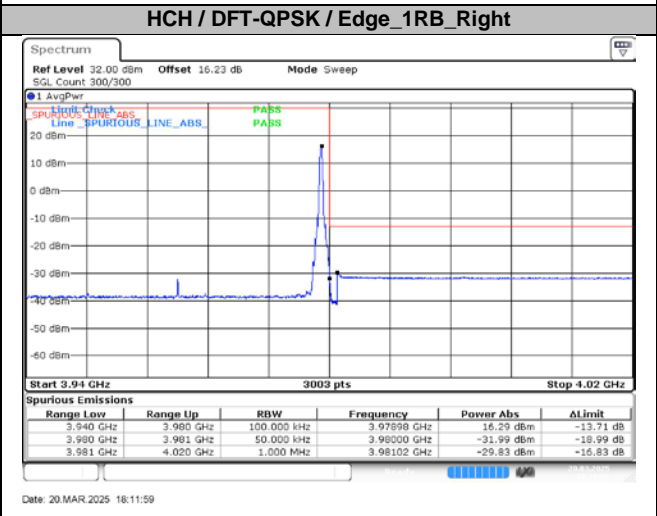
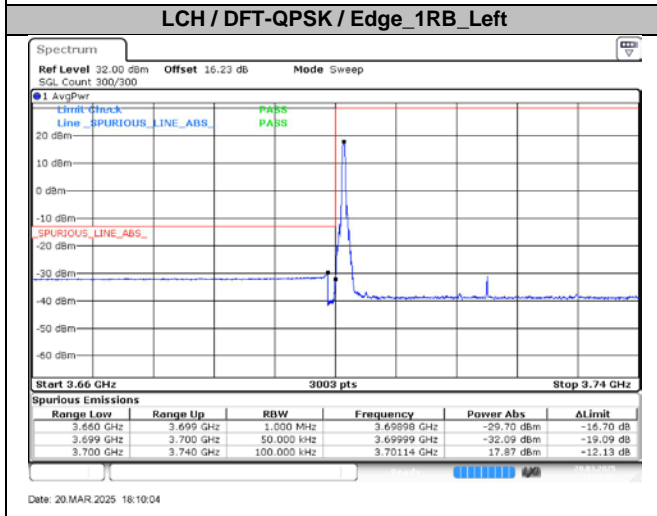
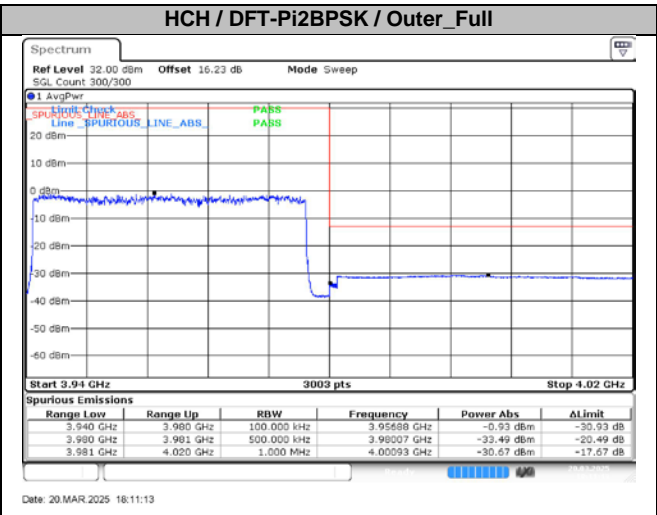
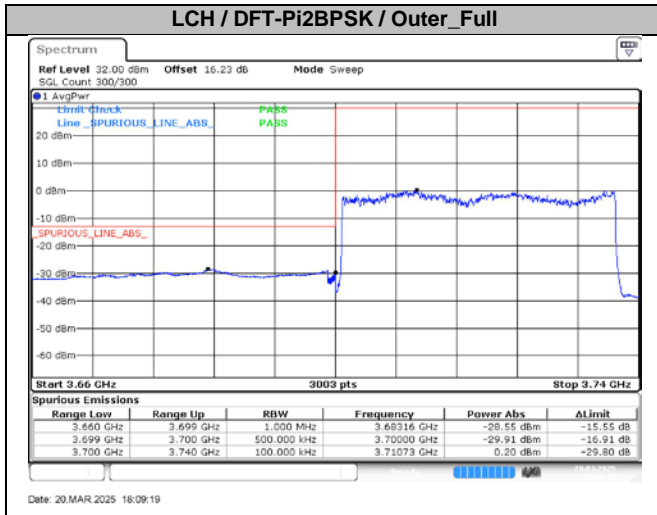


n77A / 30KHz / 40MHz

LCH / DFT-Pi2BPSK / Edge\_1RB\_Left

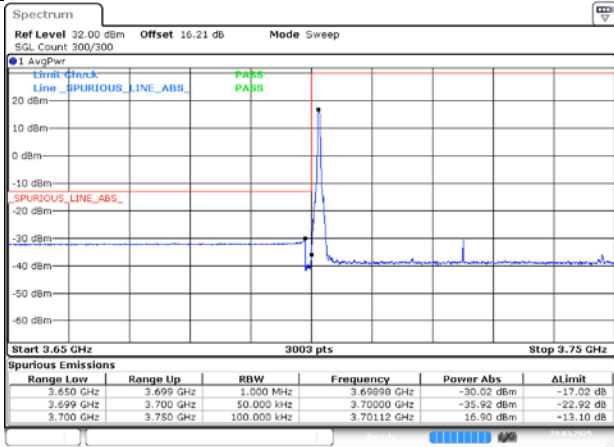
HCH / DFT-Pi2BPSK / Edge\_1RB\_Right





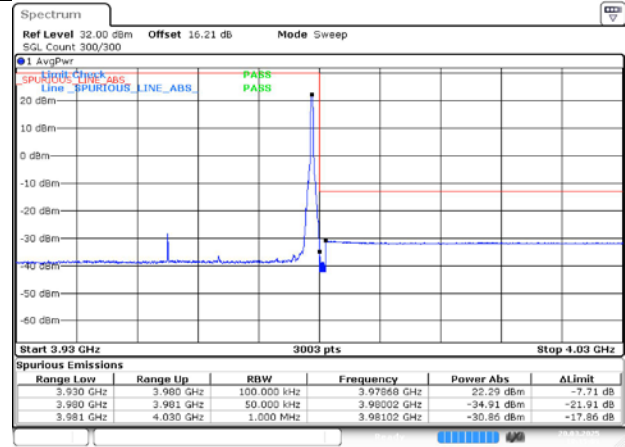
n77A / 30KHz / 50MHz

LCH / DFT-Pi2BPSK / Edge\_1RB\_Left



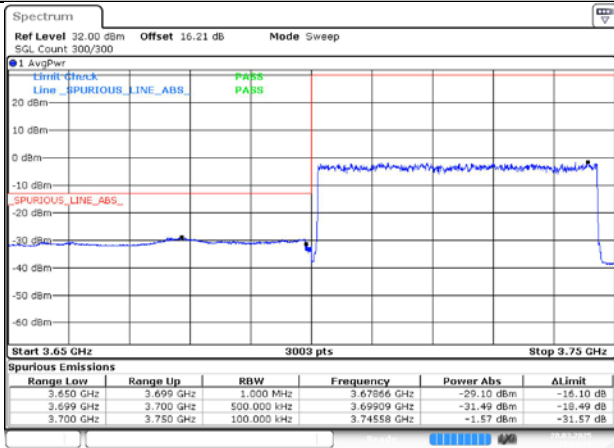
Date: 20.MAR.2025 18:12:58

HCH / DFT-Pi2BPSK / Edge\_1RB\_Right



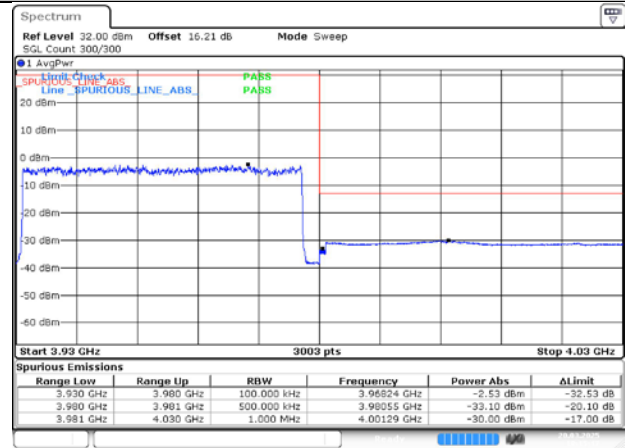
Date: 20.MAR.2025 18:15:04

LCH / DFT-Pi2BPSK / Outer\_Full



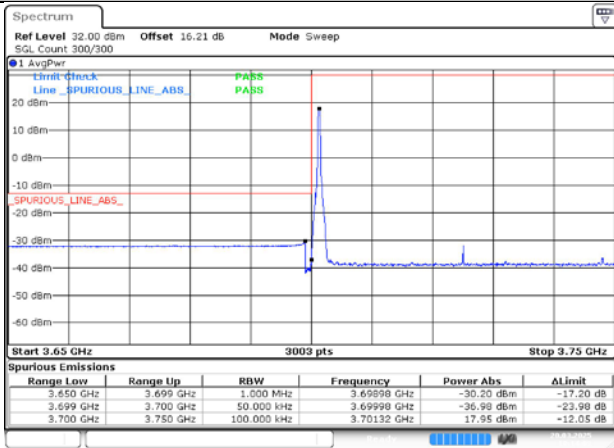
Date: 20.MAR.2025 18:13:10

HCH / DFT-Pi2BPSK / Outer\_Full



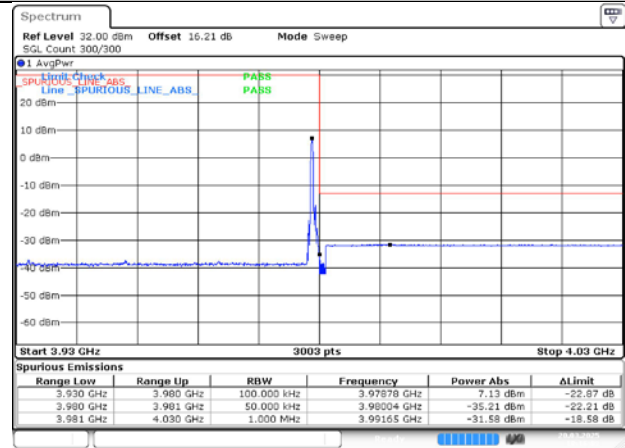
Date: 20.MAR.2025 18:15:16

LCH / DFT-QPSK / Edge\_1RB\_Left

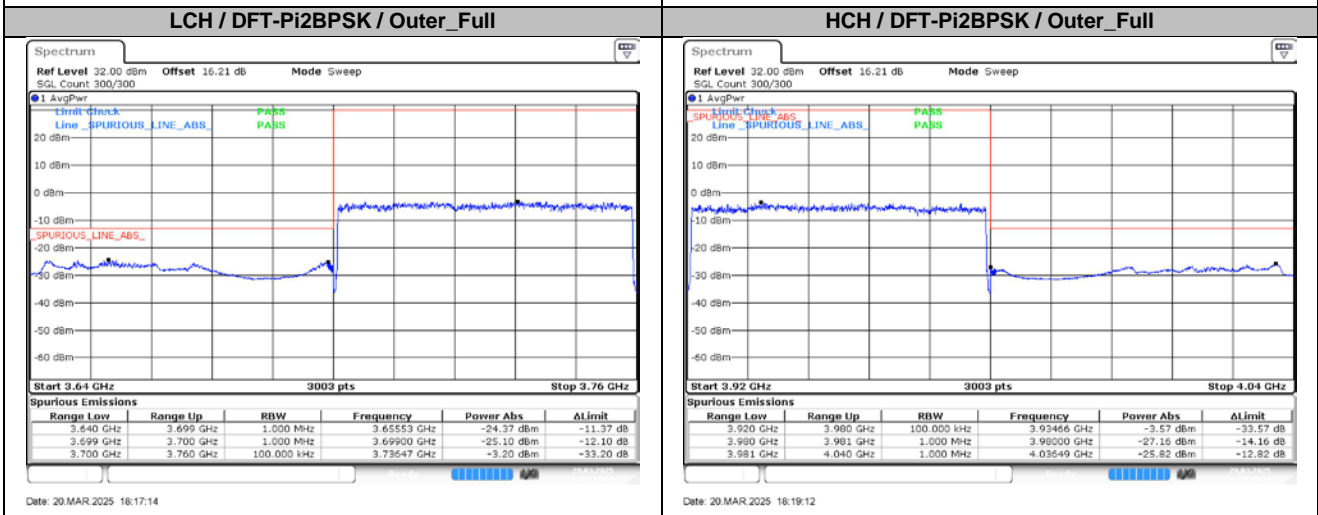
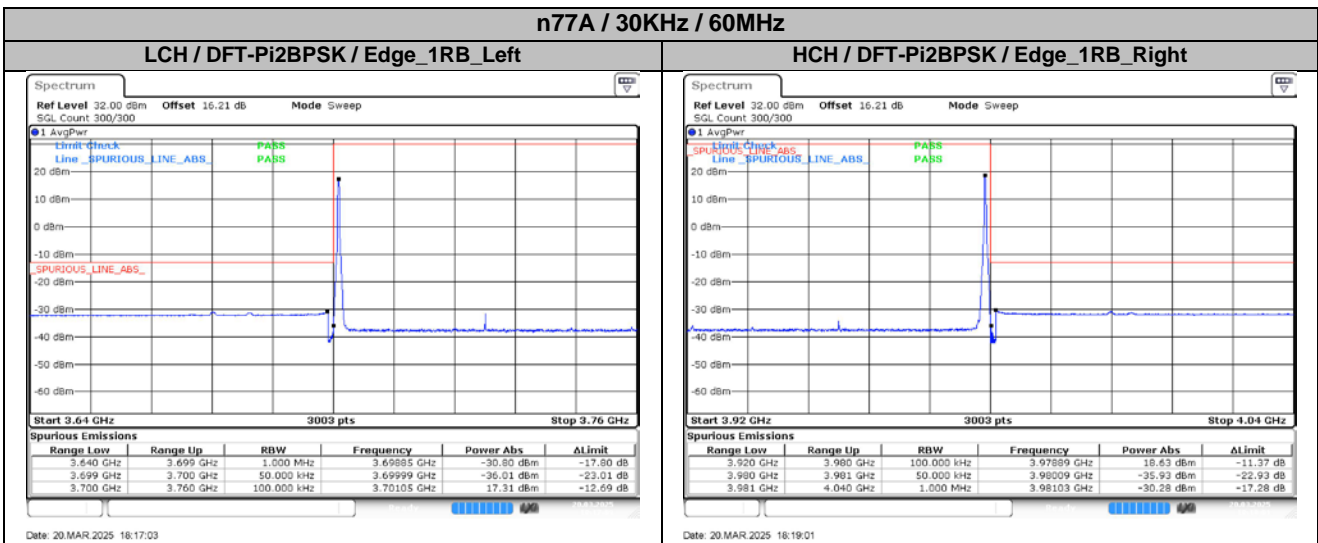
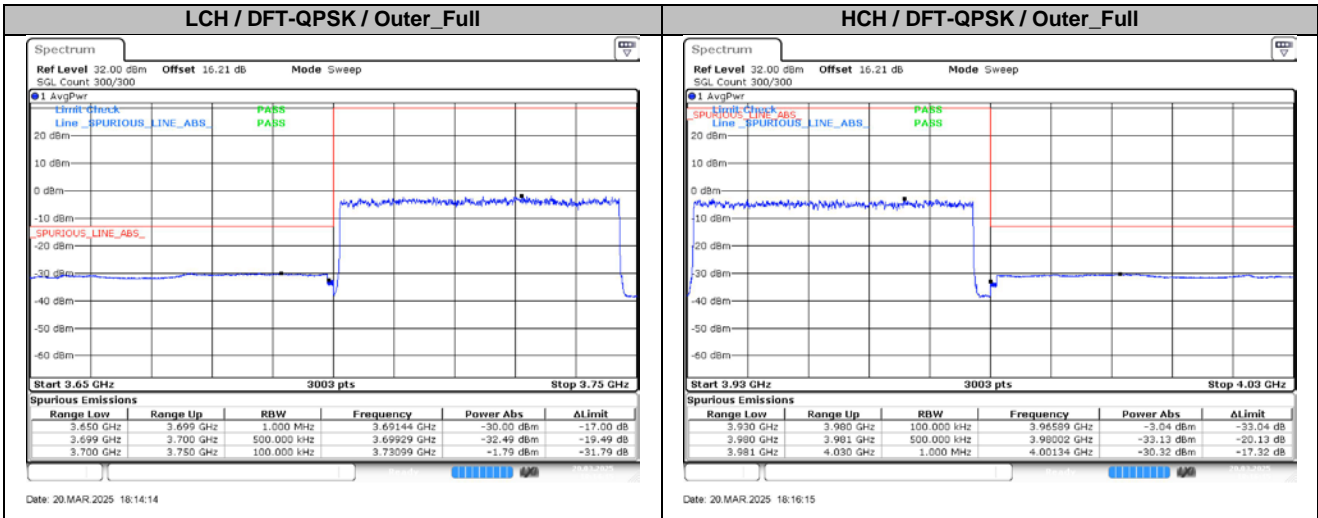


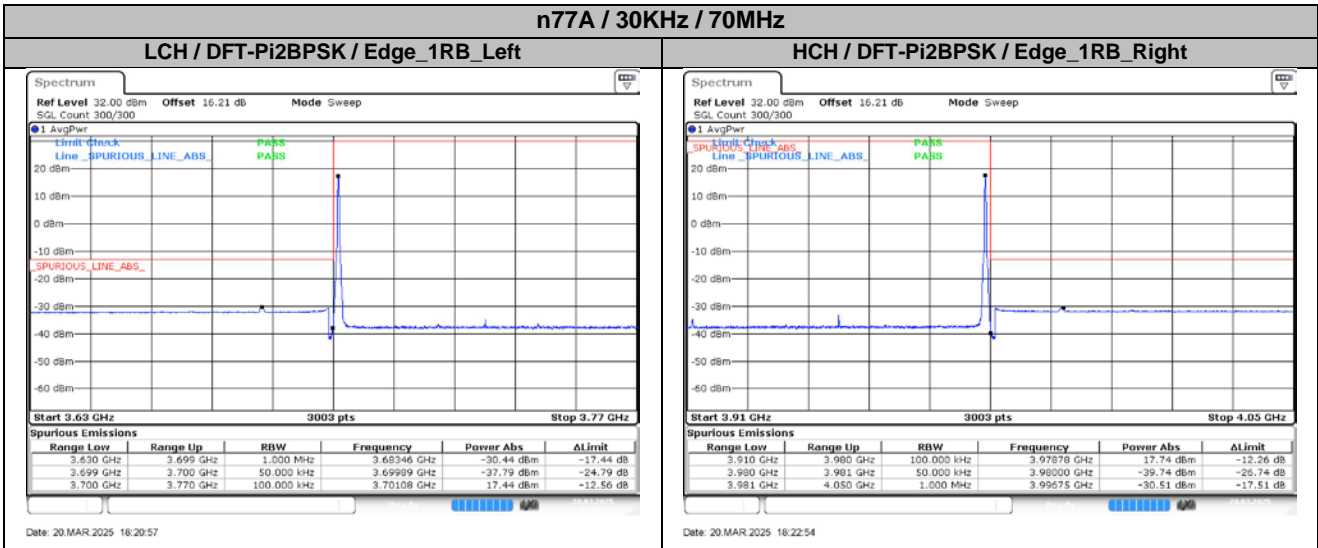
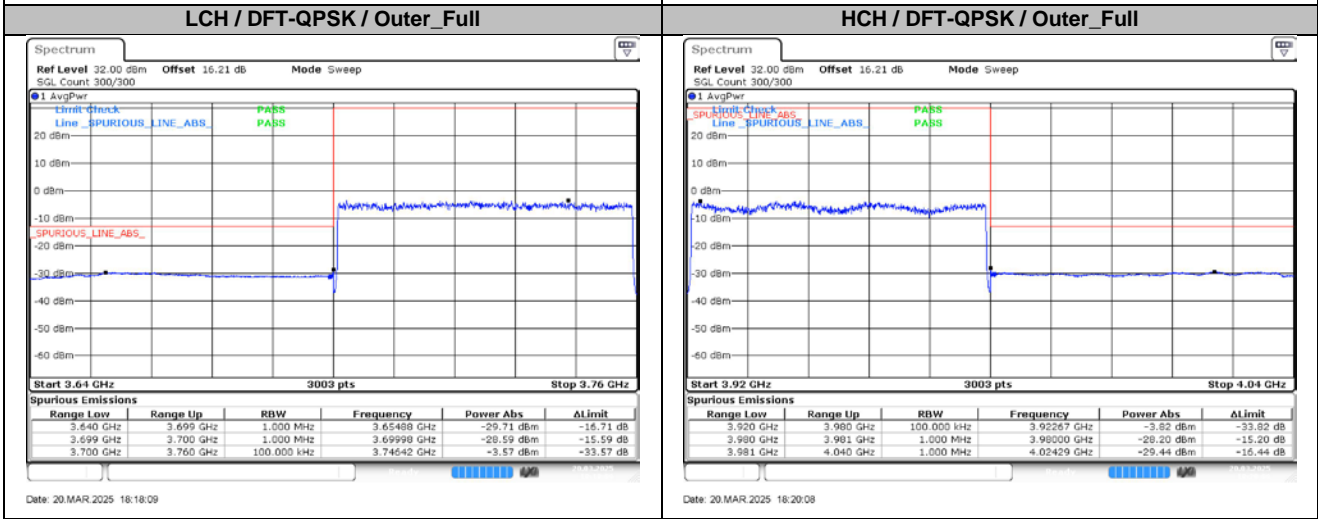
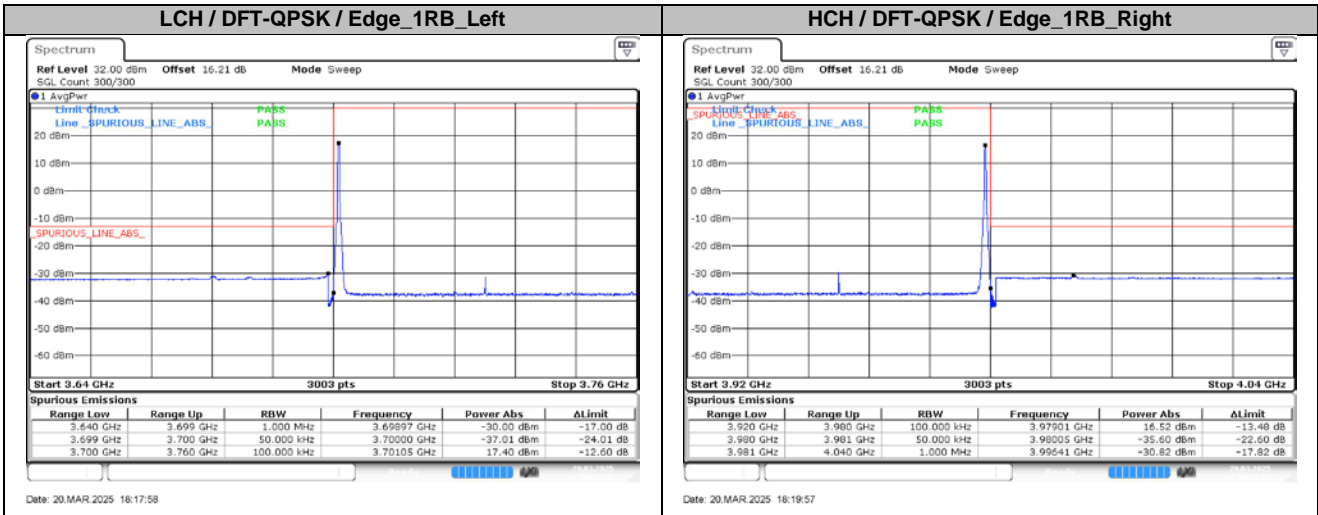
Date: 20.MAR.2025 18:14:02

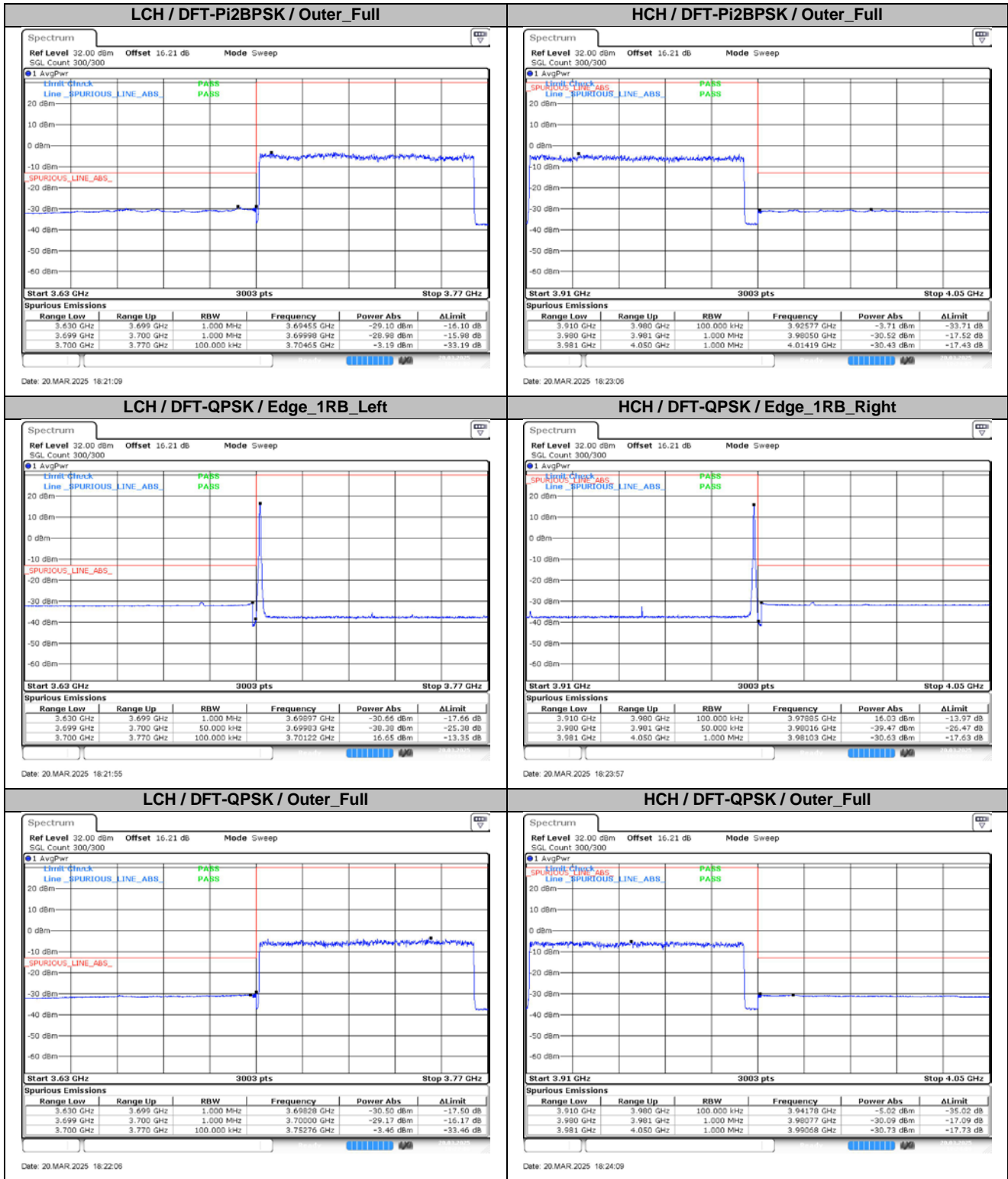
HCH / DFT-QPSK / Edge\_1RB\_Right



Date: 20.MAR.2025 18:16:03

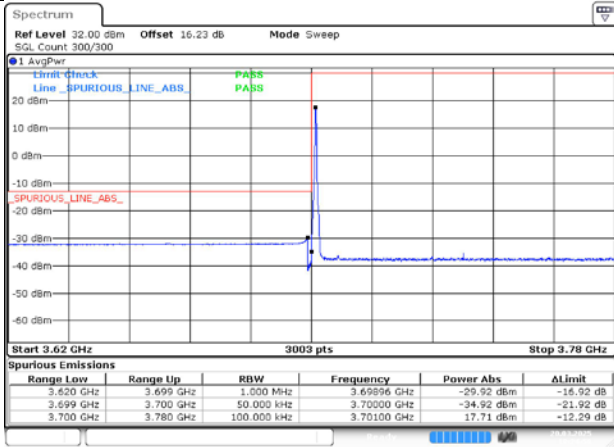






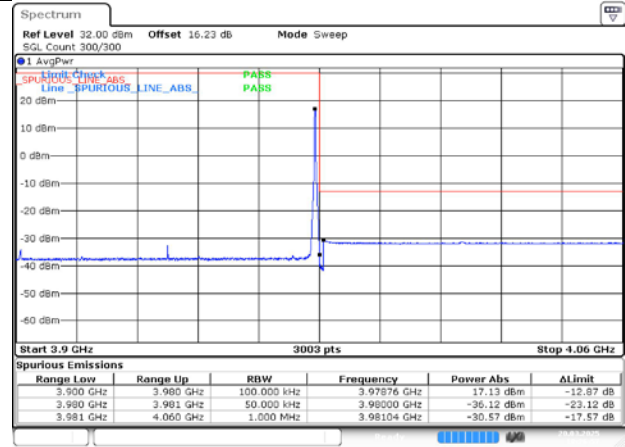
n77A / 30KHz / 80MHz

LCH / DFT-Pi2BPSK / Edge\_1RB\_Left



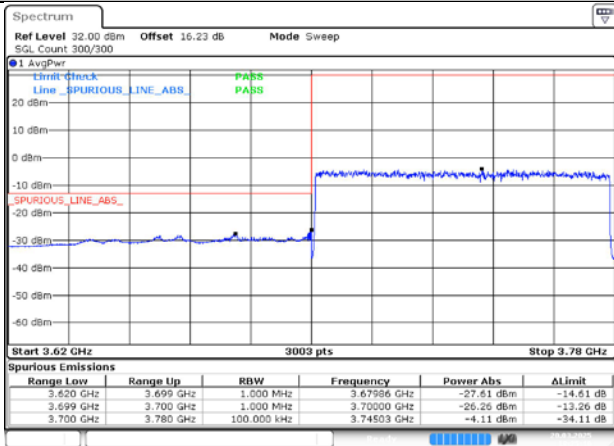
Date: 20.MAR.2025 16:24:58

HCH / DFT-Pi2BPSK / Edge\_1RB\_Right



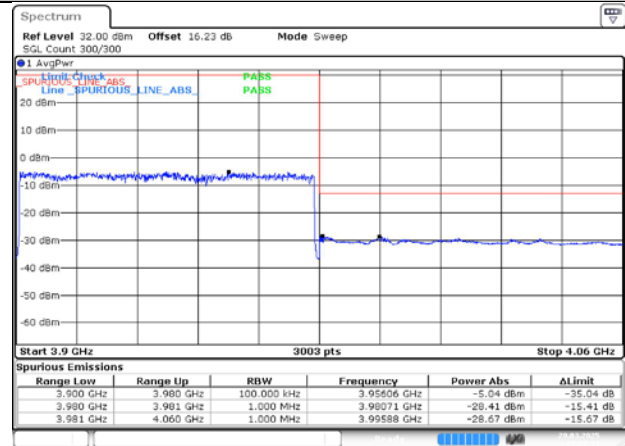
Date: 20.MAR.2025 16:26:58

LCH / DFT-Pi2BPSK / Outer\_Full



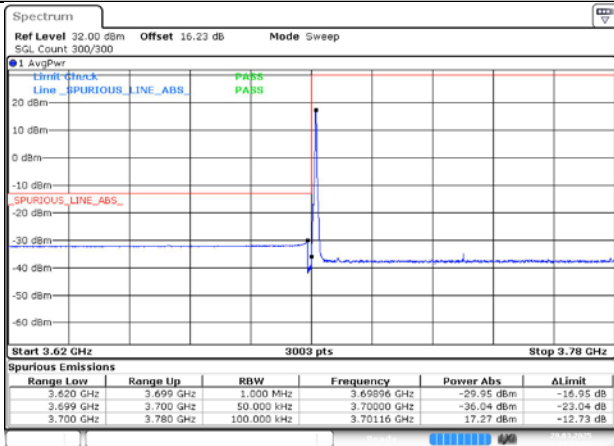
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HCH / DFT-Pi2BPSK / Outer\_Full



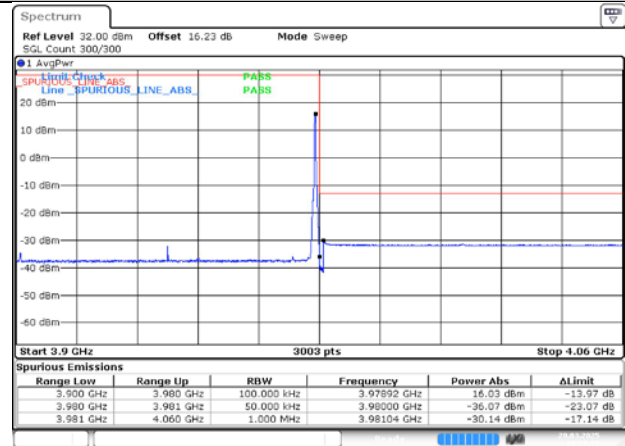
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LCH / DFT-QPSK / Edge\_1RB\_Left

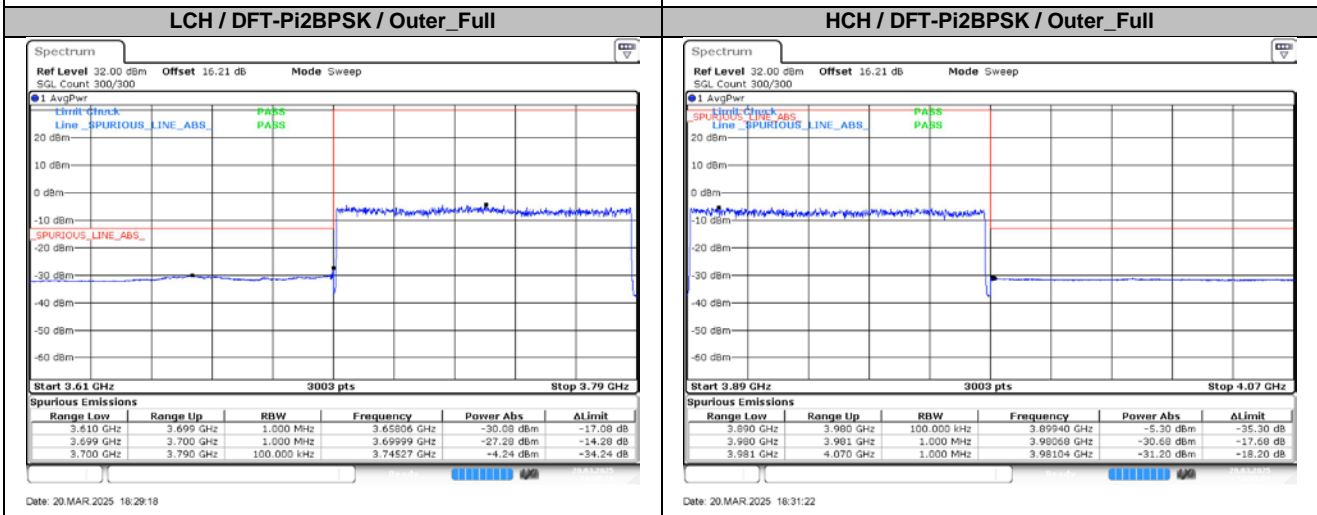
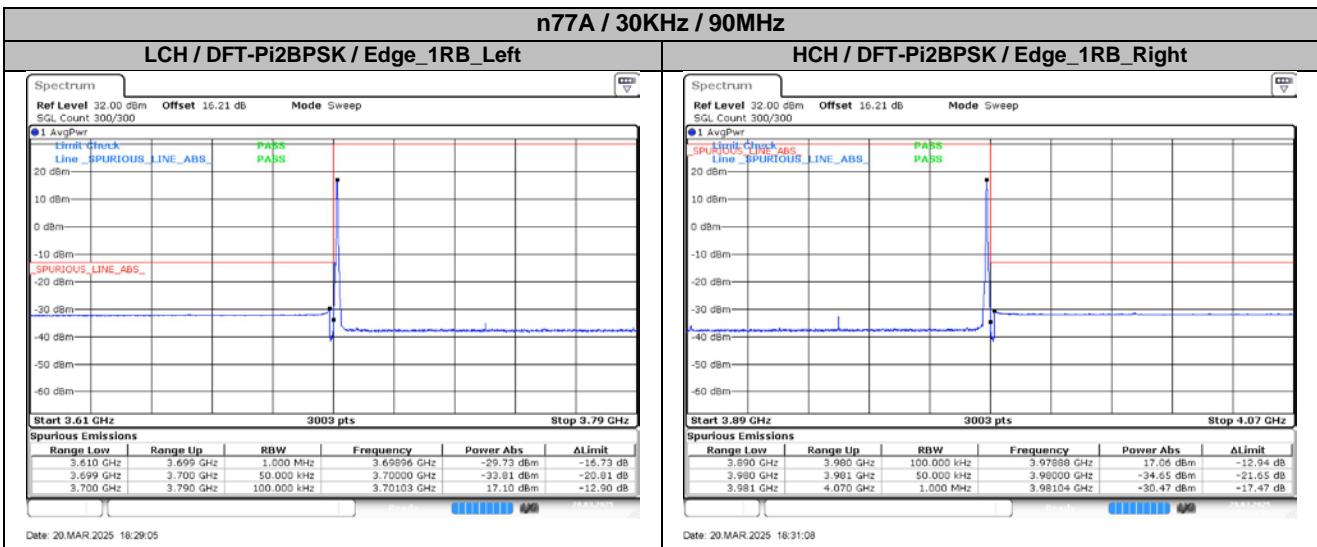
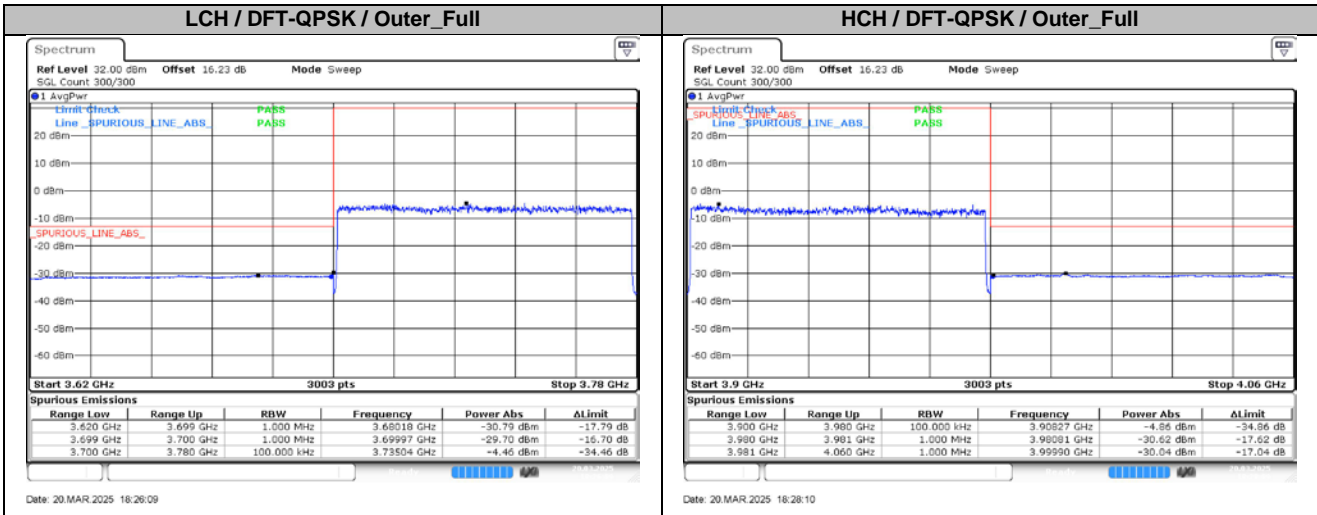


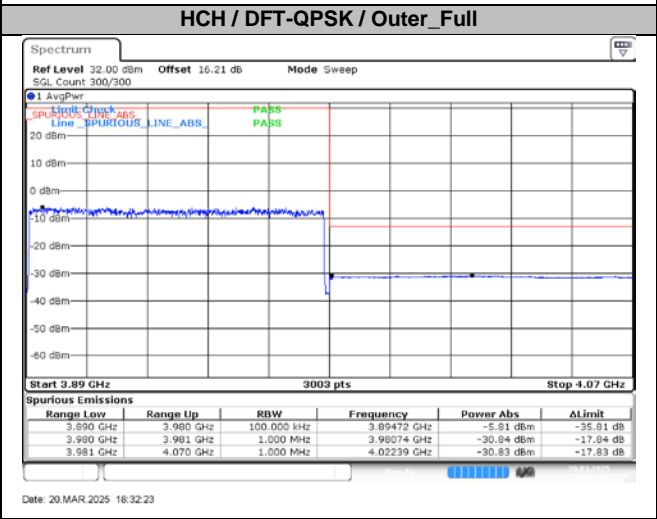
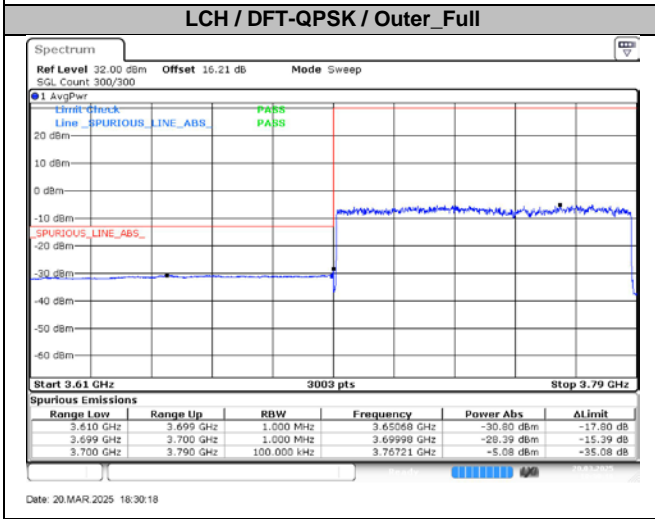
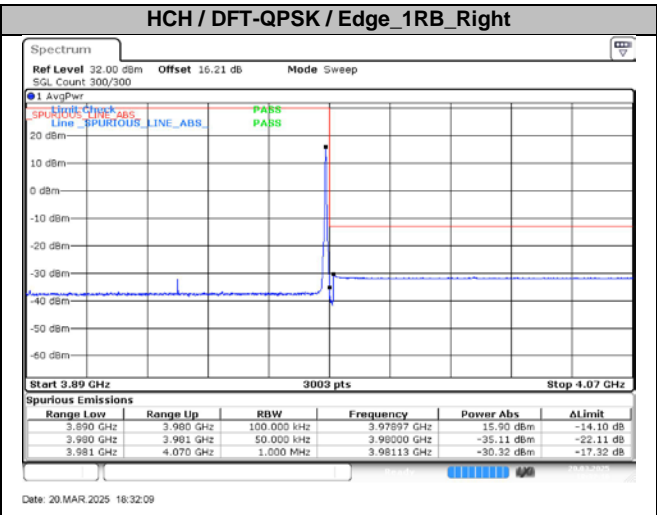
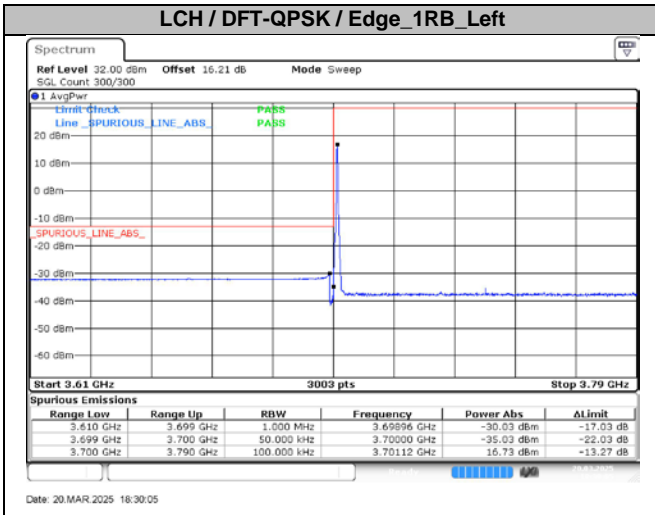
Date: 20.MAR.2025 16:25:56

HCH / DFT-QPSK / Edge\_1RB\_Right



Date: 20.MAR.2025 16:27:57





## n77A / 30kHz / 100MHz

