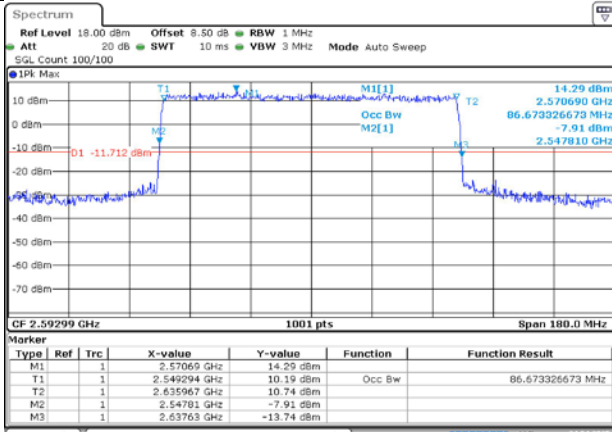


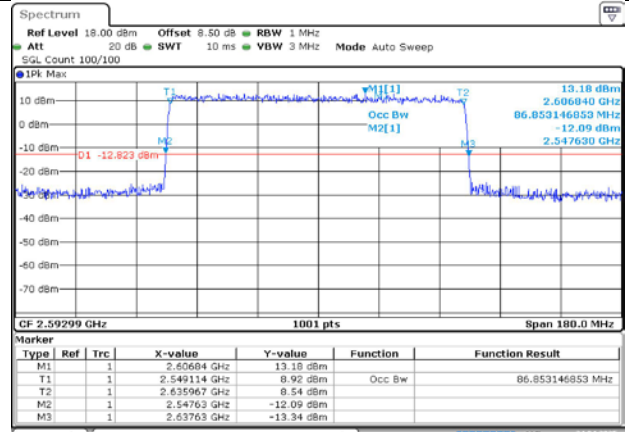
DC_66A_n41A / 30KHz / 90MHz

MCH / DFT-Pi2BPSK / Outer_Full



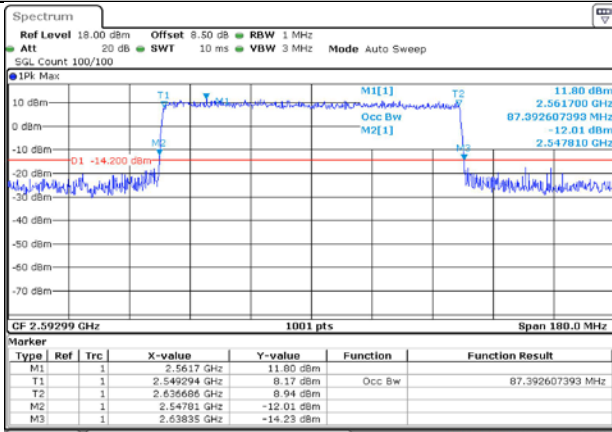
Date 21/4/2025 13:29:47

MCH / DFT-QPSK / Outer_Full



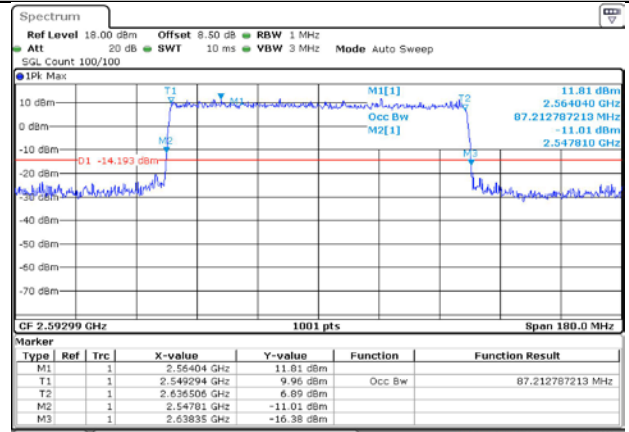
Date 21/4/2025 13:29:54

MCH / CP-QPSK / Outer_Full



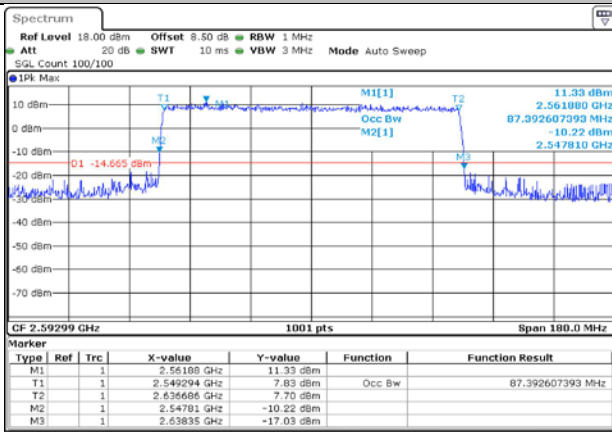
Date 21/4/2025 13:30:12

MCH / CP-16QAM / Outer_Full



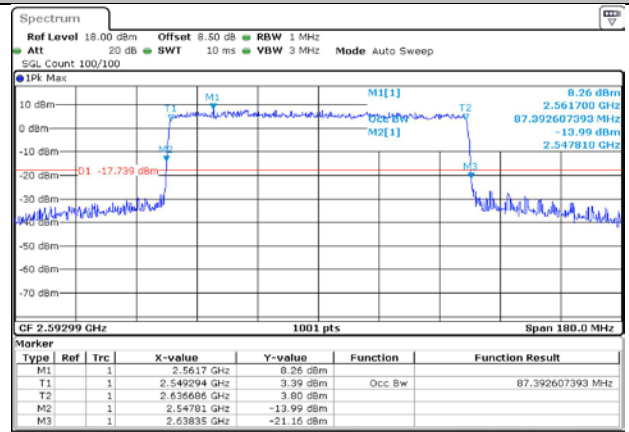
Date 21/4/2025 13:30:17

MCH / CP-64QAM / Outer_Full



Date 21/4/2025 13:30:21

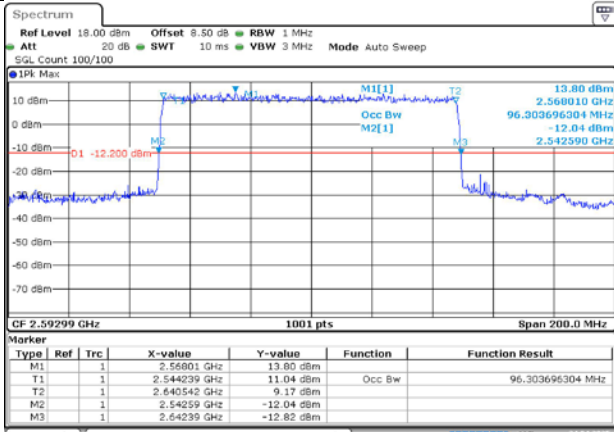
MCH / CP-256QAM / Outer_Full



Date 21/4/2025 13:30:26

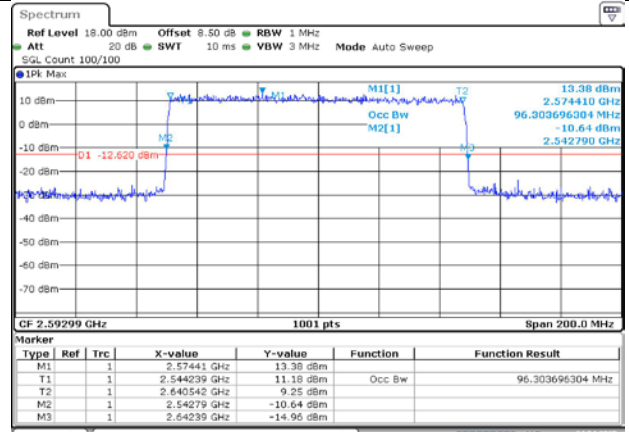
DC_66A_n41A / 30KHz / 100MHz

MCH / DFT-Pi2BPSK / Outer_Full



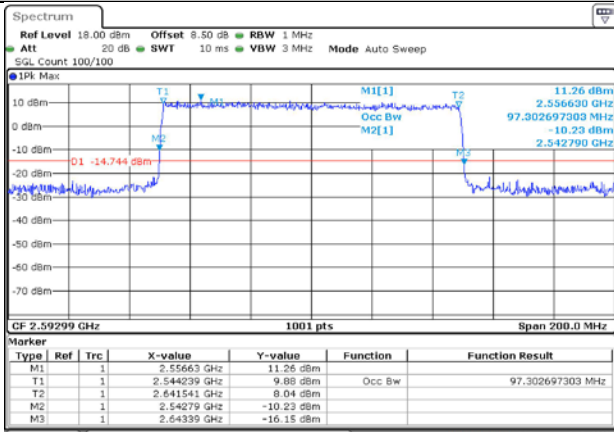
Date 24/07/2025 13:30:41

MCH / DFT-QPSK / Outer_Full



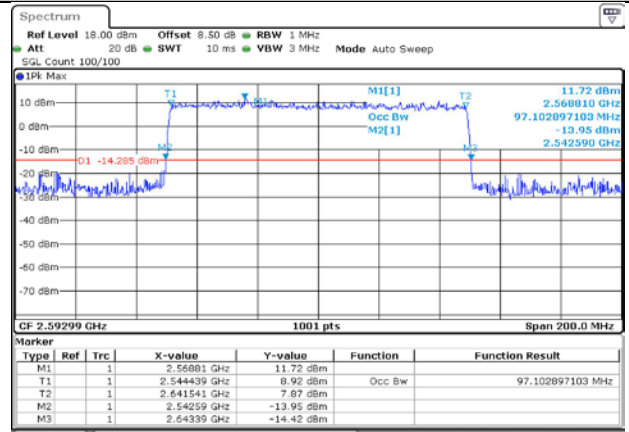
Date 24/07/2025 13:30:41

MCH / CP-QPSK / Outer_Full



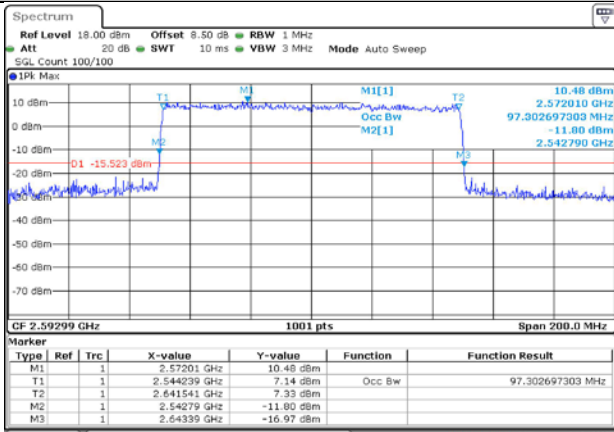
Date 24/07/2025 13:30:50

MCH / CP-16QAM / Outer_Full



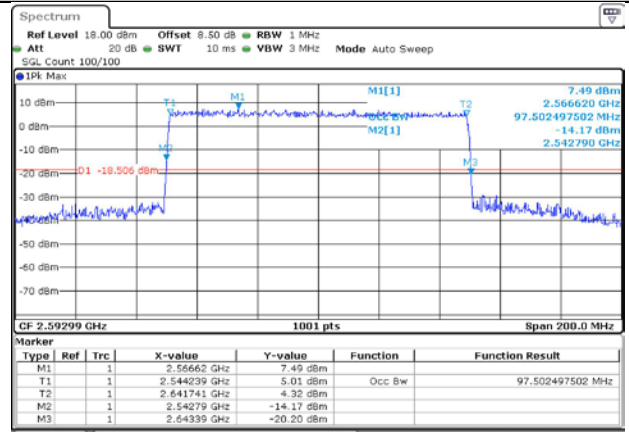
Date 24/07/2025 13:31:03

MCH / CP-64QAM / Outer_Full



Date 24/07/2025 13:31:08

MCH / CP-256QAM / Outer_Full



Date 24/07/2025 13:31:12

5. Conducted Band Edges

5.1. Test Plots for SCS=30KHz

