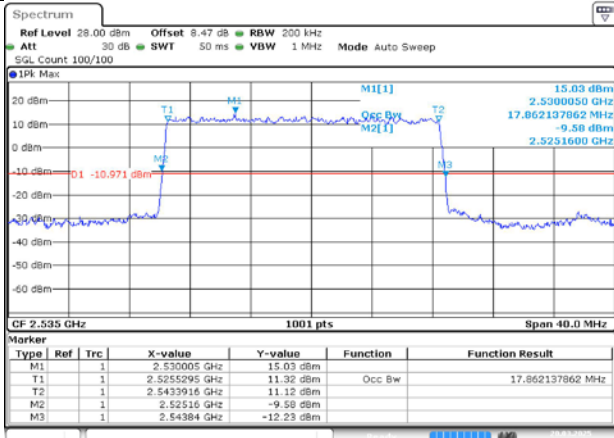


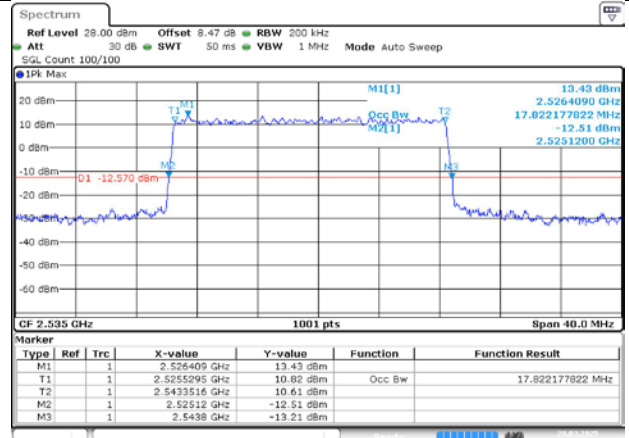
n7A / 15KHz / 20MHz

MCH / DFT-Pi2BPSK / Outer_Full



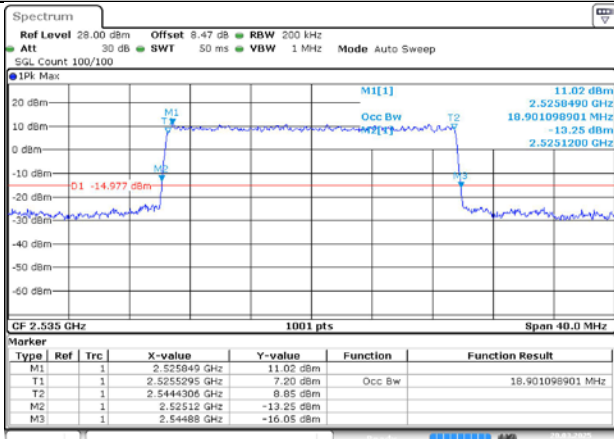
Date: 20.MAR.2025 10:39:30

MCH / DFT-QPSK / Outer_Full



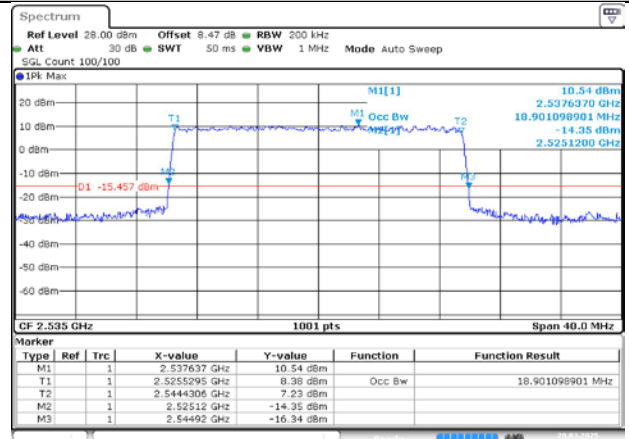
Date: 20.MAR.2025 10:39:52

MCH / CP-QPSK / Outer_Full



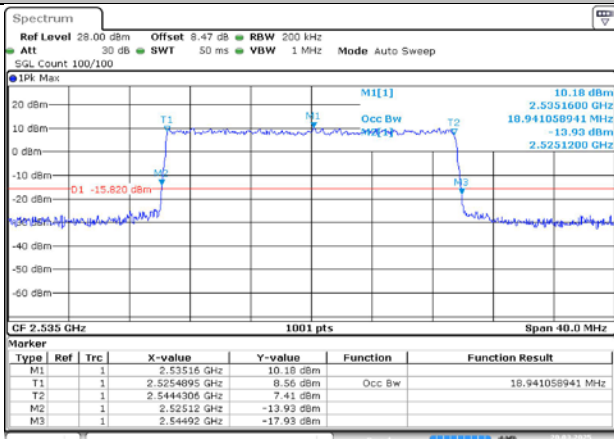
Date: 20.MAR.2025 10:40:32

MCH / CP-16QAM / Outer_Full



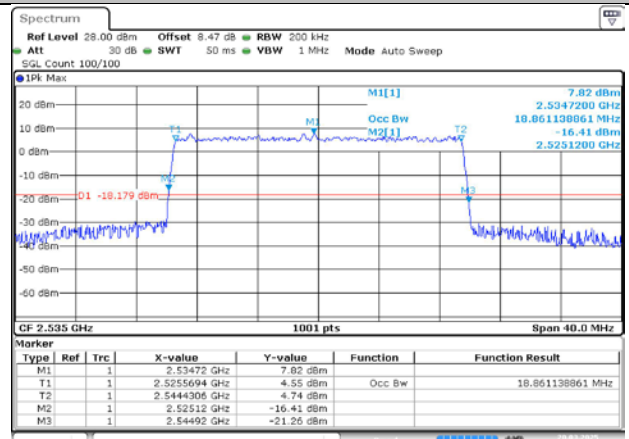
Date: 20.MAR.2025 10:40:40

MCH / CP-64QAM / Outer_Full



Date: 20.MAR.2025 10:40:49

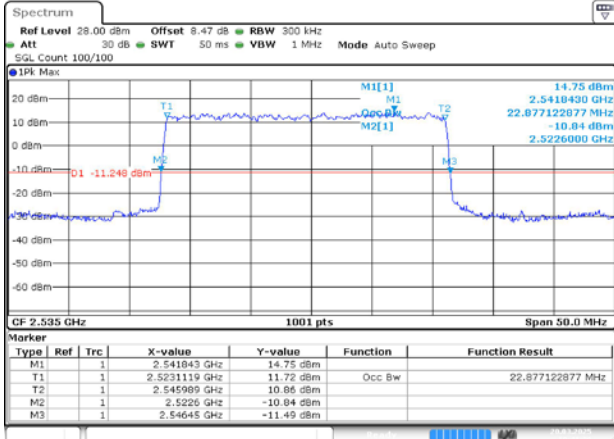
MCH / CP-256QAM / Outer_Full



Date: 20.MAR.2025 10:40:59

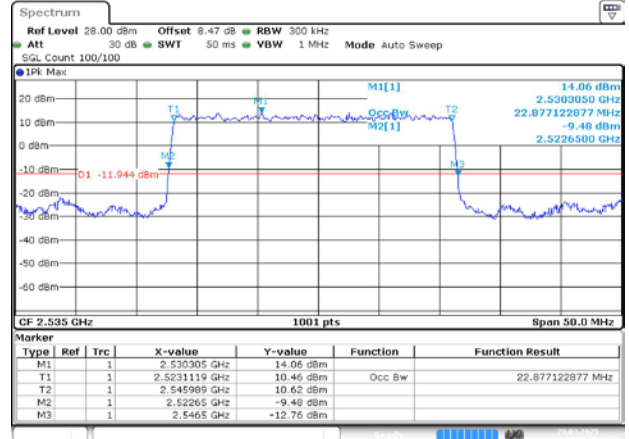
n7A / 15KHz / 25MHz

MCH / DFT-Pi2BPSK / Outer_Full



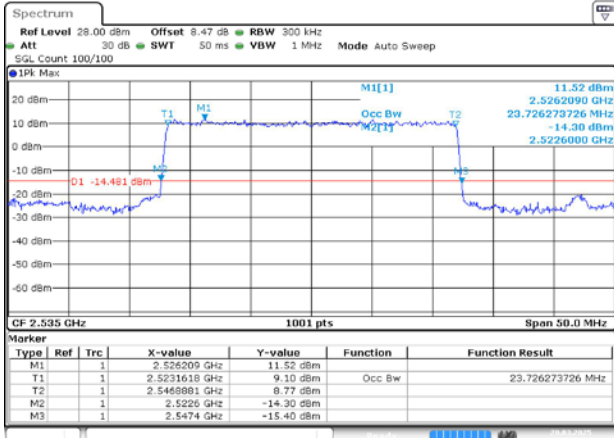
Date: 20.MAR.2025 10:41:30

MCH / DFT-QPSK / Outer_Full



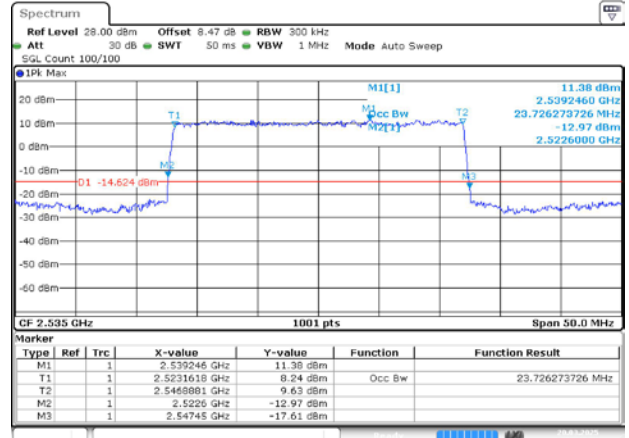
Date: 20.MAR.2025 10:41:45

MCH / CP-QPSK / Outer_Full



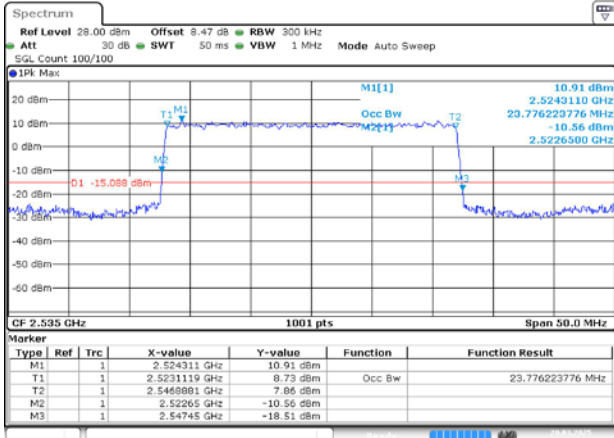
Date: 20.MAR.2025 10:42:21

MCH / CP-16QAM / Outer_Full



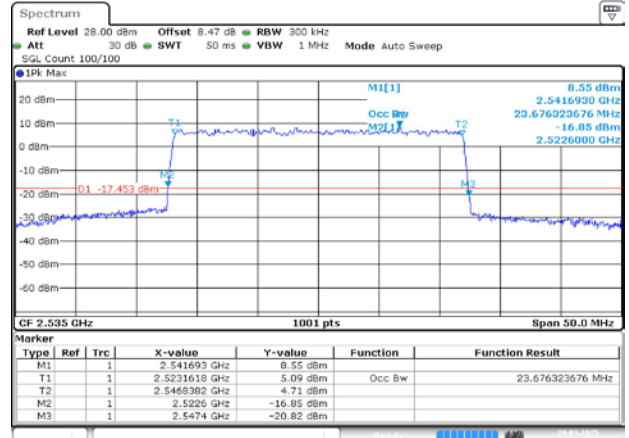
Date: 20.MAR.2025 10:42:29

MCH / CP-64QAM / Outer_Full



Date: 20.MAR.2025 10:42:36

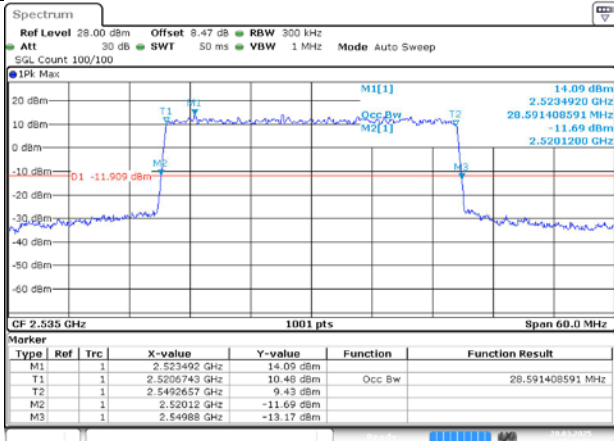
MCH / CP-256QAM / Outer_Full



Date: 20.MAR.2025 10:42:46

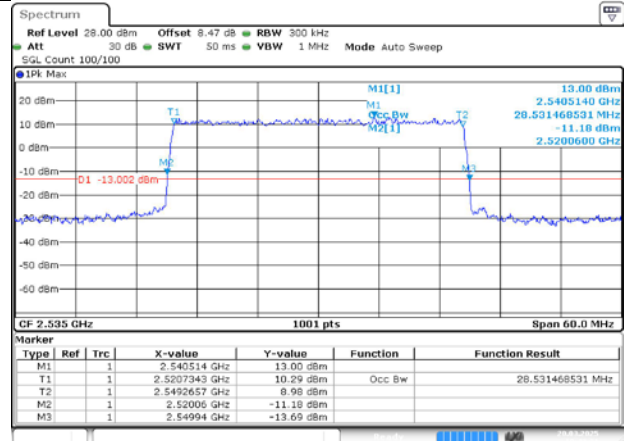
n7A / 15KHz / 30MHz

MCH / DFT-Pi2BPSK / Outer_Full



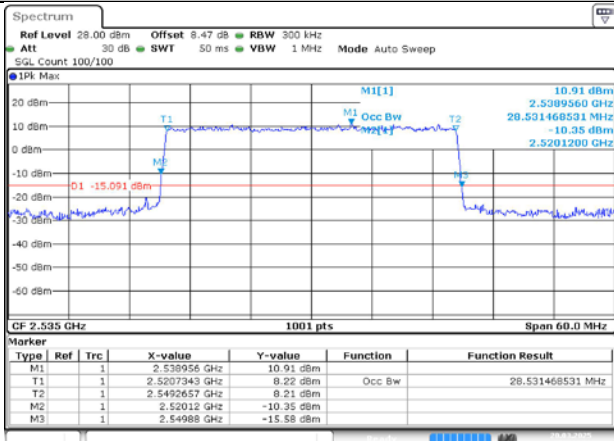
Date: 20.MAR.2025 10:43:06

MCH / DFT-QPSK / Outer_Full



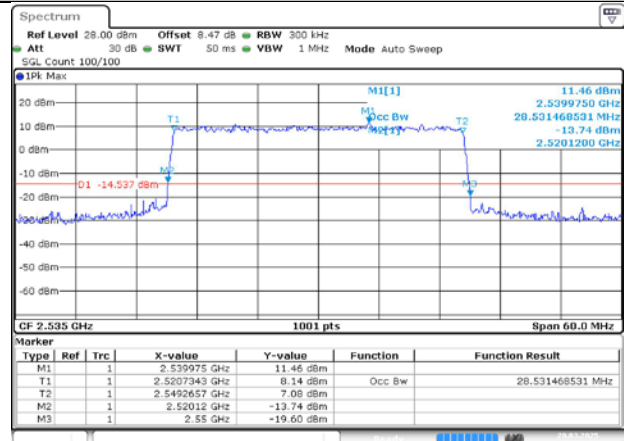
Date: 20.MAR.2025 10:43:22

MCH / CP-QPSK / Outer_Full



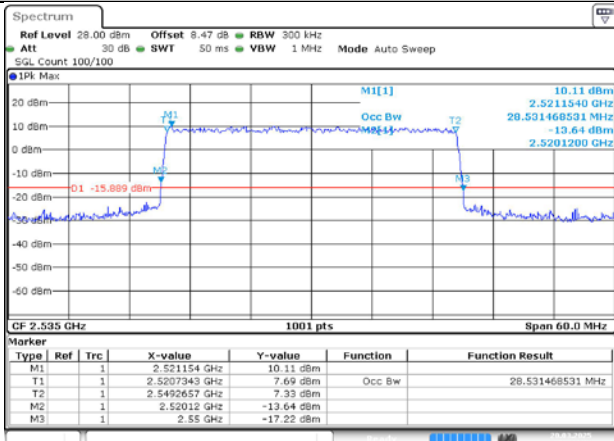
Date: 20.MAR.2025 10:43:57

MCH / CP-16QAM / Outer_Full



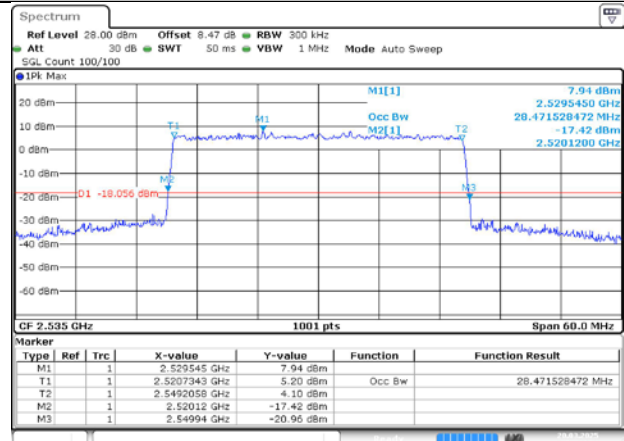
Date: 20.MAR.2025 10:44:04

MCH / CP-64QAM / Outer_Full



Date: 20.MAR.2025 10:44:12

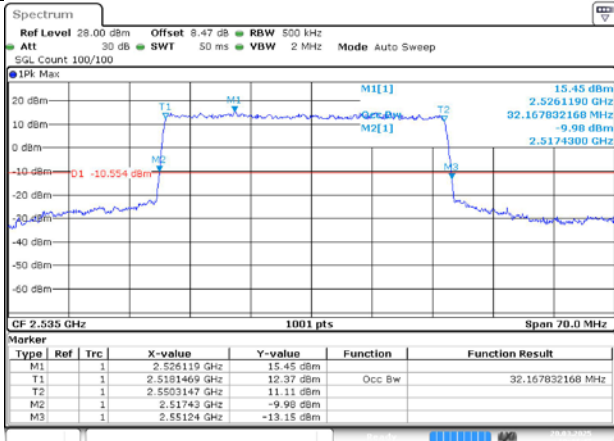
MCH / CP-256QAM / Outer_Full



Date: 20.MAR.2025 10:44:21

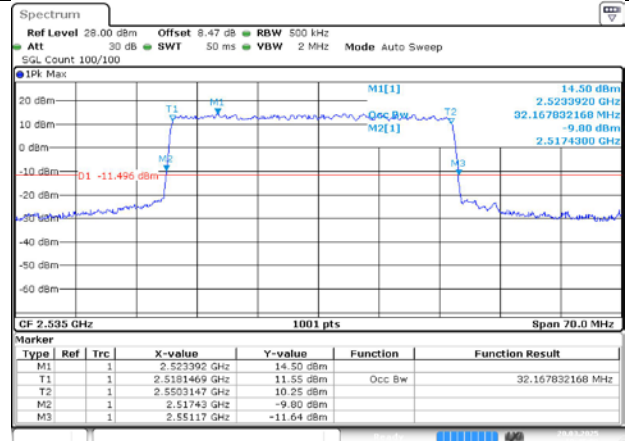
n7A / 15KHz / 35MHz

MCH / DFT-Pi2BPSK / Outer_Full



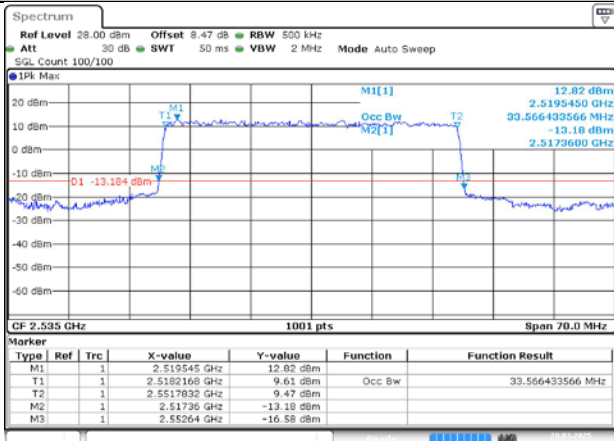
Date: 20.MAR.2025 10:44:47

MCH / DFT-QPSK / Outer_Full



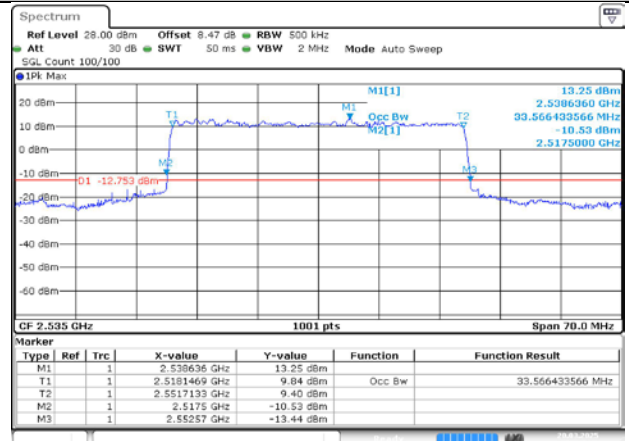
Date: 20.MAR.2025 10:45:03

MCH / CP-QPSK / Outer_Full



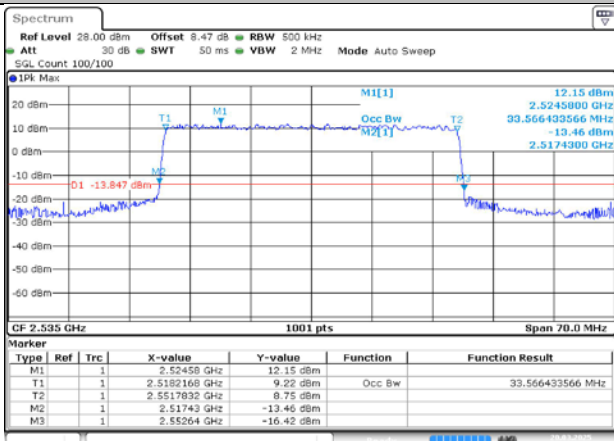
Date: 20.MAR.2025 10:45:38

MCH / CP-16QAM / Outer_Full



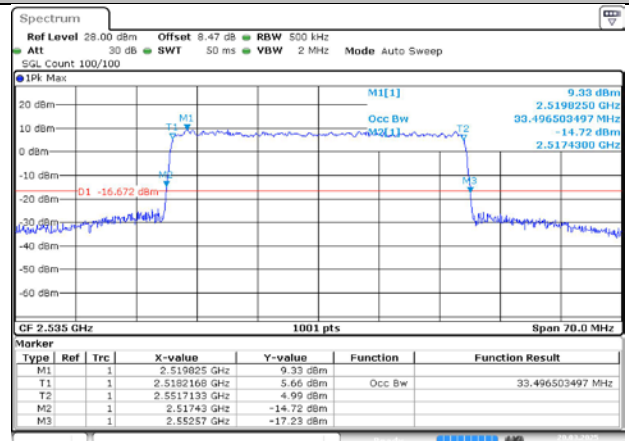
Date: 20.MAR.2025 10:45:46

MCH / CP-64QAM / Outer_Full



Date: 20.MAR.2025 10:45:53

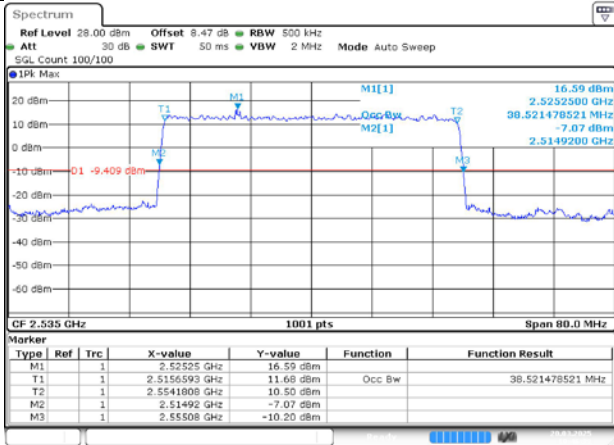
MCH / CP-256QAM / Outer_Full



Date: 20.MAR.2025 10:46:03

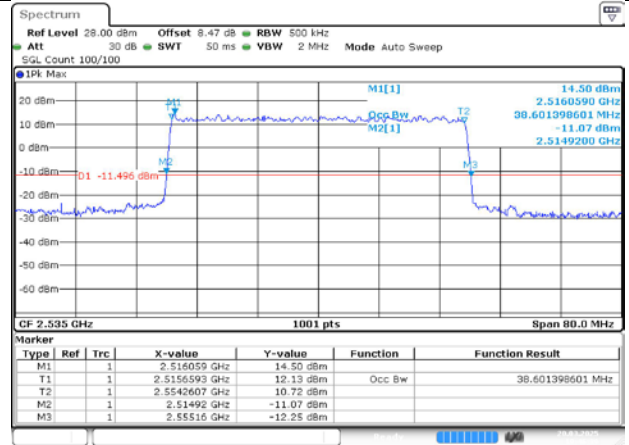
n7A / 15KHz / 40MHz

MCH / DFT-Pi2BPSK / Outer_Full



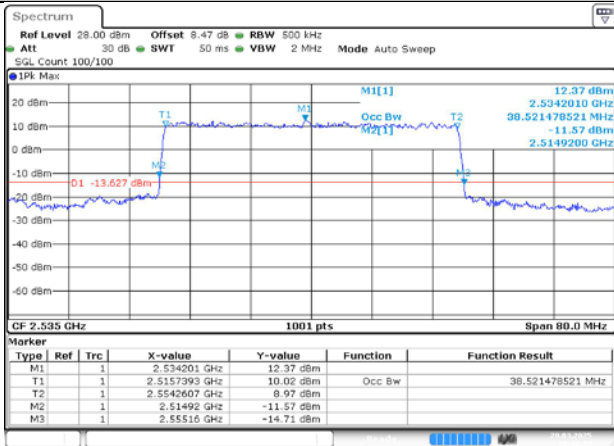
Date: 20.MAR.2025 10:46:23

MCH / DFT-QPSK / Outer_Full



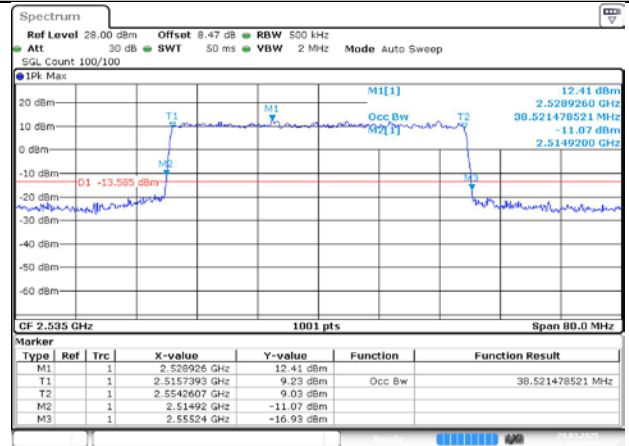
Date: 20.MAR.2025 10:46:39

MCH / CP-QPSK / Outer_Full



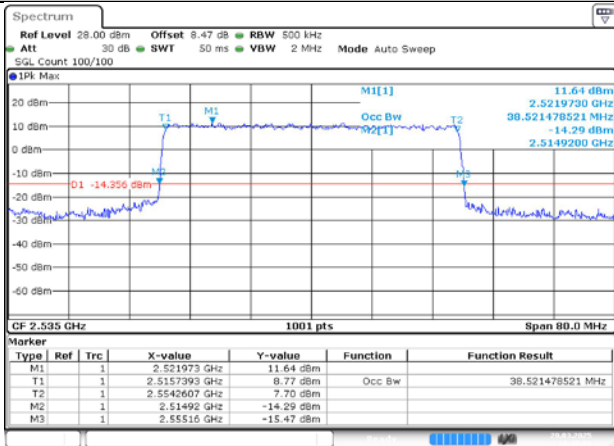
Date: 20.MAR.2025 10:47:14

MCH / CP-16QAM / Outer_Full



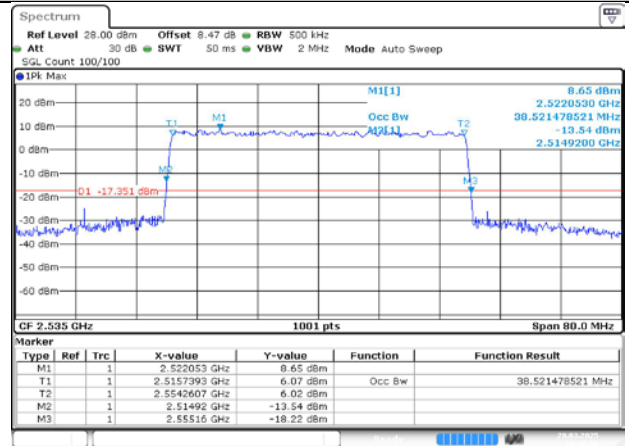
Date: 20.MAR.2025 10:47:22

MCH / CP-64QAM / Outer_Full



Date: 20.MAR.2025 10:47:29

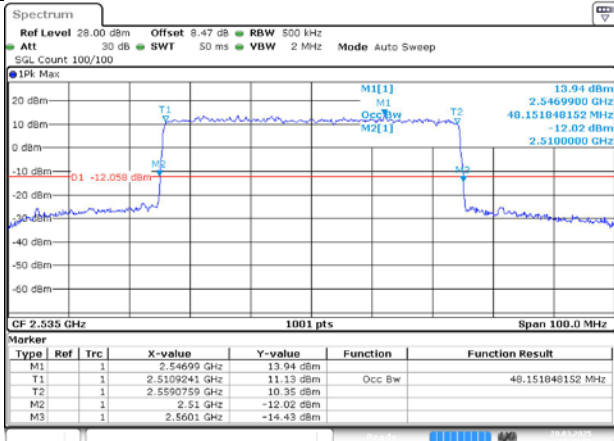
MCH / CP-256QAM / Outer_Full



Date: 20.MAR.2025 10:47:39

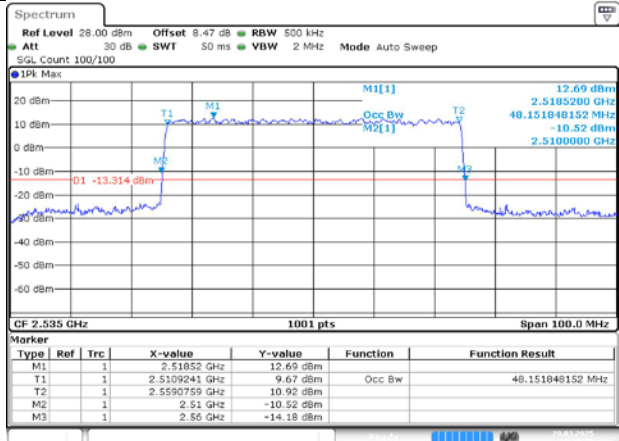
n7A / 15KHz / 50MHz

MCH / DFT-Pi2BPSK / Outer_Full



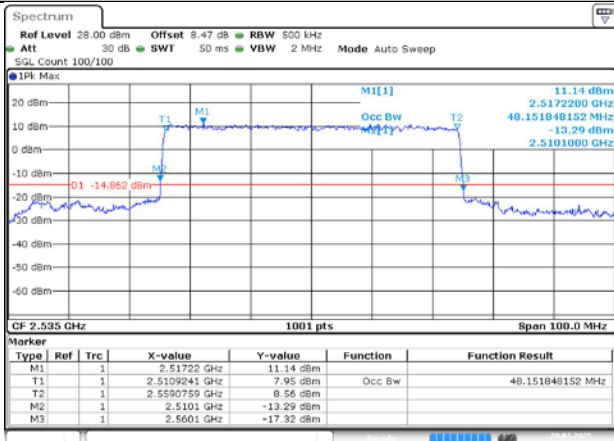
Date: 20.MAR.2025 10:47:59

MCH / DFT-QPSK / Outer_Full



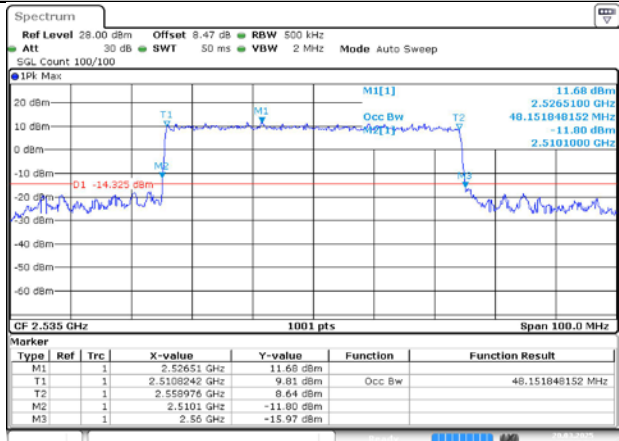
Date: 20.MAR.2025 10:48:21

MCH / CP-QPSK / Outer_Full



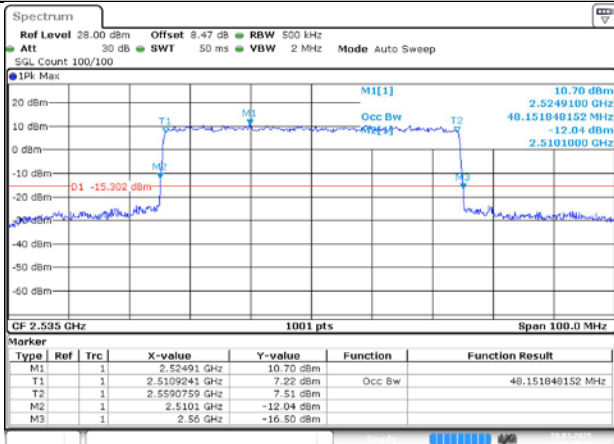
Date: 20.MAR.2025 10:48:56

MCH / CP-16QAM / Outer_Full



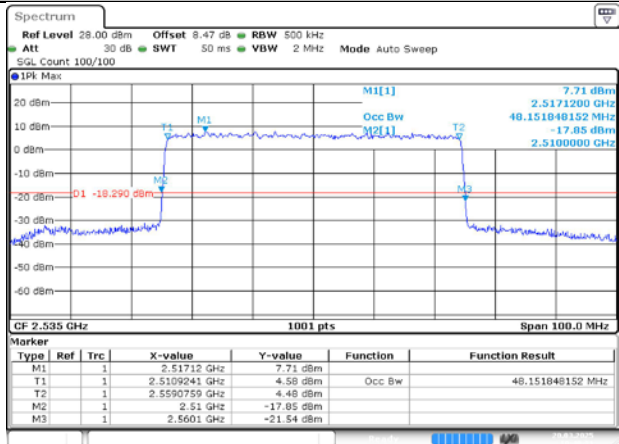
Date: 20.MAR.2025 10:49:04

MCH / CP-64QAM / Outer_Full



Date: 20.MAR.2025 10:49:11

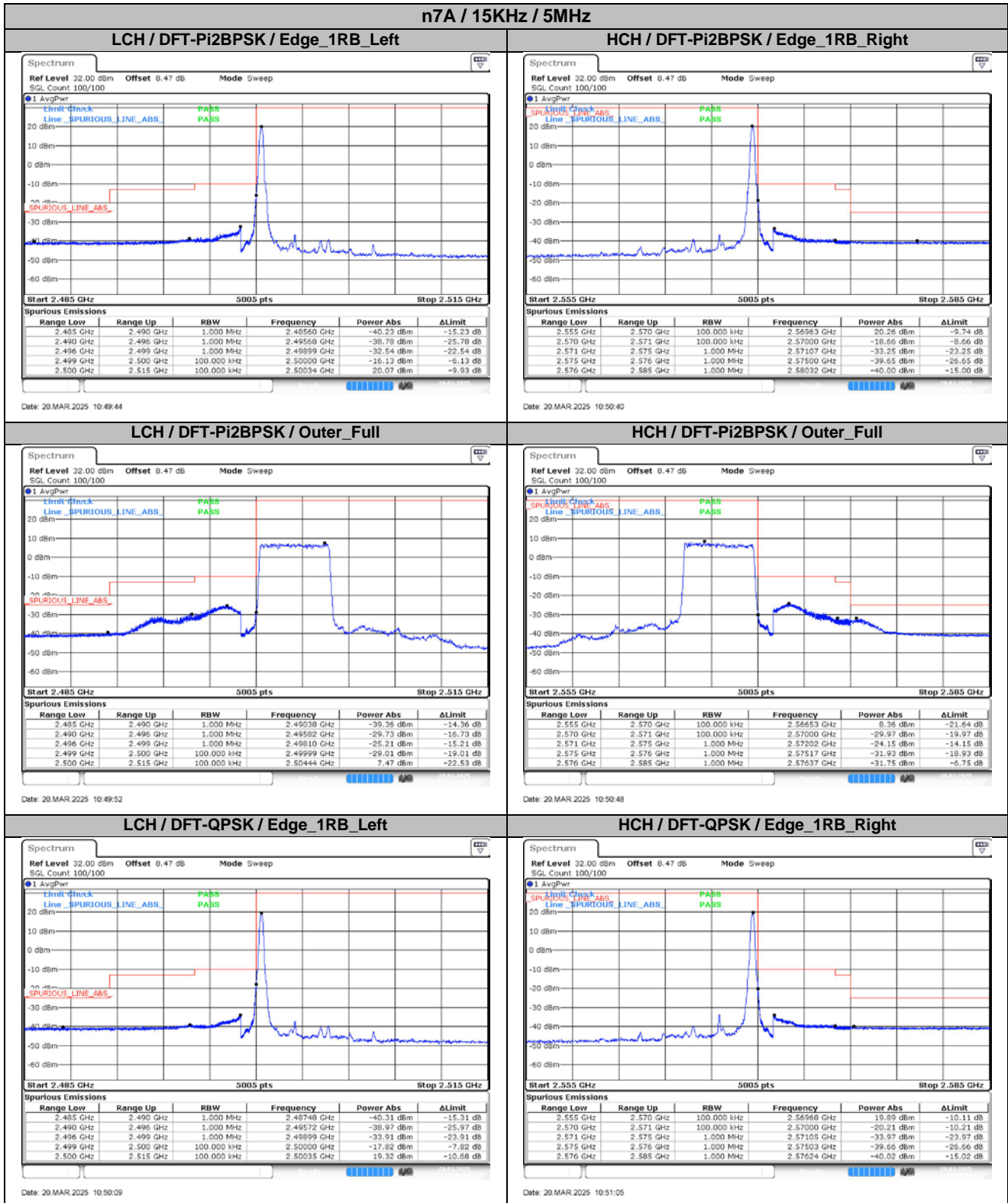
MCH / CP-256QAM / Outer_Full

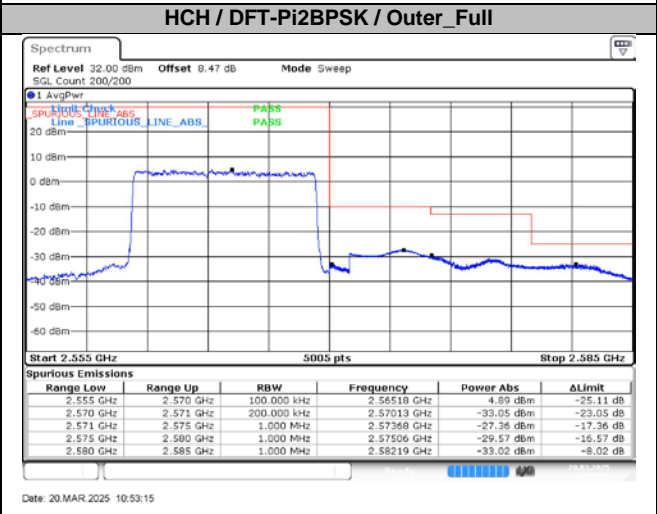
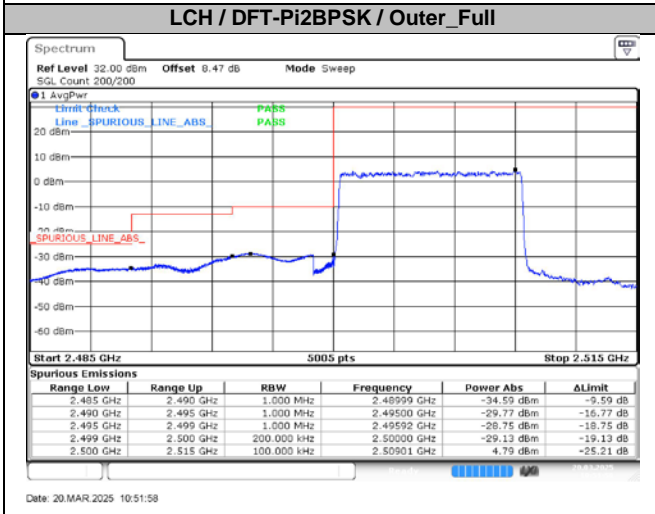
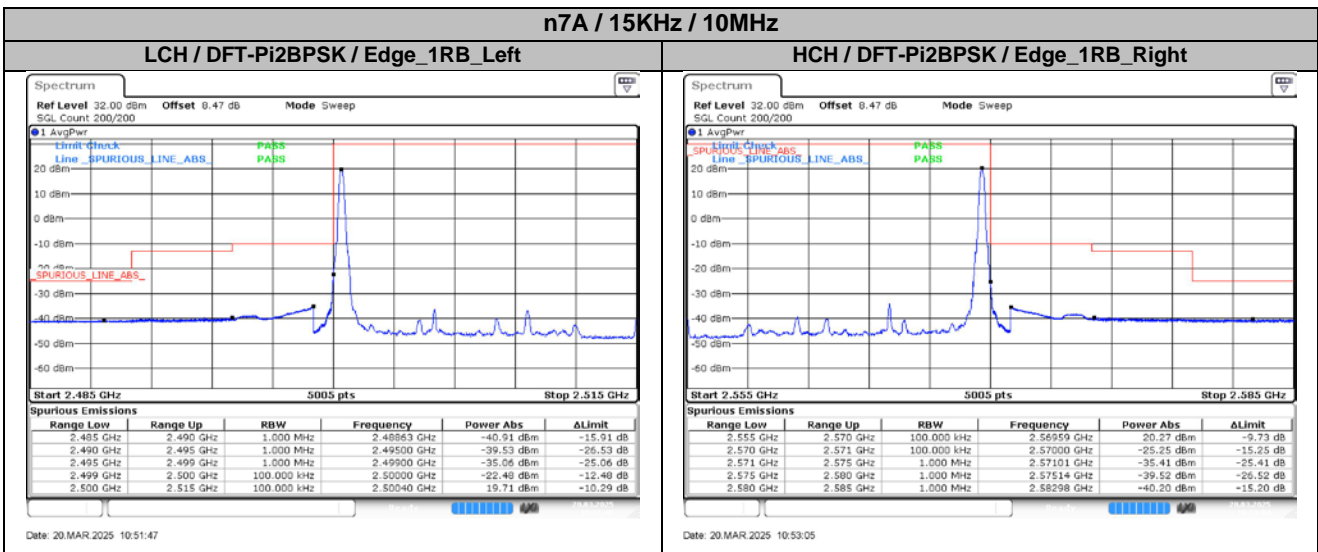
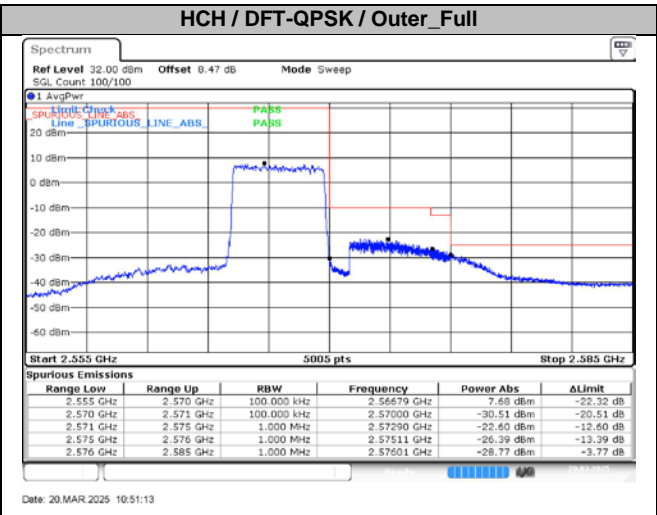
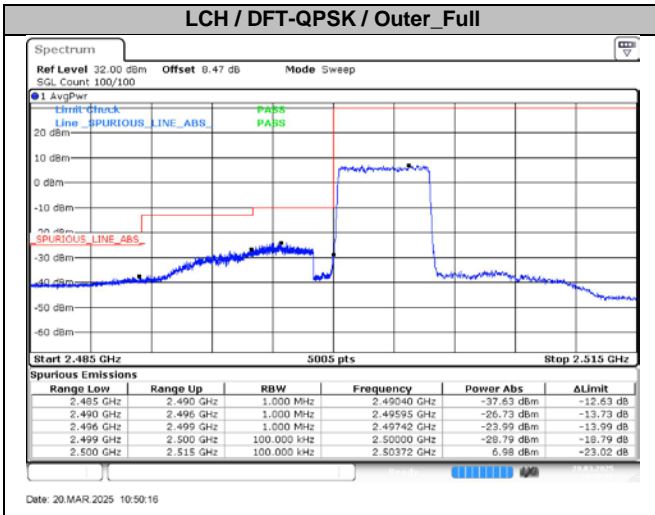


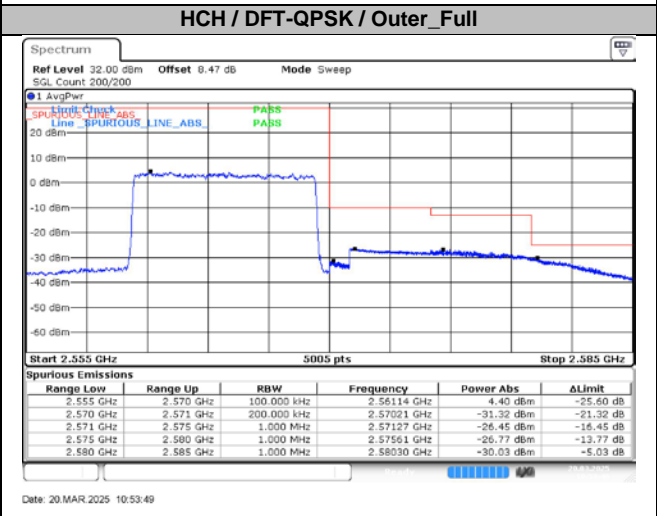
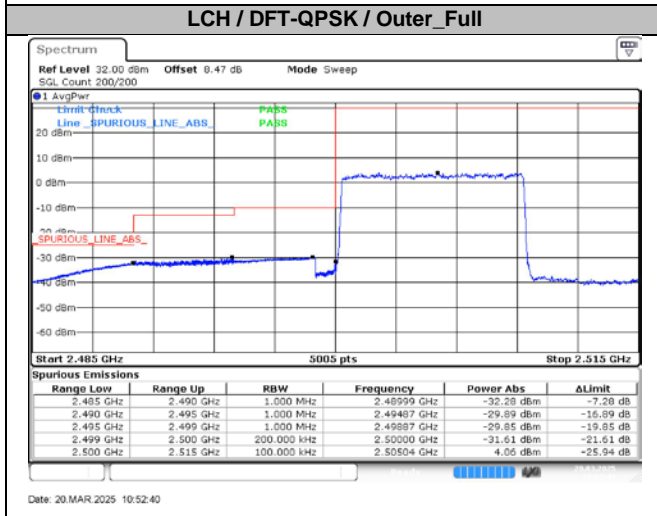
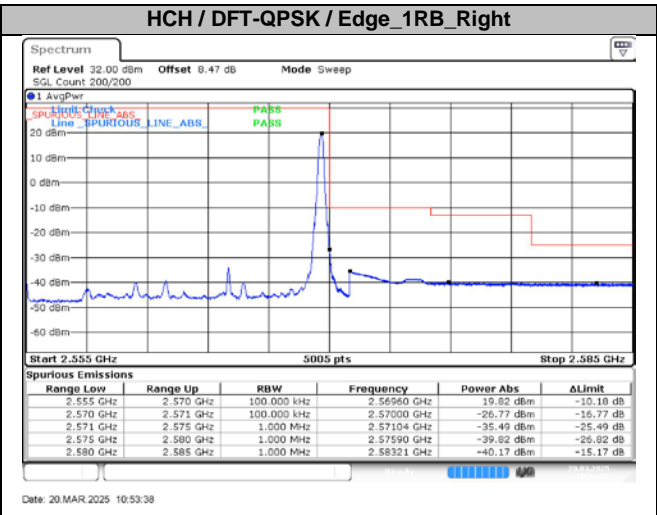
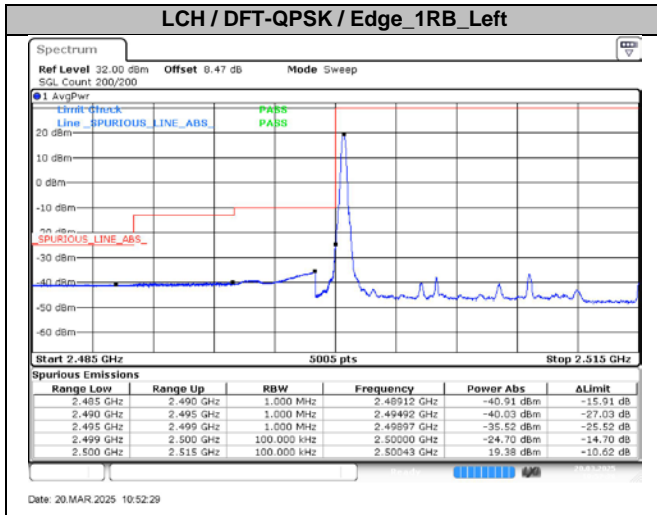
Date: 20.MAR.2025 10:49:21

5. Conducted Band Edges

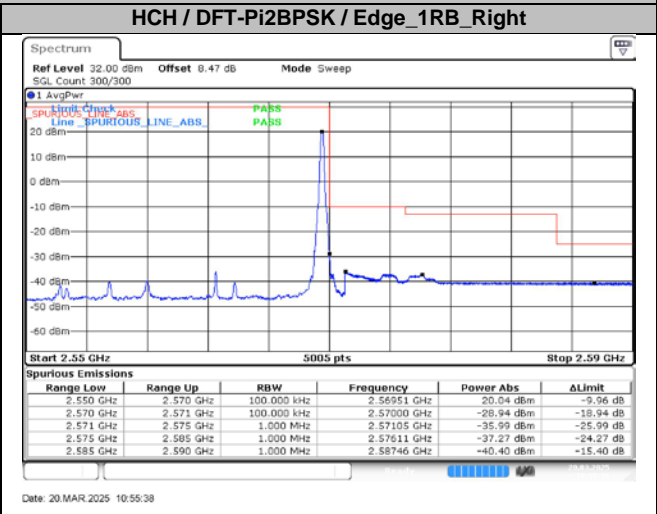
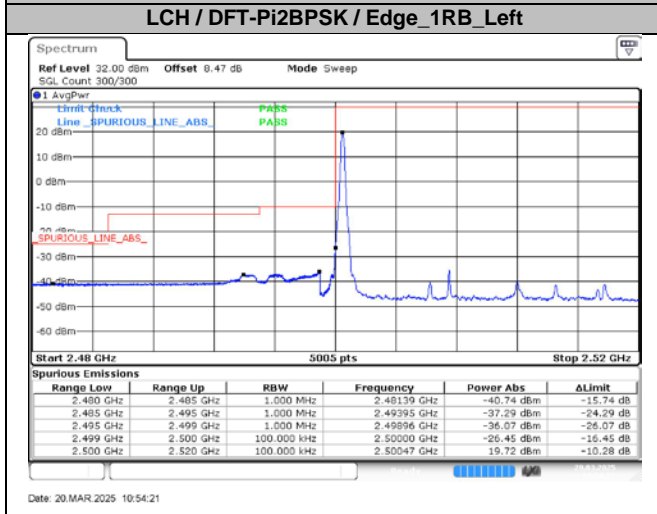
5.1. Test Plots for SCS=15KHz

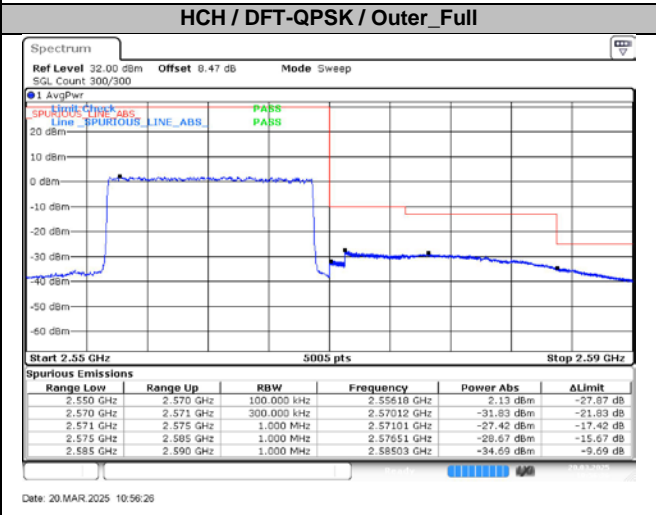
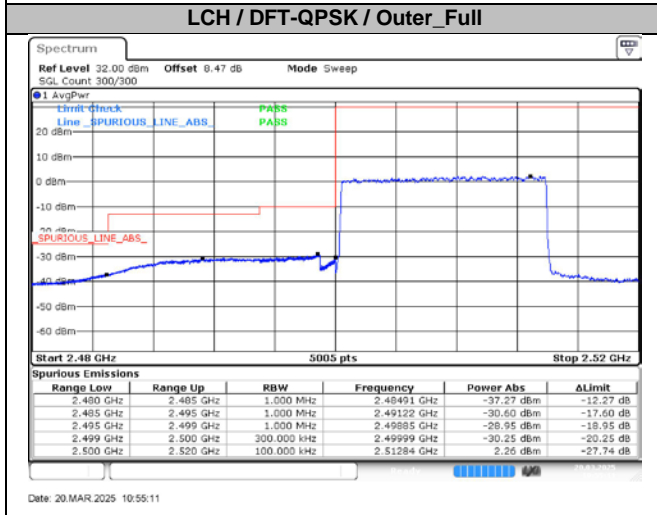
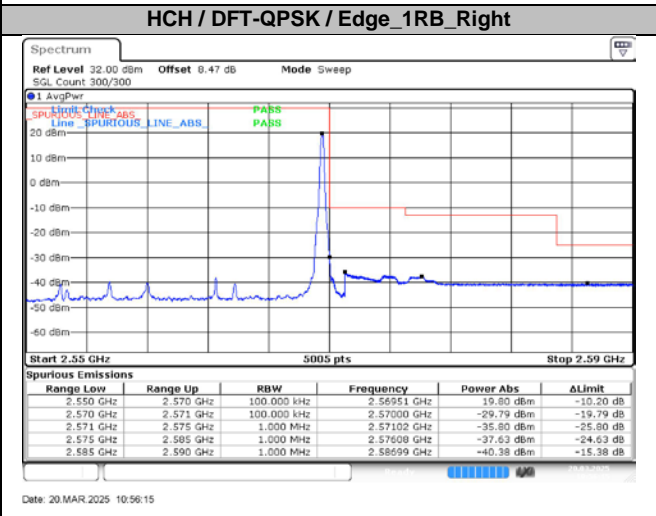
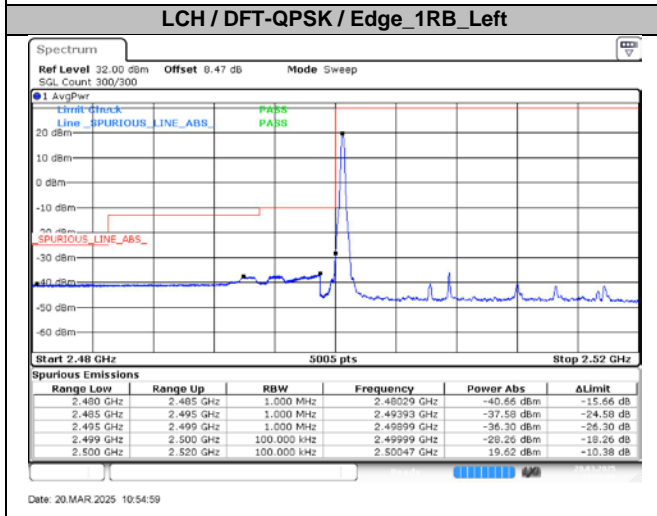
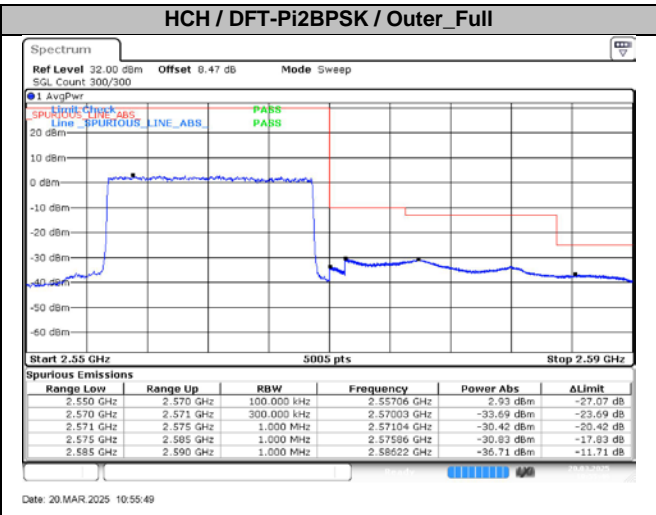
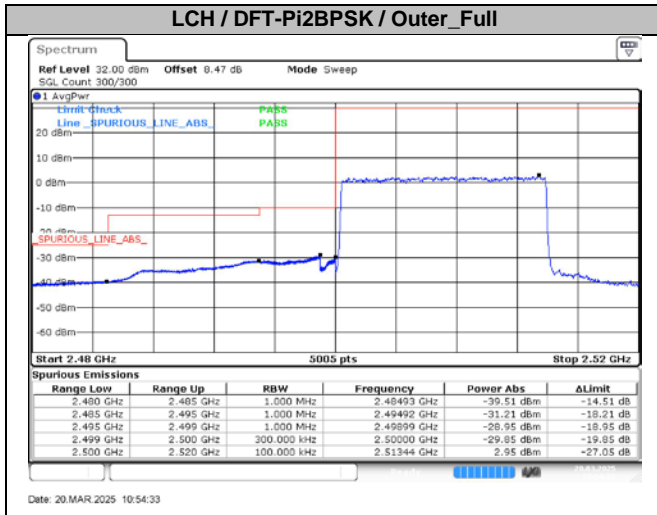






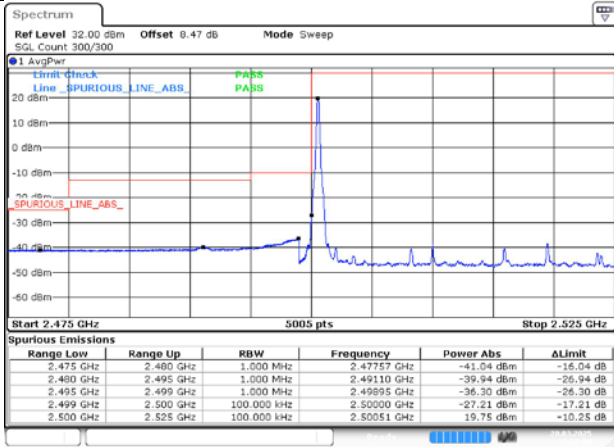
n7A / 15KHz / 15MHz





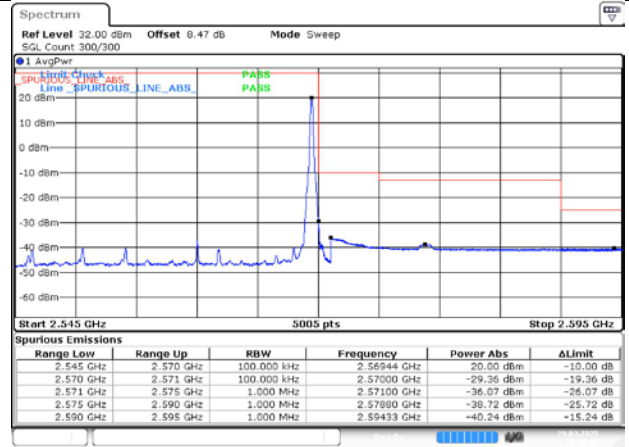
n7A / 15KHz / 20MHz

LCH / DFT-Pi2BPSK / Edge_1RB_Left



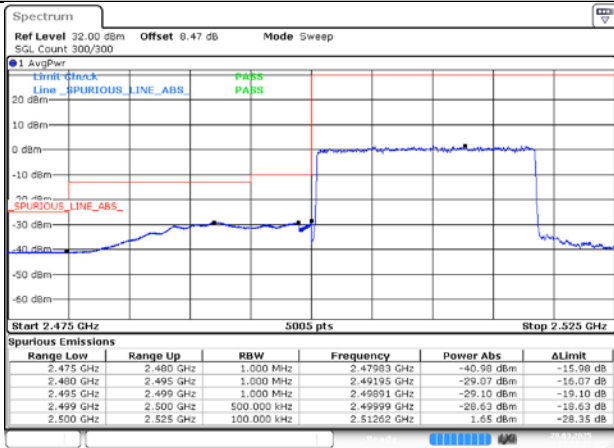
Date: 20.MAR.2025 10:57:04

HCH / DFT-Pi2BPSK / Edge_1RB_Right



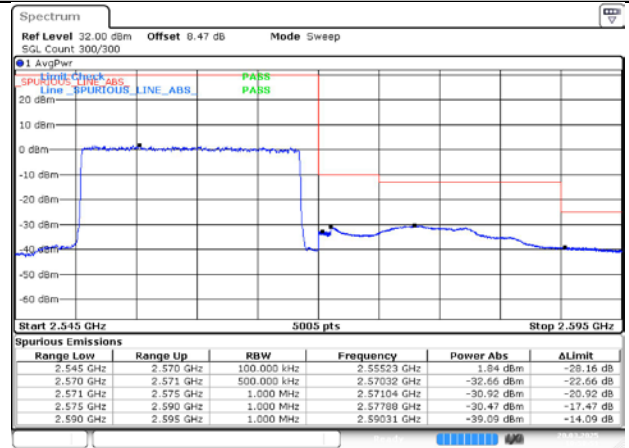
Date: 20.MAR.2025 10:58:36

LCH / DFT-Pi2BPSK / Outer_Full



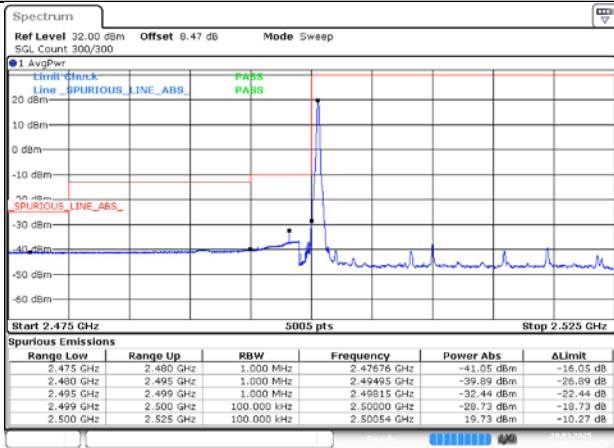
Date: 20.MAR.2025 10:57:20

HCH / DFT-Pi2BPSK / Outer_Full



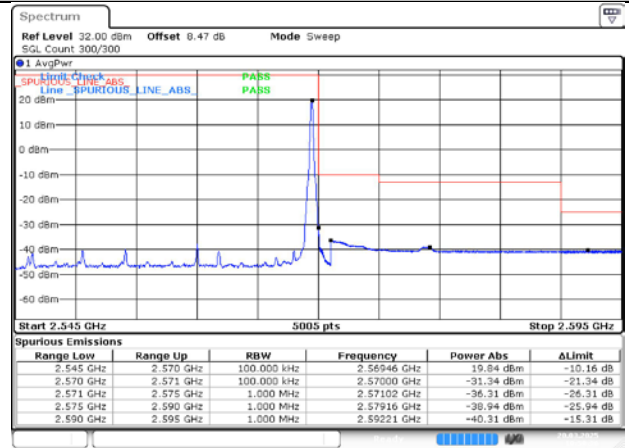
Date: 20.MAR.2025 10:58:51

LCH / DFT-QPSK / Edge_1RB_Left

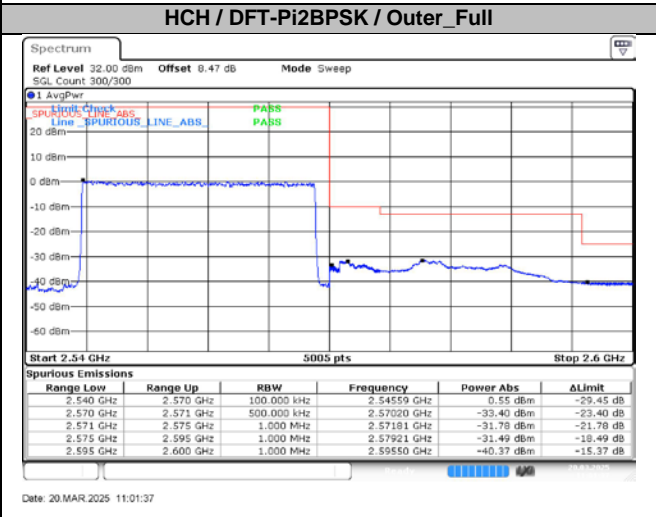
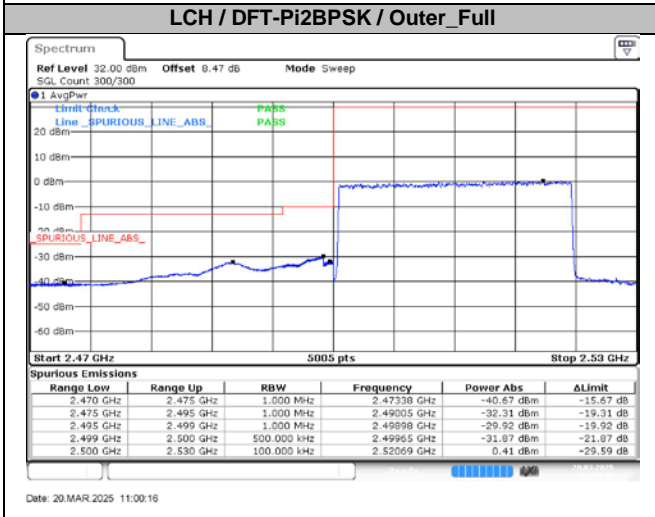
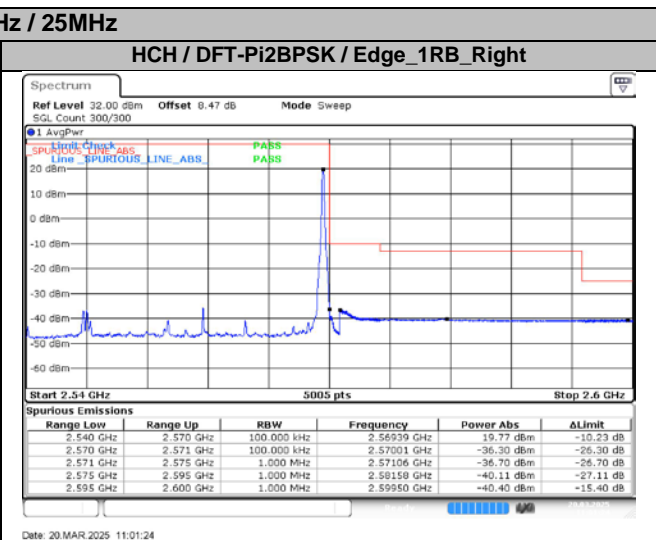
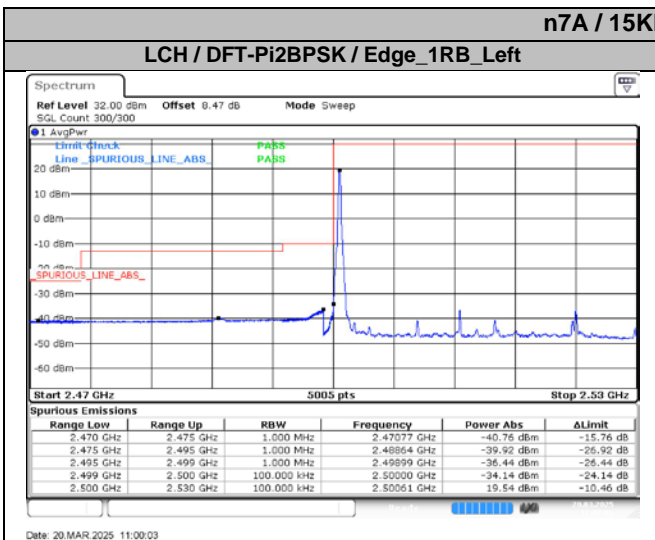
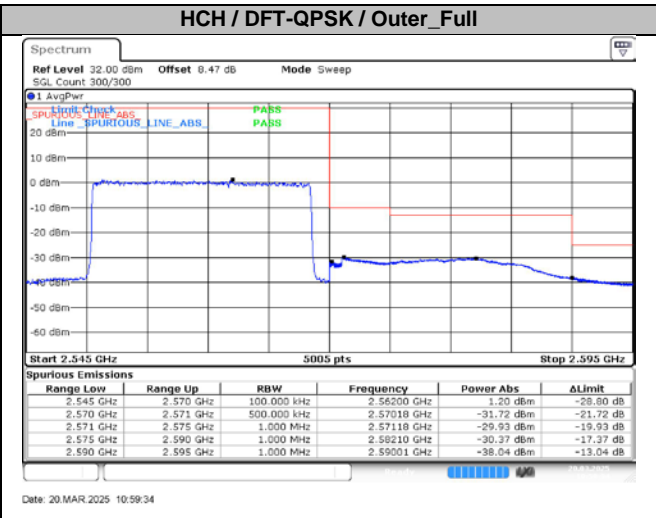
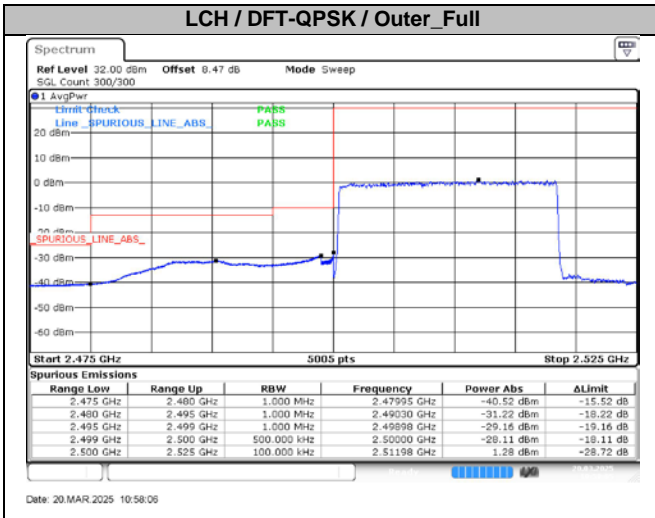


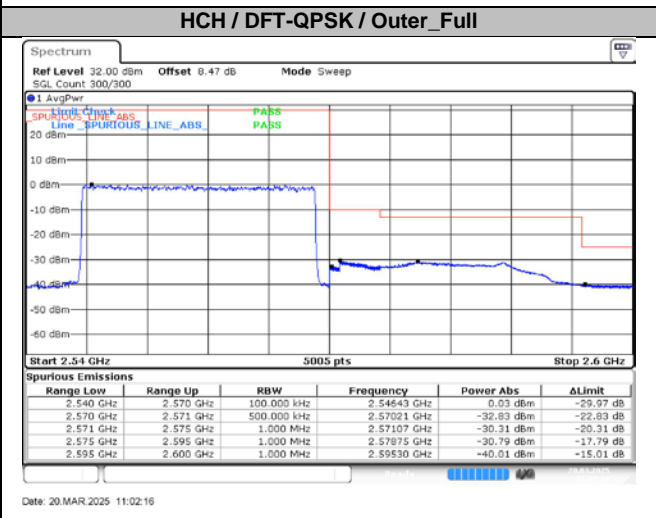
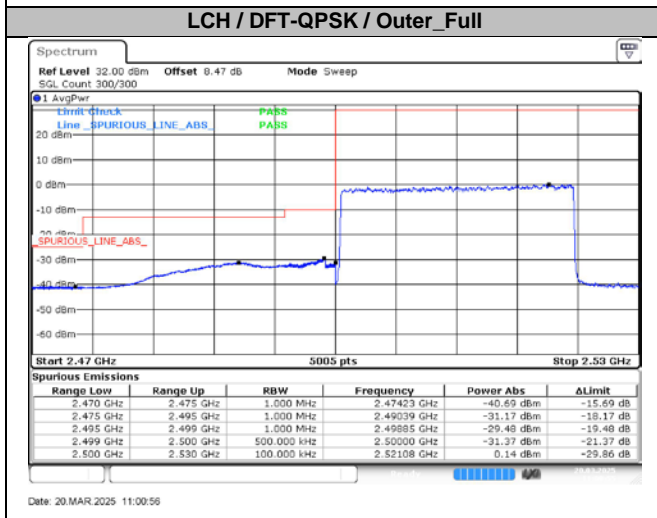
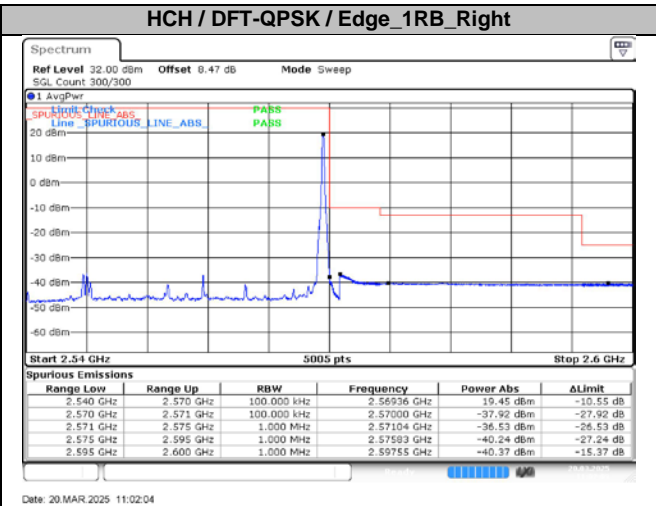
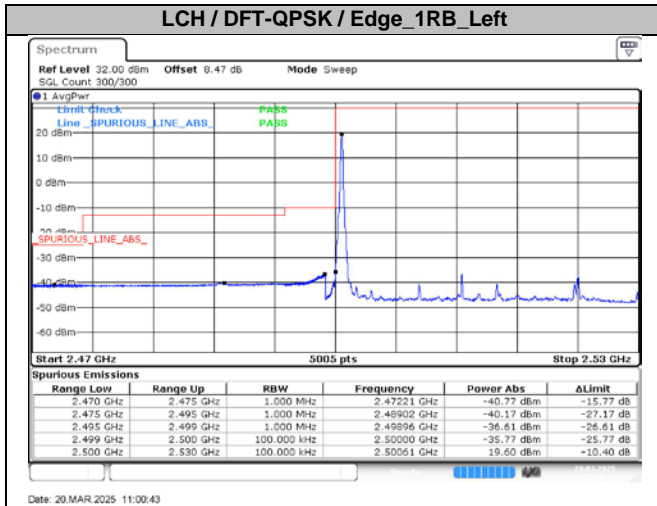
Date: 20.MAR.2025 10:57:50

HCH / DFT-QPSK / Edge_1RB_Right



Date: 20.MAR.2025 10:59:20





n7A / 15KHz / 30MHz

