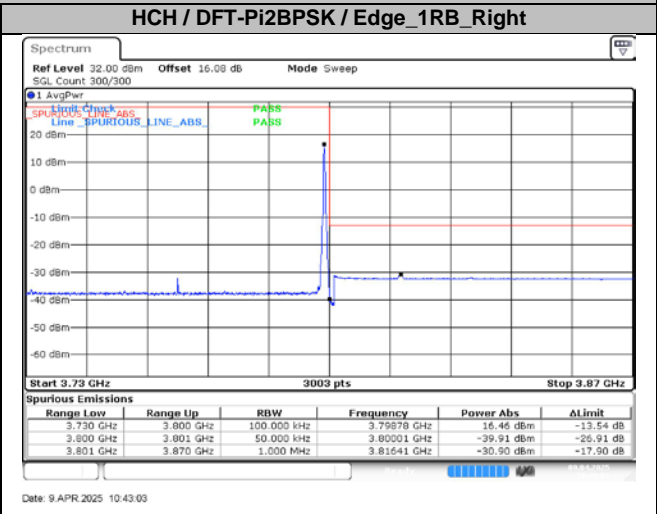
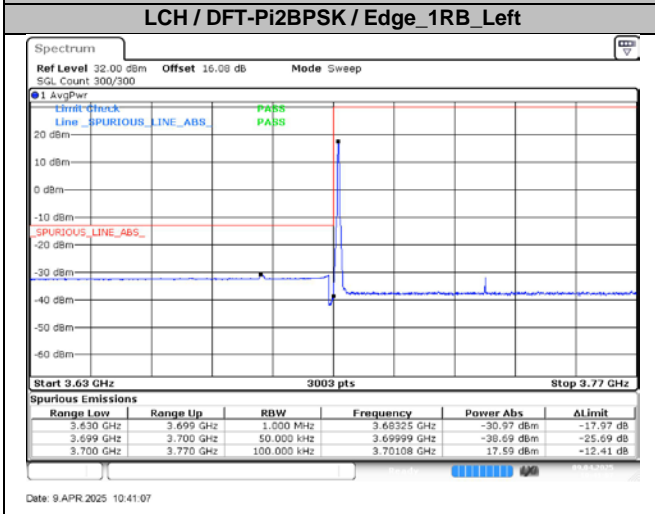
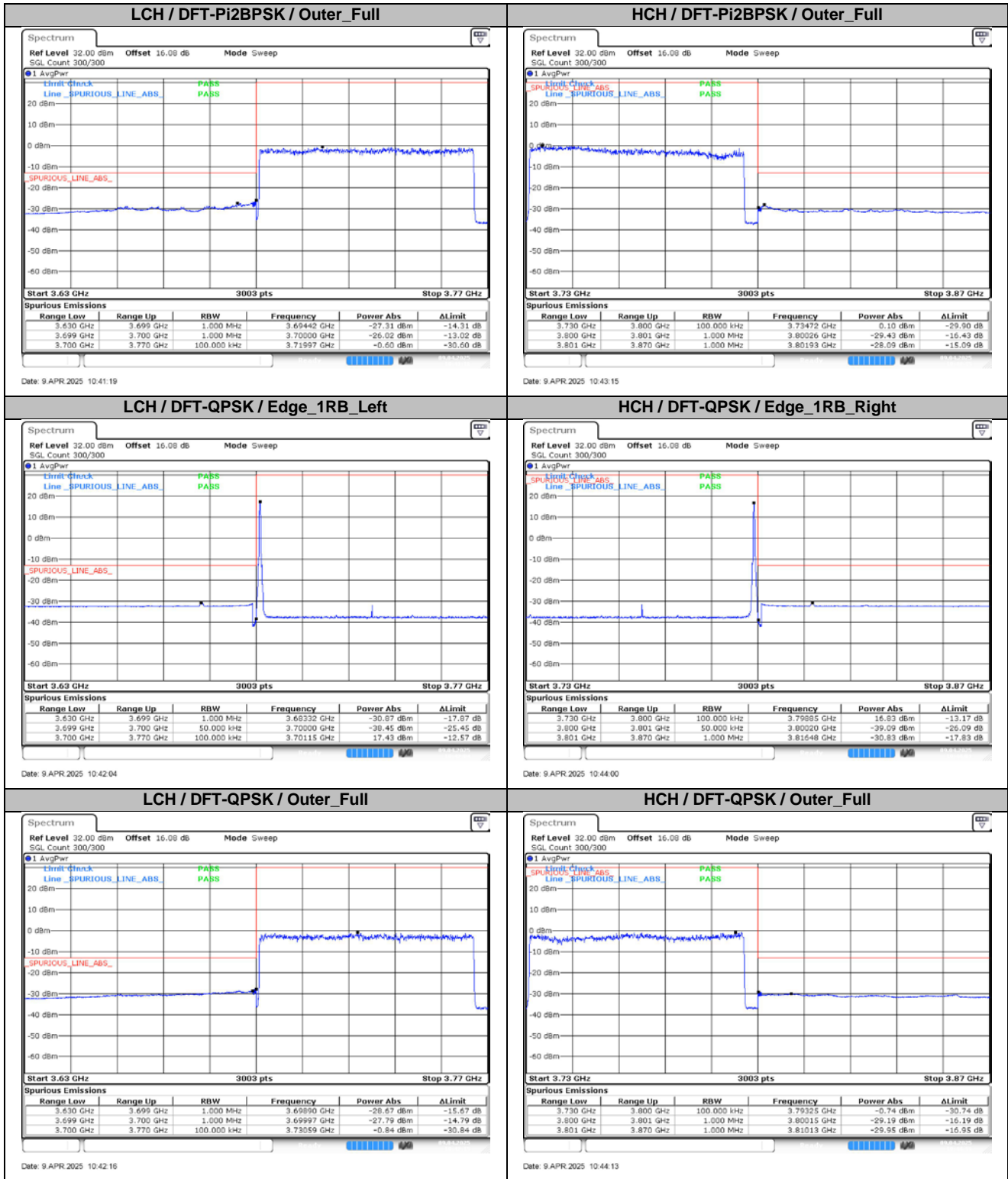


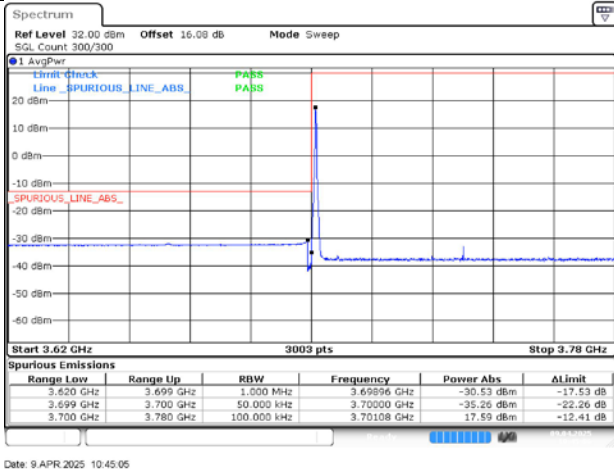
n78A / 30KHz / 70MHz



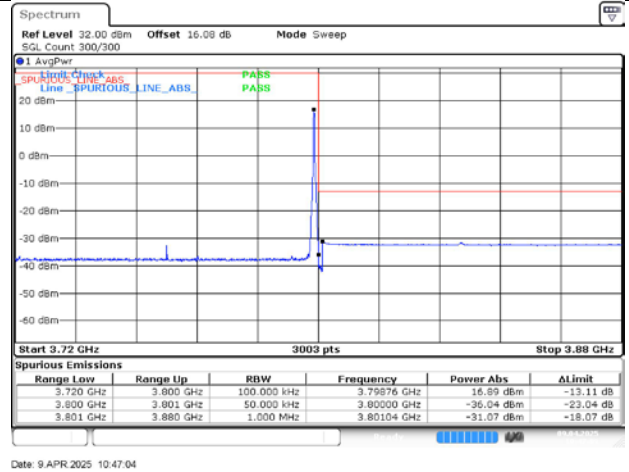


n78A / 30KHz / 80MHz

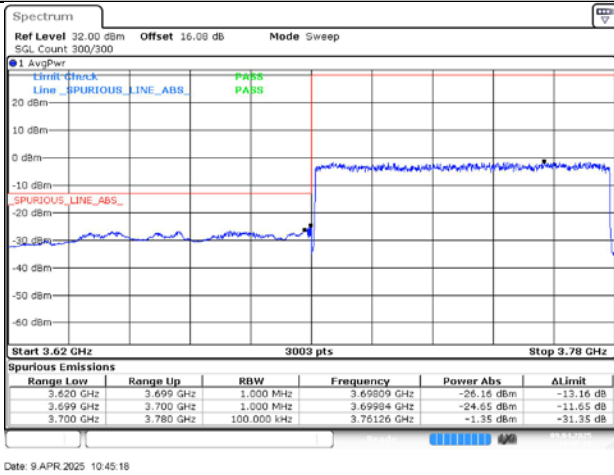
LCH / DFT-Pi2BPSK / Edge_1RB_Left



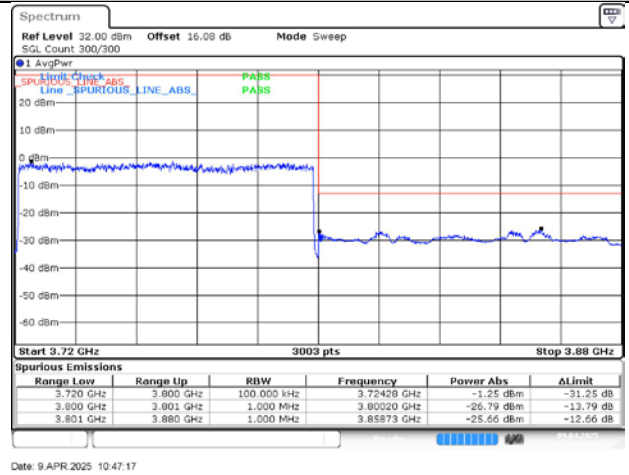
HCH / DFT-Pi2BPSK / Edge_1RB_Right



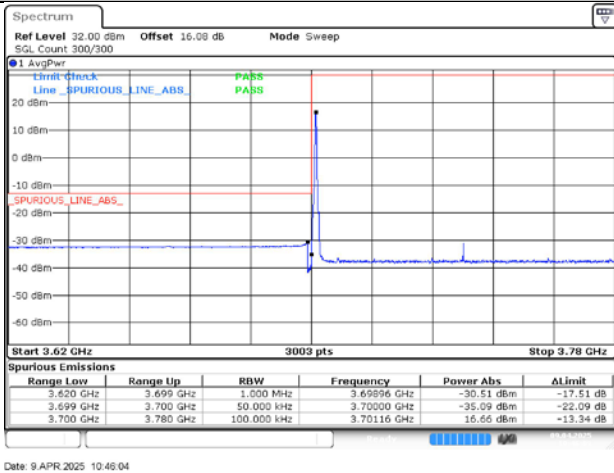
LCH / DFT-Pi2BPSK / Outer_Full



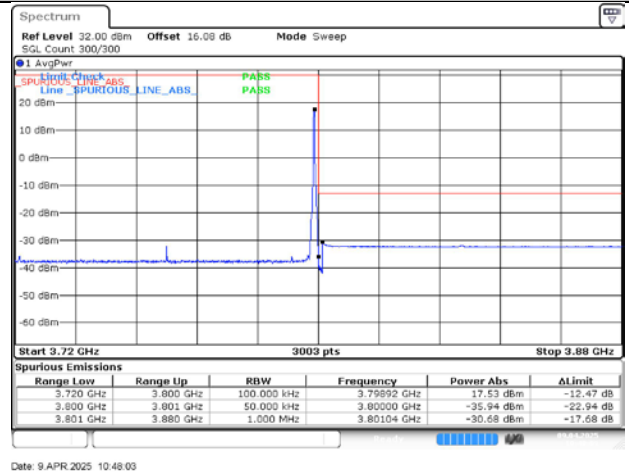
HCH / DFT-Pi2BPSK / Outer_Full

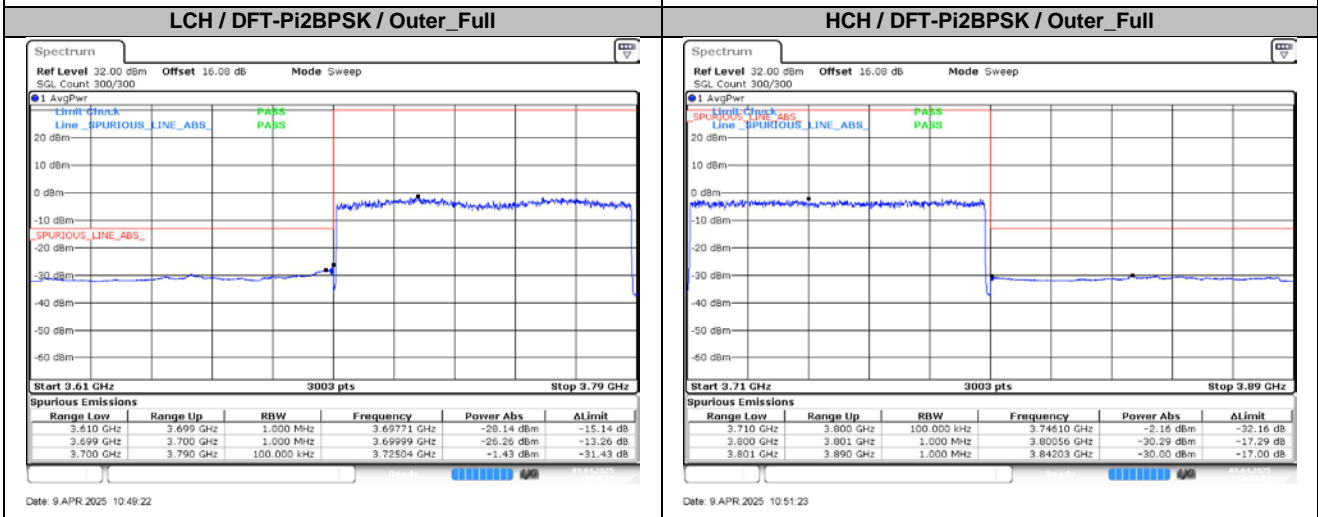
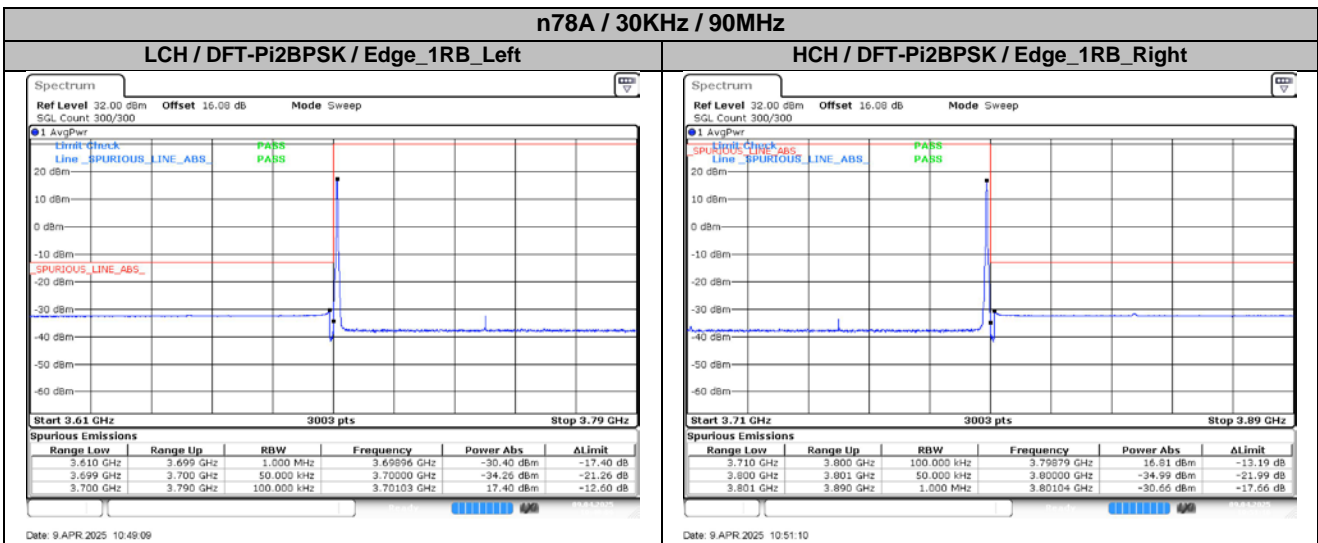
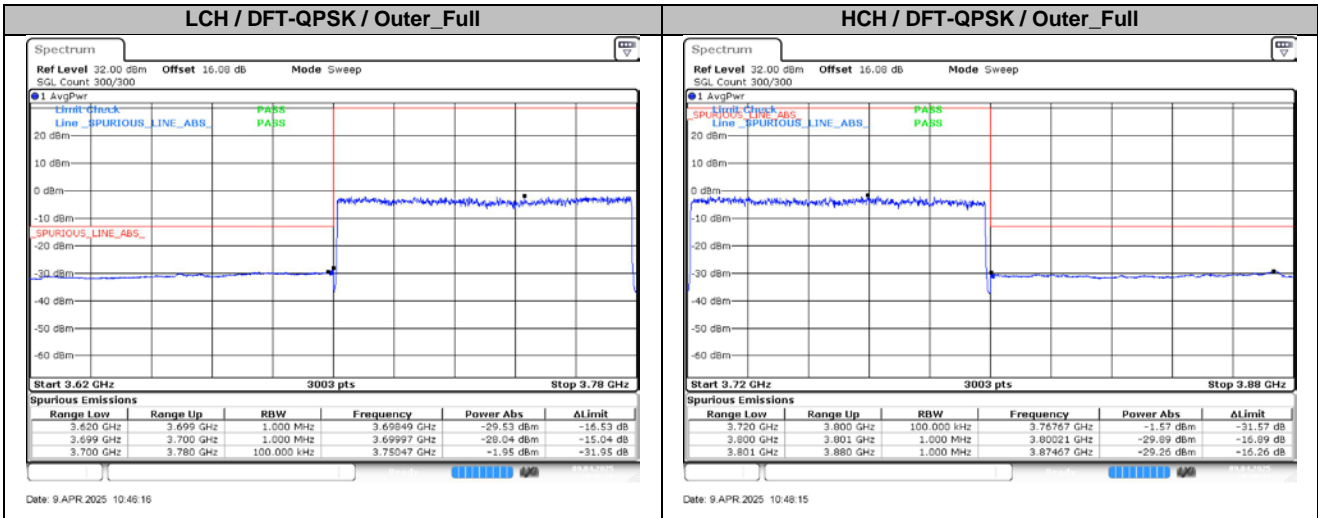


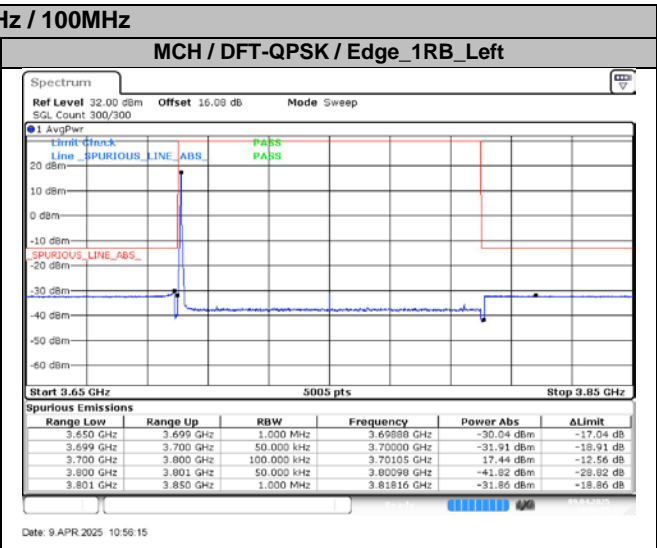
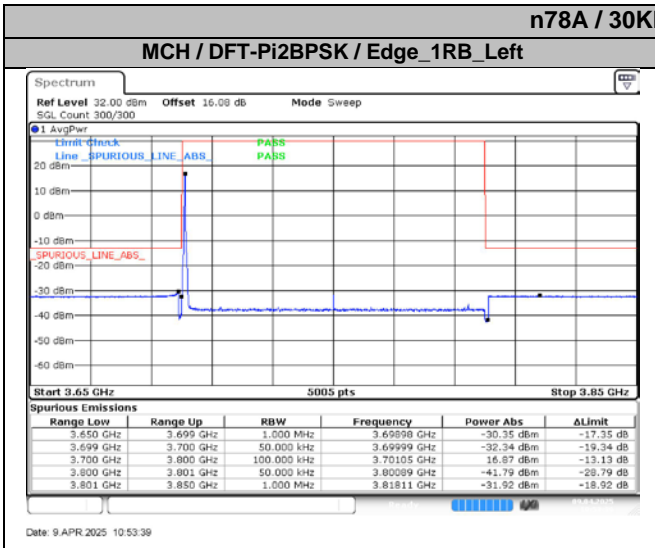
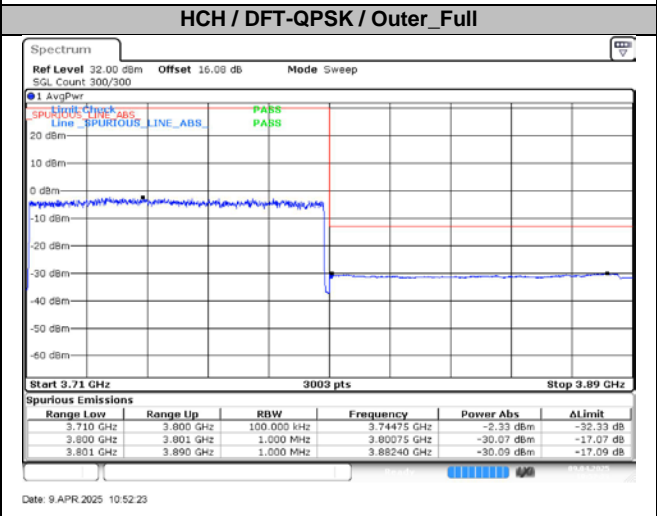
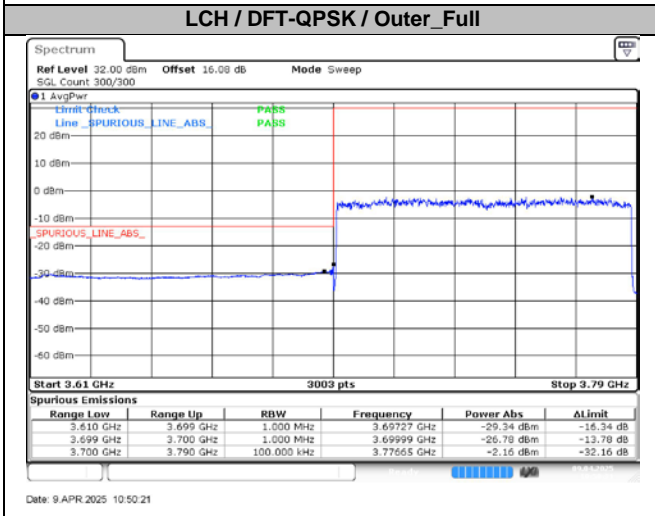
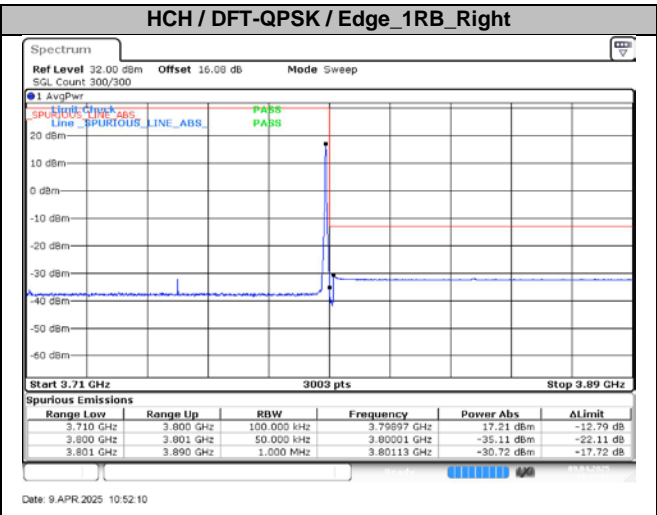
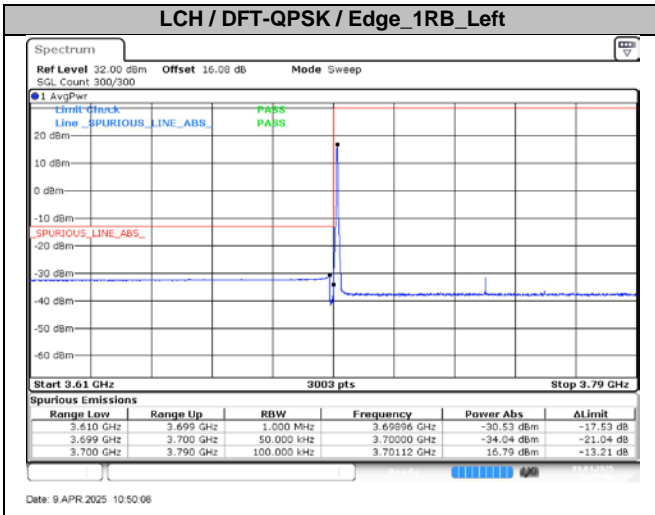
LCH / DFT-QPSK / Edge_1RB_Left

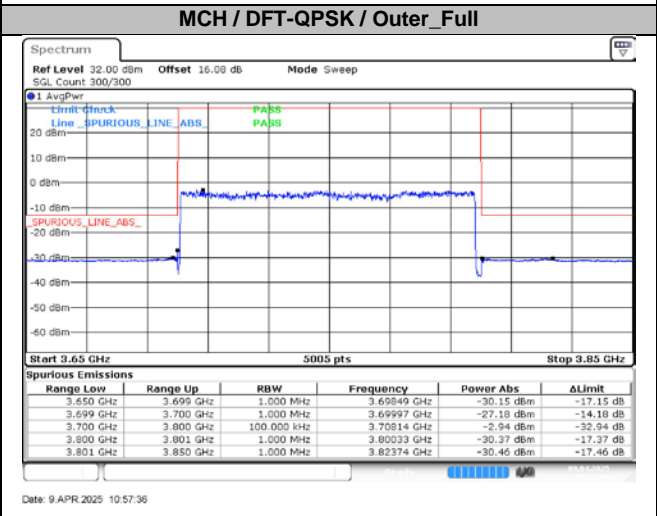
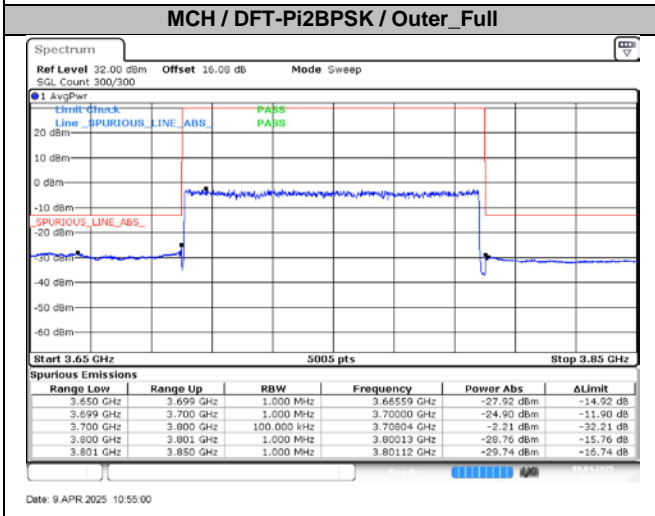
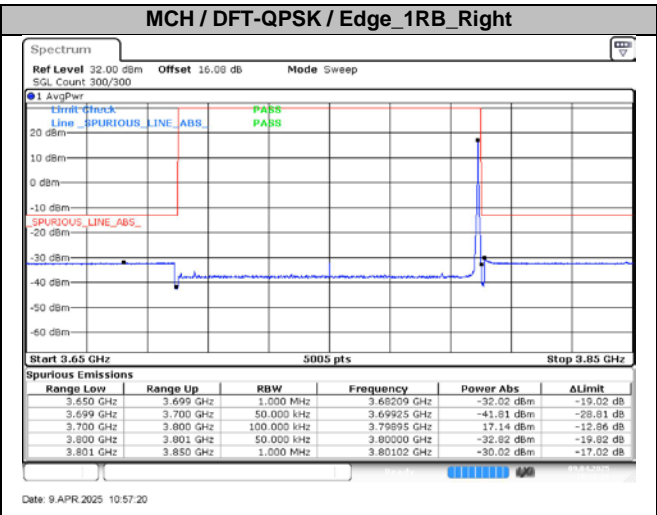
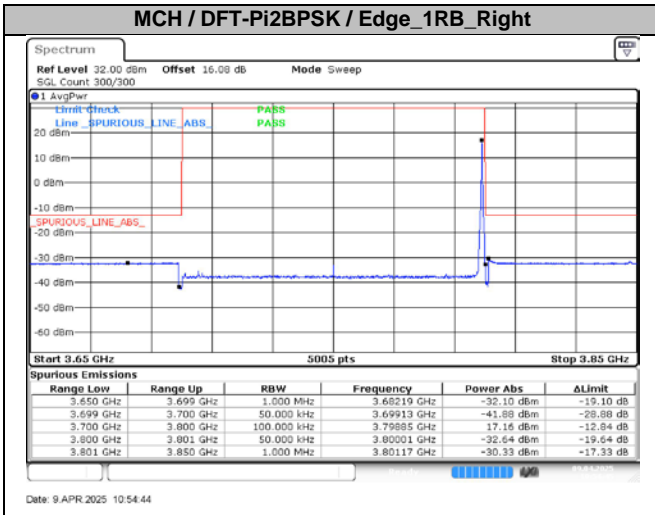


HCH / DFT-QPSK / Edge_1RB_Right



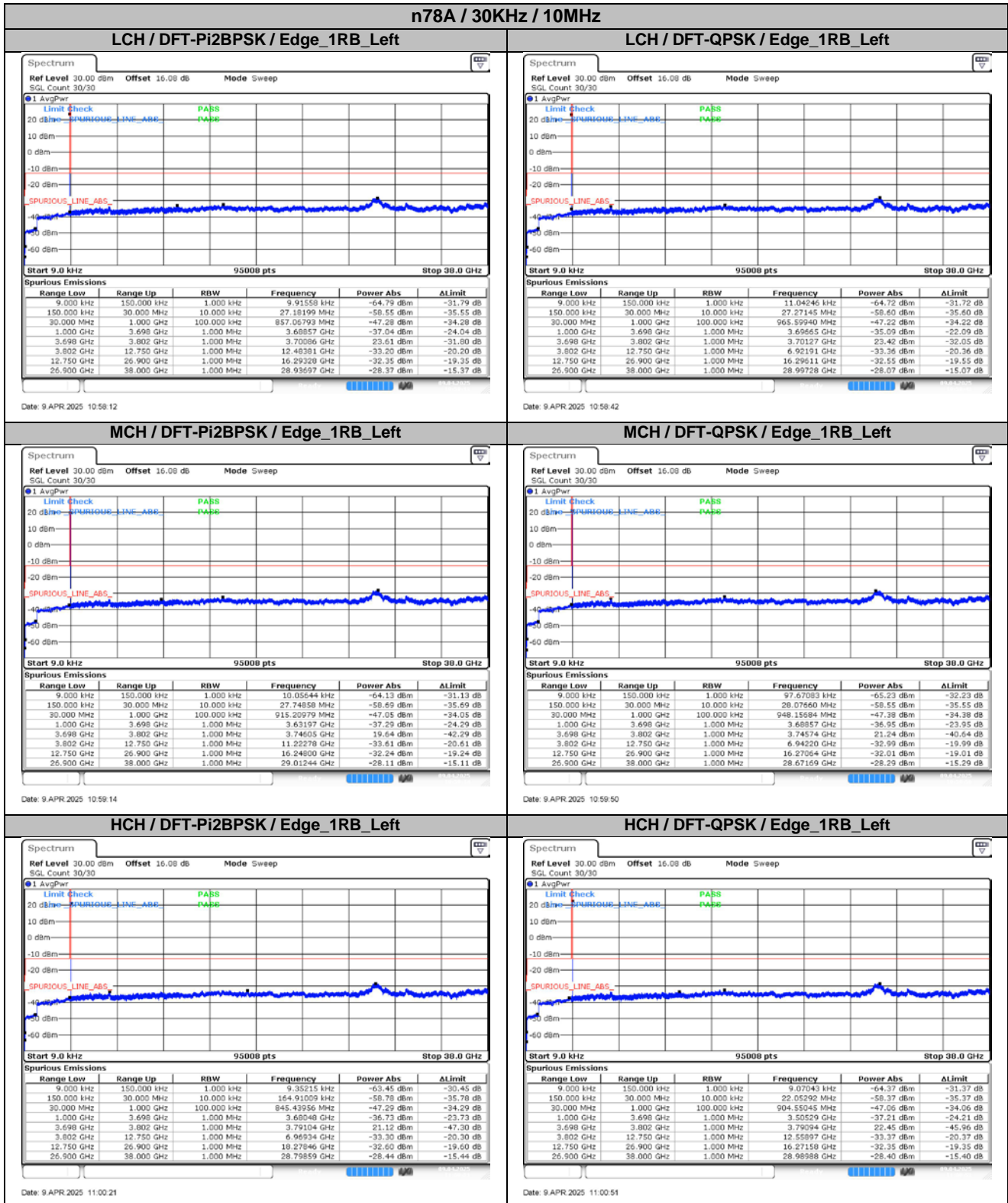


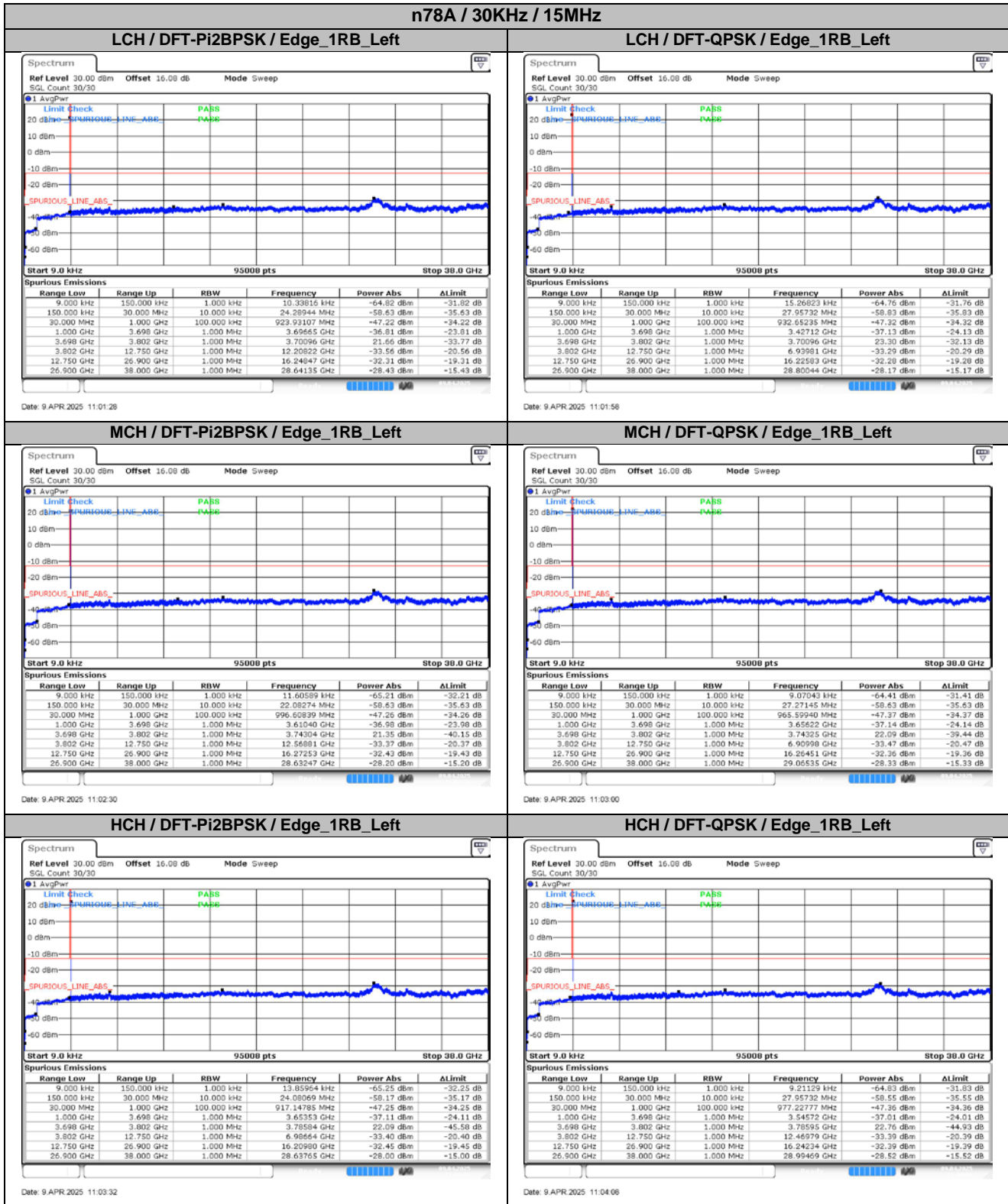




6. Conducted Spurious Emission

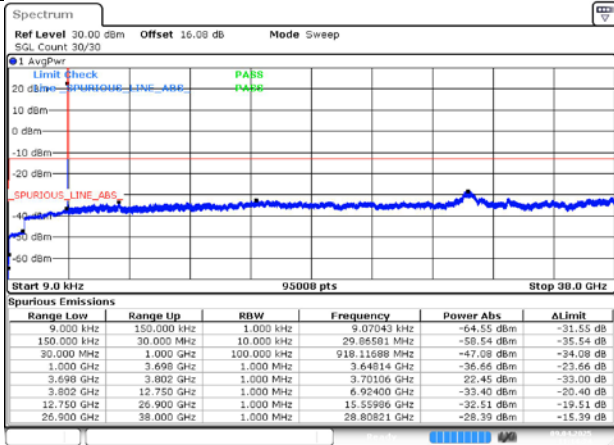
6.1. Test Plots for SCS=30KHz





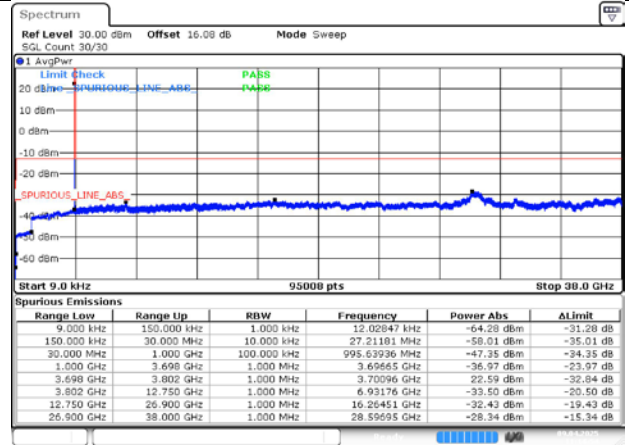
n78A / 30KHz / 20MHz

LCH / DFT-Pi2BPSK / Edge_1RB_Left



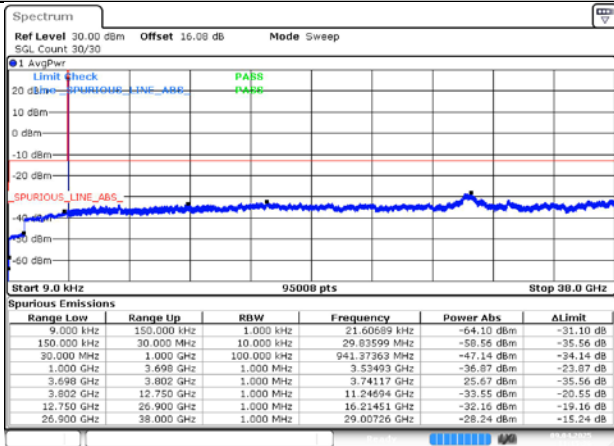
Date: 9 APR 2025 11:14:23

LCH / DFT-QPSK / Edge_1RB_Left



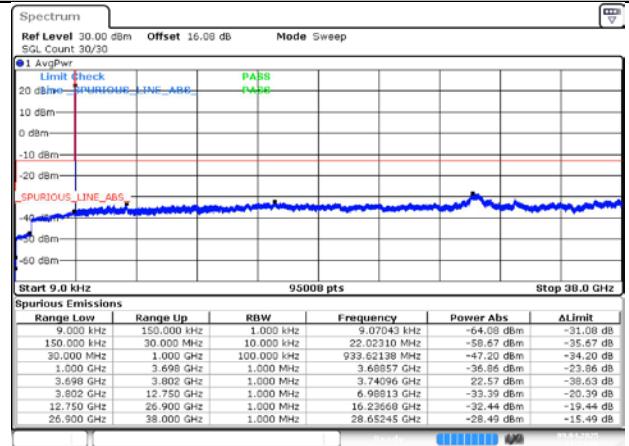
Date: 9 APR 2025 11:14:54

MCH / DFT-Pi2BPSK / Edge_1RB_Left



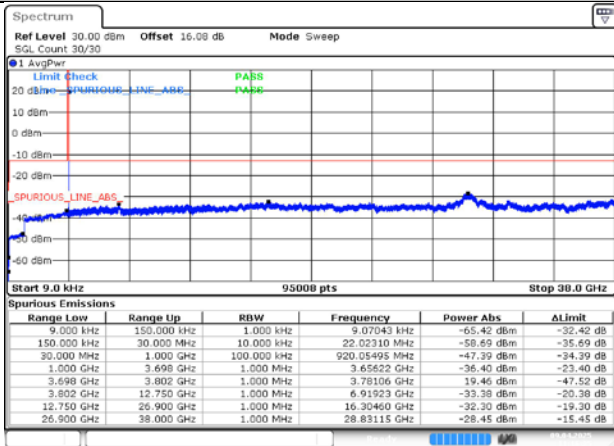
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MCH / DFT-QPSK / Edge_1RB_Left



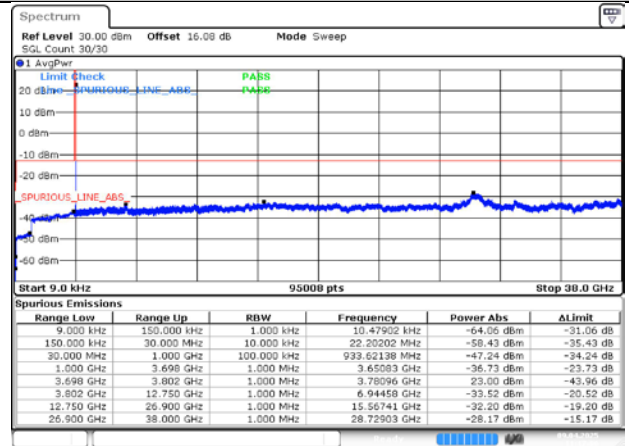
Date: 9 APR 2025 11:15:56

HCH / DFT-Pi2BPSK / Edge_1RB_Left



Date: 9 APR 2025 11:16:24

HCH / DFT-QPSK / Edge_1RB_Left



Date: 9 APR 2025 11:17:00

